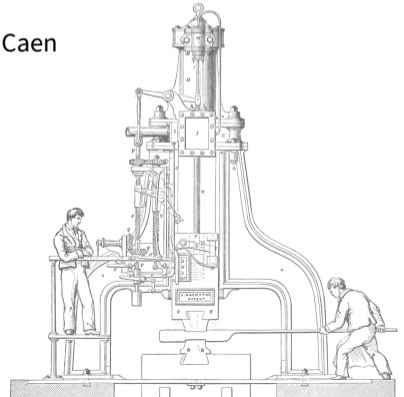


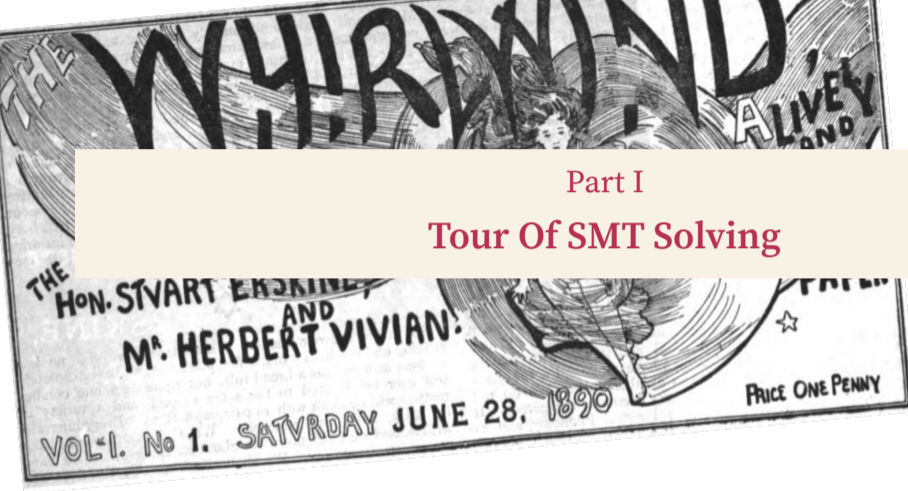
Understanding SMT Unsatisfiability Proofs

Hans-Jörg Schurr

CODAG Seminar – Université de Caen

14.01.2025





Part I

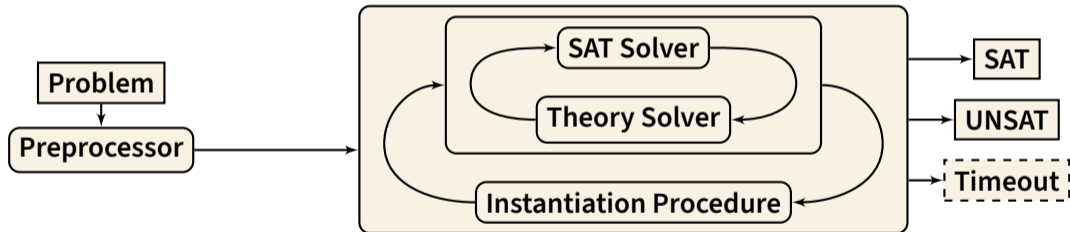
Tour Of SMT Solving

THE
HON. STUART ESKRINE
AND
M. HERBERT VIVIAN.

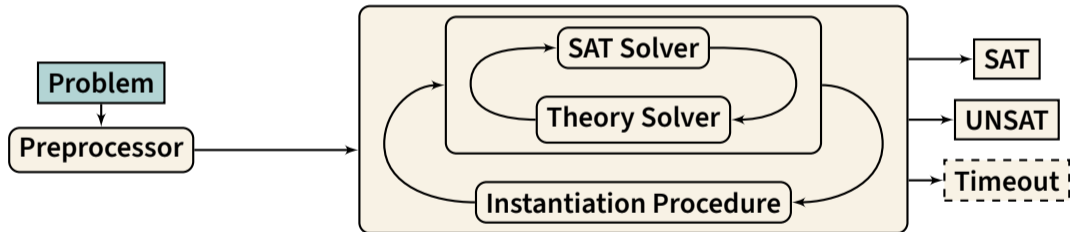
VOL. I. No 1. SATURDAY JUNE 28, 1890

PRICE ONE PENNY

SMT Solving As A Diagram



SMT Solving As A Diagram



An Example: Problem Specification

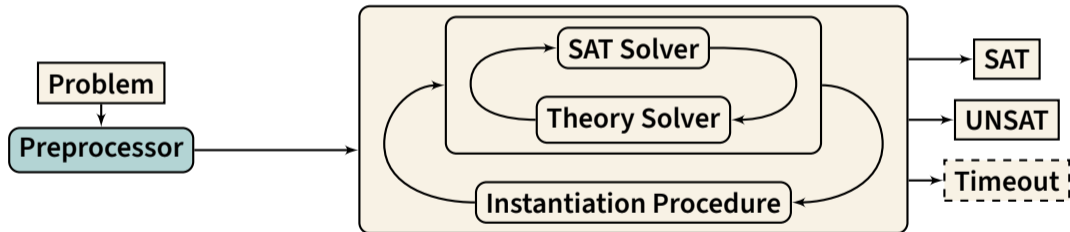
1. We produce 1L, 2L, and 3L bottles.
2. The price of a bottle is the volume plus four times the wall thickness (in mm).
3. The price must be less than 4\$.
4. If the new machine is broken, we cannot produce 3L bottles, and the wall thickness must be more than 1mm.
5. The new machine is broken.
6. For all bottle sizes, the wall thickness in millimetre can at most be the volume in liters.

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2. $v + 2t < p$
3. $p = 4$
4. $b \rightarrow (v \neq 3 \wedge t > 1)$
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SMT Solving As A Diagram



An Example: Preprocessing

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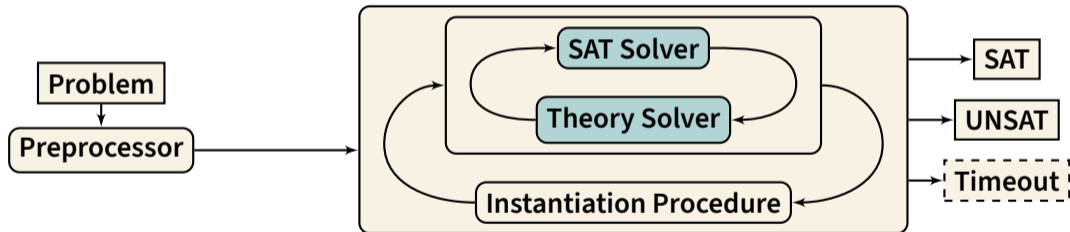
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SMT Solving As A Diagram



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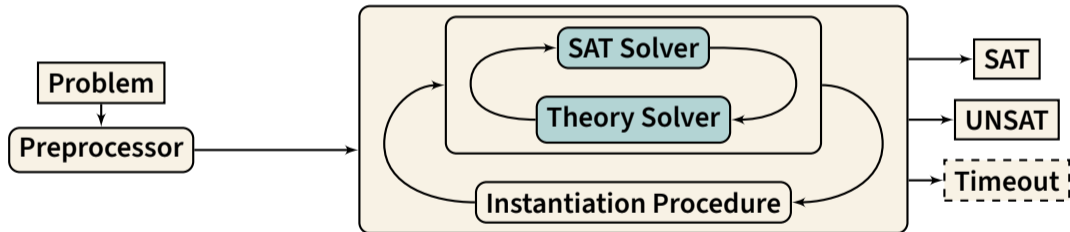
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SMT Solving As A Diagram



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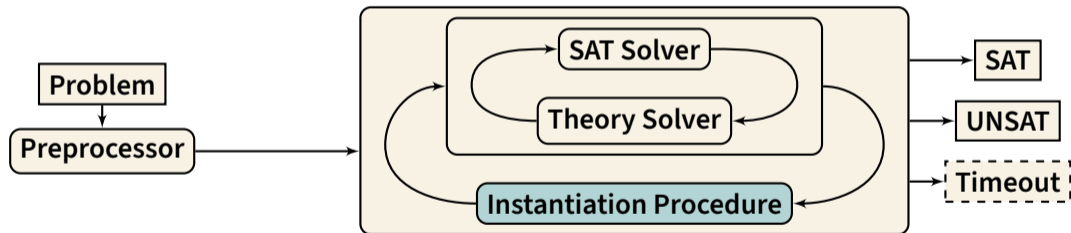
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I have to pick b, p_1, p_4 , and p_5 🙌

Linear Arithmetic Solver

1. I get $v = 1, v + 2t < 4$, and $t > 1$
2. That works! 🎉

SMT Solving As A Diagram



An Example: Quantifier Instantiation

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Instantiation Procedure

- I have $\forall z. \neg v = z \vee \neg z < t$

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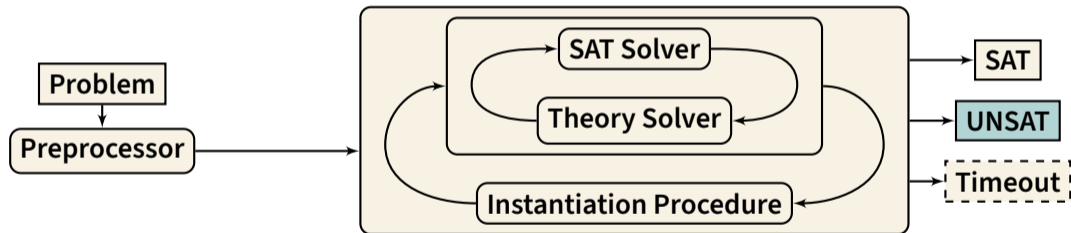
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- I have $\forall z. \neg v = z \vee \neg z < t$
- What happens if I pick $z \leftarrow 1$? 😈
- That's $\neg v = 1 \vee \neg t > 1$

SAT Solver

- That's $\neg p_1 \vee \neg p_5$
- Oh no 😞

SMT Solving As A Diagram



Using SMT-LIB

```
(set-logic LRA)
(declare-const v Real) (declare-const t Real)
(declare-const b Bool)
(assert (or (= v 1) (= v 2) (= v 3)))
(assert (< (+ v (* 2 t)) p))
(assert (= p 4))
(assert (=> b (and (not (= v 3)) (> t 1))))
(assert b)
(assert (forall ((z Real)) (=> (= v z) (<= t z))))
(check-sat)
```



Most SMT solvers support SMT-LIB



Theories: arithmetic, arrays, data-types, bit-vectors, strings, ...



Yearly competition (SMT-COMP)



Large benchmark library

Some Solvers You Can Try (a Biased List)

VeriT

- Small solver
- Excellent proofs, good quantifier support
- www.verit-solver.org

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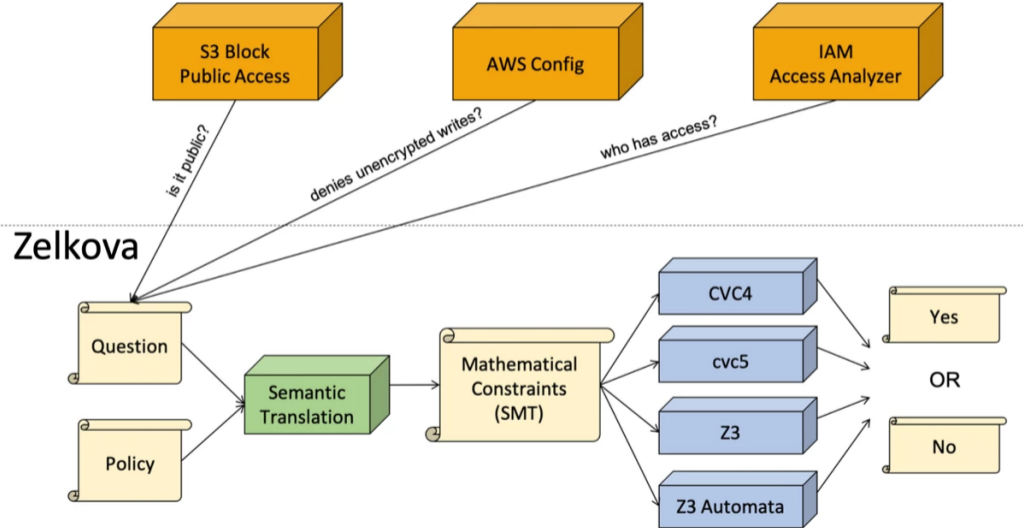
Z3

- Very established
- Also supports everything
- <https://github.com/Z3Prover/z3>

The background of the slide is a repeating pattern of stylized green leaves and stems on a light beige background. The leaves are elongated and pointed, with small veins visible. The stems are thin and branch out, creating a dense, naturalistic feel.

Part II
SMT Proofs

Example Application: Zelkova



Zelkova Style SMT Constraints

$Policy \Rightarrow Query$ is valid

$\neg(Policy \Rightarrow Query)$ is unsatisfiable

$Policy \wedge \neg Query$ is unsatisfiable

Zelkova Style SMT Constraints

$Policy \Rightarrow Query$ is valid

$\neg(Policy \Rightarrow Query)$ is unsatisfiable

$Policy \wedge \neg Query$ is unsatisfiable

- Query is against policy: satisfiable!
 - Evidence: counter**model**
 - Easy to check by evaluation.
- Query follows policy: unsatisfiable!
 - Evidence: refutation **proof**
 - Hard!

$$\frac{\frac{t_2}{t_3} \vdots t_1 \quad t_4}{t_1 \wedge t_4} \text{andI}$$
$$t_1, t_2 \vdash t_1 \wedge t_4$$

```
(assume a0 t1)
(assume a1 t2)
(step s1 t3
  :premises (a1)      :rule rule1)
...
(step s20 t4
  :premises (s19)     :rule rule2)
(step s21 (and t1 t4)
  :premises (a0 s20) :rule andI)
```

Proofs as Terms

- Proofs are terms of a dedicated **Proof** type.
- The **Proof** type depends on the formula it proves.

Example

```
(andI
  ((assume t1)
   (rule2 (...(rule1 ((assume t2))))...))
)
) : Proof (and t1 t4)
```

and introduction

```
(declare-rule andI ((F1 Bool) (F2 Bool))
  :premises (F1 F2)
  :conclusion (and F1 F2)
)
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)
```

Resolution

```
(program $resolve ((C1 Bool) (C2 Bool) (pol Bool) (L Bool))
  (Bool Bool Bool Bool) Bool
  (($resolve C1 C2 pol L)
    (eo::list_concat or ($nary_remove or false (eo::ite pol L (not L)) C1)
      ($nary_remove or false (eo::ite pol (not L) L) C2))
  )
)
(declare-rule resolution ((C1 Bool) (C2 Bool) (pol Bool) (L Bool))
  :premises (C1 C2) :args (pol L) :conclusion ($resolve C1 C2 pol L)
)
```

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- Programs can diverge.
- If there is no matching branch, they get stuck!

System Y: A Mechanized Formal Core Language For Eunoia

- Ongoing work!
- Mechanized in Agda, so we can trust the results.



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- Divergence is handled via guards
 - you must provide evidence a program evaluates in finitely many steps.



Thank You!



IOWA