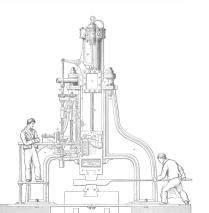
SMT Proof Certificates With AletheLF Fast, Flexible, and Familiar

Hans-Jörg Schurr CS Extras – Grinnell College March 7 2024









- Our tool: formal logic.
- It's unfeasible to write formal proofs by hand:
 Reliability mistakes happen easily
 Effort horribly time consuming

"Push Button" Usually refute problems and produce proofs.

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Satisfiability Modulo Theories

Propositional reasoning + theories.

- Functions
- Linear Arithmetic
- Quantifiers

Examples:

- cvc5
- veriT
- Z3

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Proof Assistants Reliability trusted kernel Effort proof construction routines Examples:

- Isabelle/HOL
- Coq
- Lean

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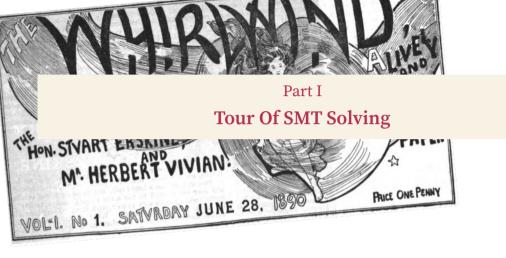
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Automation

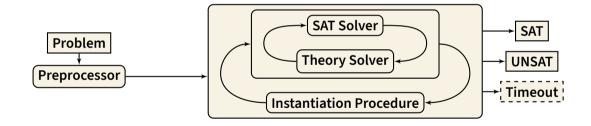
Must build upon the kernel.

- Simplifier: replaces equal by equal.
- Integration of automated theorem provers.

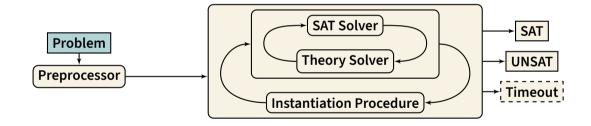
[•] Z3



SMT Solving As A Diagram



SMT Solving As A Diagram



- 1. We produce 1L, 2L, and 3L bottles.
- 2. The price of a bottle is the volume plus four times the wall thickness (in mm).
- 3. The price must be less than 4\$.
- If the new machine is broken, we cannot produce 3L bottles, and the wall thickness must be more than 1mm.
- 5. The new machine is broken.
- 6. For all bottle sizes, the wall thickness in millimetre can at most be the volume in liters.

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 or $v = 2$ or $v = 3$
2. $v + 2t < p$

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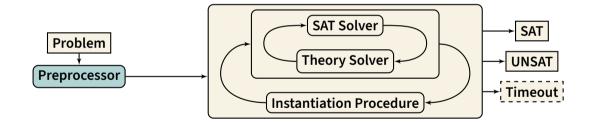
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$$v = z$$
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SMT Solving As A Diagram



1. v = 1 or v = 2 or v = 32. v + 2t < p3. p = 44. If *b* then: not v = 3 and t > 1

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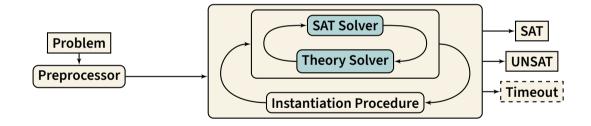
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SMT Solving As A Diagram



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An Example: The Ground Solver

- v = 1 or v = 2 or v = 3
- v + 2t < 4
- not b or not v = 3
- not b or t > 1
- b
- For all z: not v = z or not t > z

SAT Problem

- p_1 or p_2 or p_3
- p_4
- $\bullet \ \operatorname{not} b \operatorname{or} \operatorname{not} p_3$
- not b or p_5
- b

An Example: The Ground Solver

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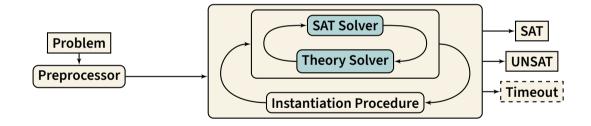
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Theory Literals

- $\bullet \ p_1 \text{ is } v=1 \text{, } p_2 \text{ is } v=2 \text{, } p_3 \text{ is } v=3$
- p_4 is v + 2t < 4
- $\bullet \ p_5 \text{ is } t>1$

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SAT Solver I pick b, p_2 , p_4 , and p_5 😌

SAT Problem

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- p_4
- not $b \operatorname{or} \operatorname{not} p_3$
- not b or p_5
- b

SAT Solver I pick b, p_2 , p_4 , and $p_5 \bigcirc$

Linear Arithmetic Solver

1. I get v = 2, v + 2t < 4, and t > 1

Theory Literals

- p_1 is v=1, p_2 is v=2, p_3 is v=3
- $\bullet \ p_4 \text{ is } v+2t<4$
- $\bullet \ p_5 \text{ is } t>1$

SAT Problem

- $\bullet \ p_1 \text{ or } p_2 \text{ or } p_3$
- p_4
- $\bullet \ \operatorname{not} b \operatorname{or} \operatorname{not} p_3$
- not b or p_5
- b
- not p_2 or not p_4 or not p_5

Theory Literals

- $\bullet \ p_1 \text{ is } v=1 \text{, } p_2 \text{ is } v=2 \text{, } p_3 \text{ is } v=3$
- $\bullet \ p_4 \text{ is } v+2t<4$
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SAT Solver I pick b, p_2 , p_4 , and $p_5 \textcircled{c}$



- 1. I get v = 2, v + 2t < 4, and t > 1
- 2. Doesn't work: not v = 2 or not v + 4t < 4 or not t > 1

SAT Problem

- $\bullet \ p_1 \text{ or } p_2 \text{ or } p_3$
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SAT Solver I have to pick b, p_1 , p_4 , and $p_5 ightarrow ightarrow in the image of the$

An Example: The SAT Solver and the Theory Solver

SAT Problem

- $\bullet \ p_1 \text{ or } p_2 \text{ or } p_3$
- p_4
- not $b \operatorname{or} \operatorname{not} p_3$
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Theory Literals

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Linear Arithmetic Solver

1. I get v = 1, v + 2t < 4, and t > 1

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- not p_2 or not p_4 or not p_5

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SAT Solver I pick b, p_2 , p_4 , and $p_5 \textcircled{c}$

Linear Arithmetic Solver

- 1. I get v = 2, v + 2t < 4, and t > 1
- 2. Doesn't work: not v = 2 or not v + 4t < 4 or not t > 1

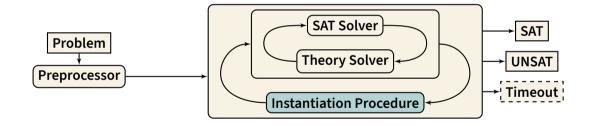
SAT Solver I have to pick b, p_1 , p_4 , and $p_5 ightarrow in the interval of the inter$

Linear Arithmetic Solver

1. I get v = 1, v + 2t < 4, and t > 1

2. That works! 🎉

SMT Solving As A Diagram



An Example: Quantifier Instantiation

SAT Problem

- $\bullet \ p_1 \text{ or } p_2 \text{ or } p_3$
- p_4
- not b or not p_3
- not b or p_5
- b

Instantiation Procedure

• I have For all z: not v = z or not t > z

Theory Literals

- p_1 is v=1, p_2 is v=2, p_3 is v=3
- $\bullet \ p_4 \text{ is } v+2t<4$
- $\bullet \ p_5 \text{ is } t>1$

SAT Problem

- $\bullet \ p_1 \text{ or } p_2 \text{ or } p_3$
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- not b or p_5

• b

Theory Literals

- p_1 is v=1, p_2 is v=2, p_3 is v=3
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Instantiation Procedure

- I have For all z: not v = z or not t > z
- What happens if I pick $z \leftarrow 1$? \overleftarrow{v}

SAT Problem

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Instantiation Procedure

- I have For all z: not v = z or not t > z
- What happens if I pick $z \leftarrow 1$? \overleftarrow{v}
- That's not v = 1 or not t > 1

Theory Literals

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SAT Problem

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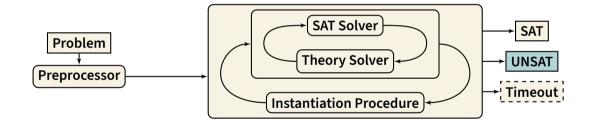
Instantiation Procedure

- I have For all z: not v = z or not t > z
- What happens if I pick $z \leftarrow 1$? \overleftarrow{v}
- That's not v = 1 or not t > 1

SAT Solver

- That's **not** p_1 **or not** p_5
- 🔹 Oh no 😢

SMT Solving As A Diagram



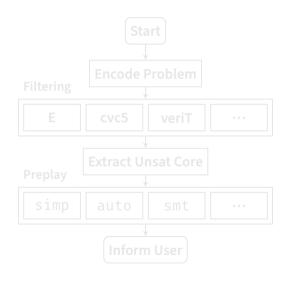
- 1. Since the new machine is broken, the volume cannot be 3L, and the wall thickness is > 1mm.
- 2. If the volume would be 2L, and the thickness is larger than 1L, then we get a contradiction with the price bound v + 2t < 4.
- 3. Since only 1L, 2L, and 3L bottles are produced, the volume must be 1L.
- 4. Because, the wall thickness must be smaller than the volume in liters, the wall thickness must be < 1mm.
- 5. This is a contradiction with the fact that we can only produce bottles with a wall thickness > 1mm.

Part II SMT Proofs in Use: Alethe in Isabelle/HOL



•••

lemma $f(x + 5) = f((1 \times 5) + x)$ 1. f(x + 5) = f(5 + x) by ×_unit 2. x + 5 = 5 + x by cong 3. x + 5 = x + 5 by +_com 4. \top by refl



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\operatorname{lemma} f(x+5) = f((1\times 5)+x)
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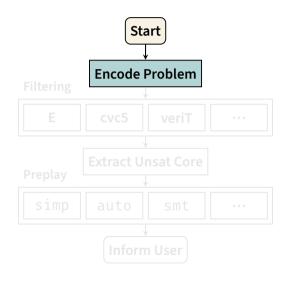
lemma
$$f(x + 5) = f((1 \times 5) + x)$$

Goal $L := f(x + 5) = f((1 \times 5) + x)$



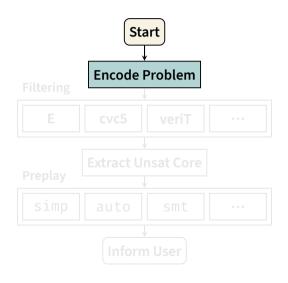
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$$\label{eq:generalized_lemma} \begin{split} & \operatorname{lemma} f(x+5) = f((1\times5)+x) \\ & \checkmark \\ & & \checkmark \\ & &$$



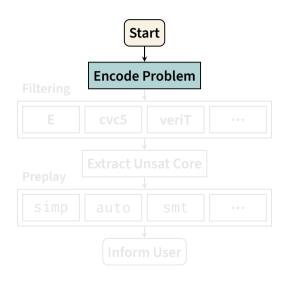
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 $\label{eq:generalized_states} \begin{array}{l} \mbox{lemma} f(x+5) = f((1\times5)+x) \\ \label{eq:generalized_states} \\ \mbox{Goal} \ L := f(x+5) = f((1\times5)+x) \\ \mbox{Add} \ B \ := \ \{ \mbox{-unit, } \mbox{-com, } \mbox{-assoc, } \\ \mbox{+_unit, } \mbox{-com, } \mbox{-assoc, } \\ \mbox{com, refl, } \mbox{...} \} \end{array}$

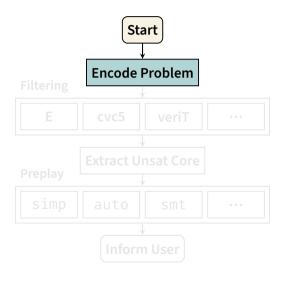


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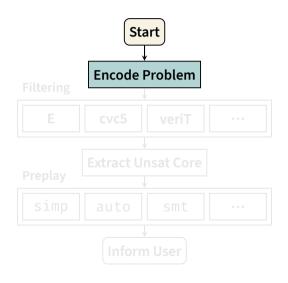


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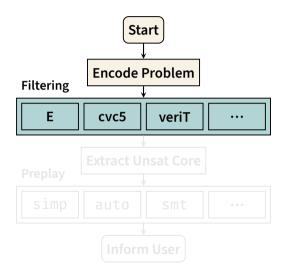
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$$\label{eq:generalized_lemma} \begin{split} & \operatorname{lemma} f(x+5) = f((1\times5)+x) \\ & \checkmark \\ & \checkmark \\ & \operatorname{Goal} L := f(x+5) = f((1\times5)+x) \\ & \operatorname{Add} B := \{ \texttt{\times_unit}, \texttt{\times_com}, \texttt{\times_assoc}, \ldots \} \\ & \operatorname{Encode} B \wedge \neg L \end{split}$$



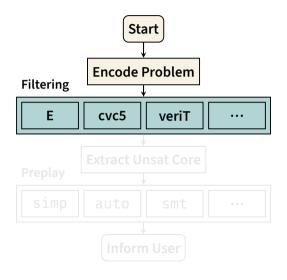
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lemma $f(x + 5) = f((1 \times 5) + x)$ Goal $L := f(x + 5) = f((1 \times 5) + x)$ Add $B := \{\times_unit, \times_com, \times_assoc, ...\}$ Encode $B \land \neg L$ Try E, cvc5, veriT, ...



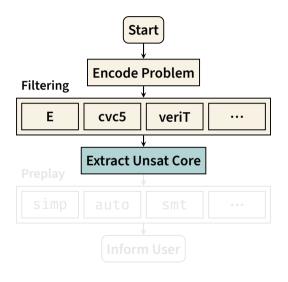
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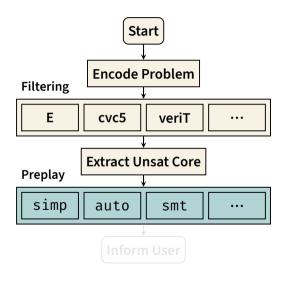
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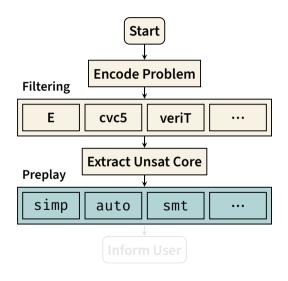
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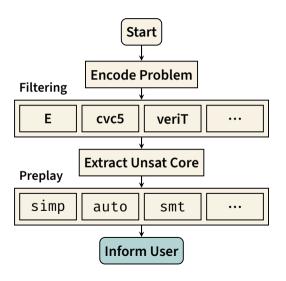
lemma $f(x + 5) = f((1 \times 5) + x)$ Goal $L := f(x+5) = f((1 \times 5) + x)$ Add $B := \{ \times \text{ unit}, \times \text{ com}, \times \text{ assoc}, ... \}$ Encode $B \wedge \neg L$ Try E, cvc5, veriT, ... veriT shows unsat! $Core C := \{ \times unit, + com, cong, refl \}$ Preplay simp, auto, smt on $C \land \neg L$, ... smt shows unsat!



•••

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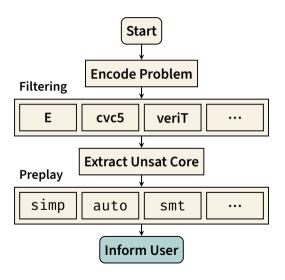
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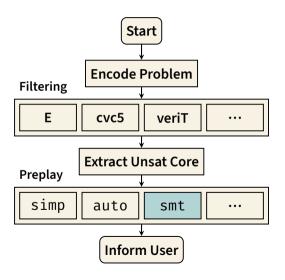
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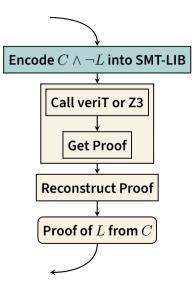


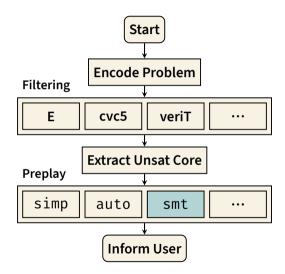
•••

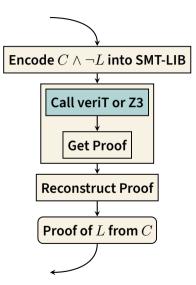
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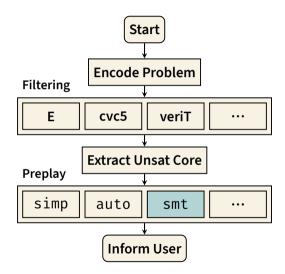
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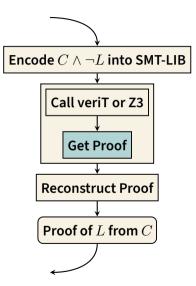


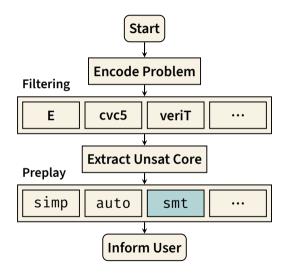


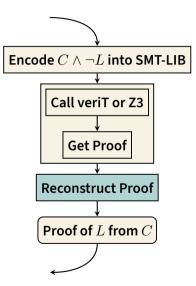


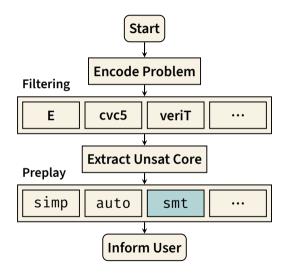


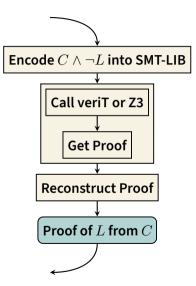


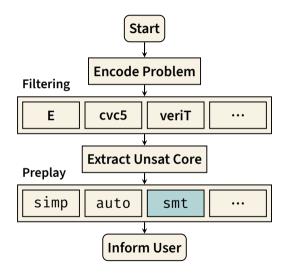












veriT Proofs

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SMT Proof Reconstruction Circa 2018

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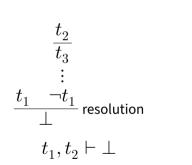
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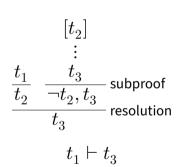
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Alethe Proofs: Basic Structure

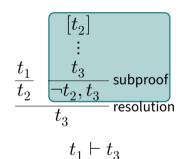


Alethe Proofs: Subproofs With Assumptions



```
(assume a0 t1)
(step s1 (cl t2)
        :premises (a0) :rule rule1)
(anchor :step s2)
    (assume s2.a1 t2)
    ...
    (step s2.s10 (cl t3)
        :premises (s2.s9) :rule rule2)
(step s2 (cl (not t2) t3) :rule subproof)
(step s3 (cl t3)
        :premises (s1 s2) :rule resolution)
```

Alethe Proofs: Subproofs With Assumptions



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$$\begin{array}{c} \hline x \mapsto y \triangleright x = y \\ \hline x \mapsto y \triangleright f(x) = f(y) \\ \hline \forall x. f(x) = \forall y. f(y) \\ \vdash \forall x. f(x) = \forall y. f(y) \end{array} \text{bind} \begin{array}{c} (anchor :step s2 :args ((:= (x S) y))) \\ (step s2.s1 (cl (= x y)) :rule refl) \\ (step s2.s2 (cl (= (f x) (f y))) \\ (step s2 (cl (= (forall ((x S)) (f x))) \\ (forall ((y S)) (f y))) \\ :rule bind) \end{array}$$

Improving Alethe for Reconstruction

Important Hurdles Solved

- Clear term simplifications.
- No implicit clause normalizations.
- Certificates for linear arithmetic.

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Other Improvements

- Complete documentation of the format.
- Rigorous handling of quantifiers.
 - No implicit clausification.
 - ∀-instantiation certificate: explicit substitution.
- Proper printing of number constants depending on theory.
- A better algorithm for proof pruning.
- Clever term sharing.

• ...

Clear Term Simplifications

Can we improve proofs of preprocessing?

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Proofs

Before a single rule combining all simplifications, undocumented

 $\vDash_T \Gamma \vartriangleright t = u$

Now 17 rules arranged by operators. **Documented** as rewrite rules. e.g. $x + 0 \rightarrow x$ in sum_simplify. Can we improve proofs of preprocessing?

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Reconstruction

Before automatic proof tactics are necessary, with tweaked timeouts. Now directed use of the simplifier parameterized with the rewrite rules.

No Implicit Clause Normalizations

Clauses in conclusions are sometimes simplified, why?

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Proofs

Before $\neg \neg \varphi$ implicitly simplified to φ in the proof module

Before clauses with complementary literals simplified to \top

Before repeated literals implicitly eliminated

Now patch every **proof step**, e.g, add step $\neg \neg \neg \varphi \lor \varphi$ and a resolution step

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Reconstruction

Before special case possible at every step! rule $(\mathbf{if} \, \varphi \, \mathbf{then} \, \psi_1 \, \mathbf{else} \, \psi_2) \Rightarrow \neg \varphi \lor \psi_1$ step $(\mathbf{if} \, \varphi \, \mathbf{then} \, \neg \varphi \, \mathbf{else} \, \psi_2) \Rightarrow \neg \varphi$ Now no pollution in rule reconstruction.

step $(\mathbf{if} \, \varphi \, \mathbf{then} \, \neg \varphi \, \mathbf{else} \, \psi_2) \Rightarrow \neg \varphi \lor \neg \varphi$

Certificates for Linear Arithmetic

Reconstruction fails on this LA tautology: $(2x < 3) = (x \le 1)$ over $\mathbb Z$ Why? Strengthening!

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Proofs

Before just a clause of inequalities, no certificate. Now strengthening documented.

$$(2x < 3) = (x \le 1)$$
 Strengthened: $(2x \le 2) = (x \le 1)$

Now certificate: coefficient. Here: $\frac{1}{2}$ and 1.

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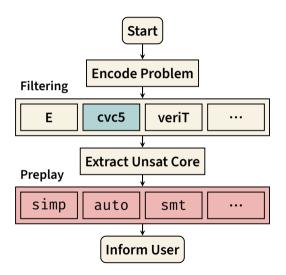
Reconstruction

Before certificate derived again.

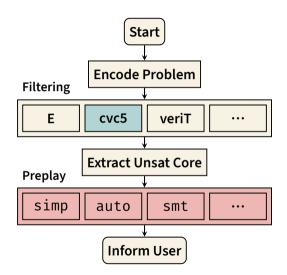
Now reconstruction amounts to calculations.

Now can abstract nested terms: $2 \times (\mathbf{if} \top \mathbf{then} \ 1 \ \mathbf{else} \ 0)$ treated as $2 \times x$.

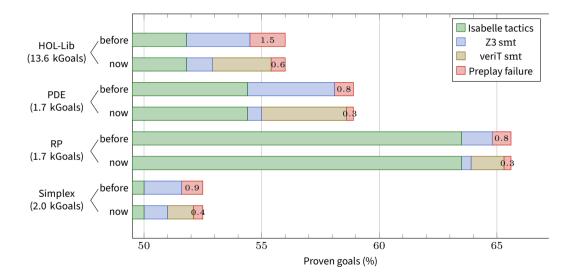
- 1. Pick an existing theory.
- 2. Try Sledgehammer on each obligation.



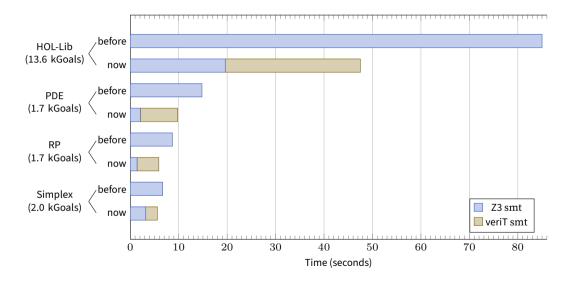
- 1. Pick an existing theory.
- 2. Try Sledgehammer on each obligation.
- Did Sledgehammer succeed?
- Which tactic did preplay suggest?
- Preplay failure: there is a proof, but it's not usable!
- Also: how long does the tactic run?



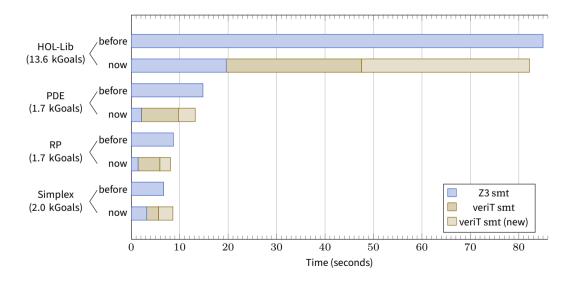
CVC4: Preplay Success Rate



CVC4: Preplay Time (smt only)



CVC4: Preplay Time (smt only)



Conclusion

Reconstruction

- 611 smt-veriT calls in AFP.
- Granular proofs matter.
- Proof size is critical.

SMT Proofs

- Danger of "Proof Rot."
- Proof checking can prevent this.
- The familiar SMT-LIB syntax reduced debugging pain.
- Solver design leaks to the format.

Part III

The Future: AletheLF

Staying in Sync

- Documentation
- Proof production
- Proof checking

Designed for SMT Solvers

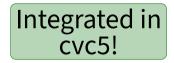
- Feels like using SMT-LIB
- Flexible enough to capture solver design details
- Fast!

Staying in Sync

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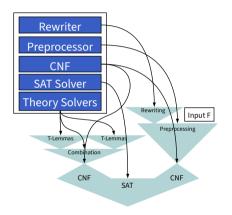
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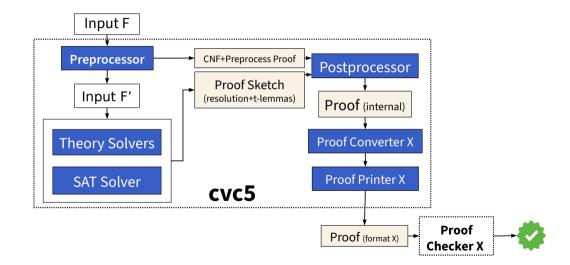
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• Internal proof calculus

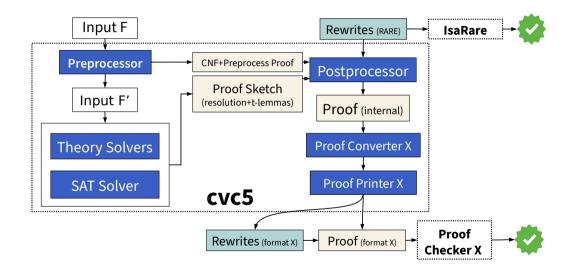
- Roughly 142 rules (132 core + 10 macro)
- Native proof checker in cvc5's core
- Original focus was on theory of strings
- Multiple backends
- Evaluated on many SMT-LIB theories [Barbosa, et al. 2022]





- Key element of preprocessing: rewriting
- A DSL to express rewrite rules
- Automatic elaboration during post-processing
- Large library of rewrites (strings, bitvectors, ...)
- Translation pipeline to Isabelle/HOL

```
(define-rule* str-concat-unify
 ((s1 String)
  (s2 String) (s3 String :list)
  (t2 String) (t3 String :list))
  (= (str.++ s1 s2 s3)
      (str.++ s1 t2 t3))
  (= (str.++ s2 s3)
      (str.++ t2 t3)))
```



A pen-and-paper standard: danger of proof rot

- Limited set of theories.
- No rule to proof testing pipeline.
- Does not capture cvc5's type system, proof calculus
 - incurs large post-processing cost

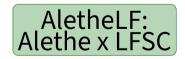
Why not LFSC?

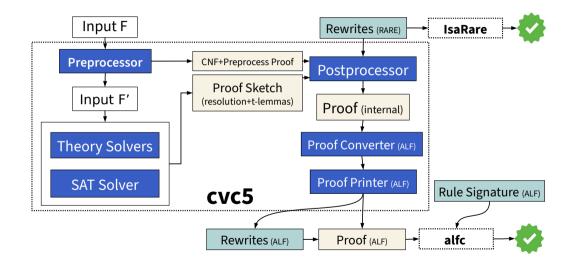
What is LFSC?

- Dedicated logical framework (LF) for SMT proofs. [Oe, et al. 2009], [Stump, et al. 2013], [Hadarean, et al. 2015], [Katz, et al. 2016]
- Based on Edinburg Logical Framework (LF) extended with side conditions.
- Allows user defined proof rules.
- Performance.
- Proof rules must encoded at a low level.
- Syntax for terms does not match SMT-LIB.
- Limited tooling support.

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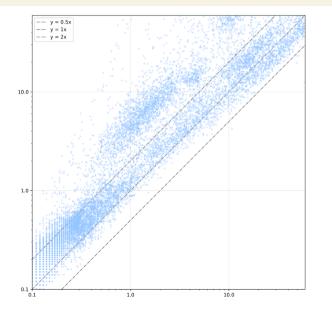


```
(declare-const = (-> (! Type :var T :implicit) T T Bool))
(declare-rule symm ((T Type) (t T) (s T))
    :premises ((= t s))
    :conclusion (= s t)
)
(declare-sort S 0)
(declare-const a S)
(declare-const b S)
(assume <code>@a0 (= a b))</code>
(step @s1 (= b a) :rule symm :premises (@a0))
```

Example: And-Elimination

```
(declare-const and (-> Bool Bool Bool))
(program select ((i Int) (l Bool) (r Bool))
        (Int Bool) Bool
                ((select 1 (and l r)) l)
                ((select 2 (and l r)) r)
(declare-rule and-elim ((l Bool) (r Bool) (i Int))
    :premises ((and l r))
    :args (i)
    :conclusion (select i (and l r))
)
(declare-const p Bool)
(declare-const g Bool)
(assume file (and p g))
(step @s1 g :rule and-elim :premises (@a0) :args (2))
```

Evaluation: AletheLF vs. LFSC



- 97348 benchmarks
- 60s timeout
- All quantifier-free SMT-LIB logics with
 - strings
 - linear arithmetic
 - uninterpreted functions
- alfc 1.56x faster
 - Due to flexibility in AletheLF (e.g. uses chain resolution)

Why do we care?

- Proof production in for SAT solvers has been successful:
 - DRAT is everywhere!
- cvc5 now uses configurable SAT solver
 - via the IPASIR-UP API (IPASIR with user propagators)
 - Notably, cvc5 supports CaDiCaL

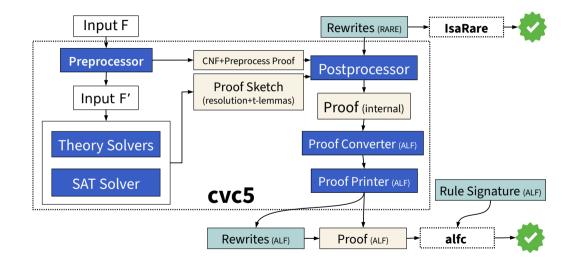
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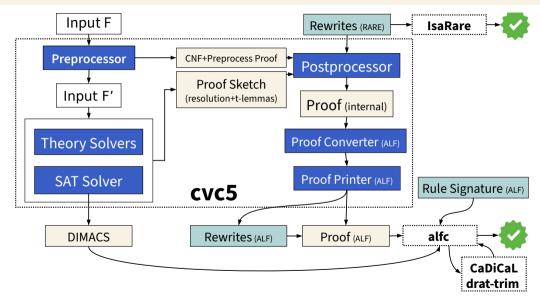
In AletheLF

- Integrate via oracles
- Use declare-oracle-fun to declare an interface with an external program.
- Communication using SMT-LIB syntax.

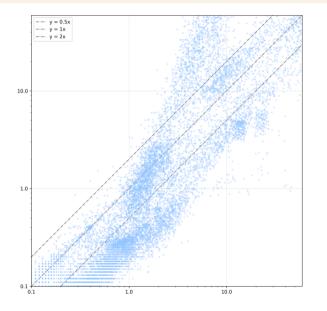
Proofs from cvc5 with SAT proofs



Proofs from cvc5 with SAT proofs

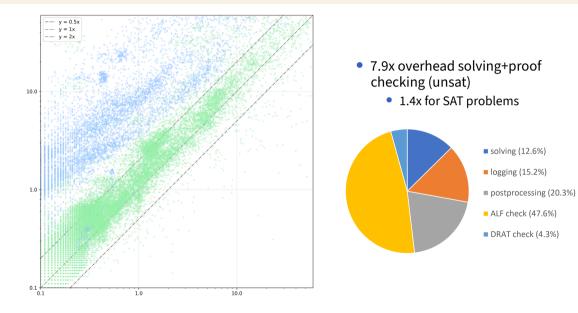


Evaluation: AletheLF with DRAT vs. resolution



- DRAT scales better than res on harder examples
 - DRAT 1.34x faster for benchmarks >5s

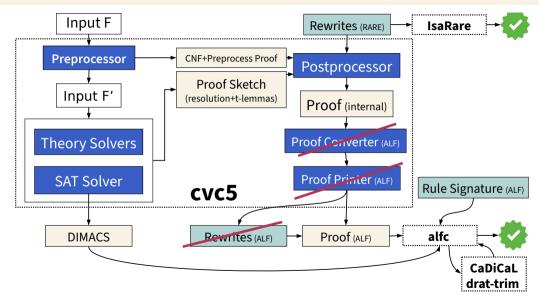
Evaluation: Proof Overhead



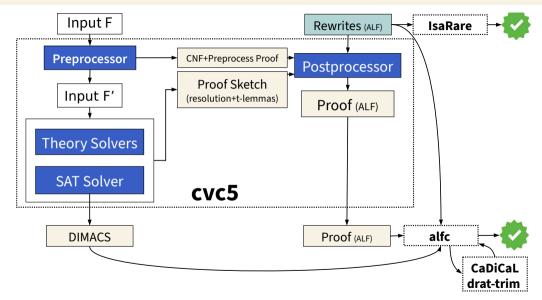
Ongoing Work

- Polish alfc for release.
- Use AletheLF as the internal format for cvc5 proofs.

AletheLF for internal proofs?



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Try it!

- cvc5: https://cvc5.github.io
 - use -- dump-proofs -- proof-format=alf
- alfc with documentation: https://github.com/cvc5/alfc
- Alethe in AletheLF: https://github.com/cvc5/AletheInAlf

Thank You!



