

# Preprocessing in Higher-order Reasoning

## Learning from QBF Solving

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Wien, 17. August 2017

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Diploma Thesis

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17. August 2017

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# Kurzfassung

In dieser Diplomarbeit präsentieren wir mehrere Algorithmen zur Vorverarbeitung von Formeln der höherstufigen Logik. Diese Logik erlaubt, neben der Quantifizierung über Individuen, auch die Quantifizierung über Funktionen und Relationen. Wir beschäftigen uns im speziellen mit dem Problem des automatischen Beweisens. Ein automatischer Beweiser für die Logik höherer Stufe ist das Leo-III System. Im Rahmen der vorliegenden Arbeit, wurde Leo-III um mehrere Algorithmen, welche auf Verfahren zur Vorverarbeitung von quantifizierte Boolesche Formeln basieren, erweitert. Quantifizierte Boolesche Formeln sind eine Erweiterung der Aussagenlogik und erlauben zusätzlich die Quantifizierung über Wahrheitswerte. Diese Formeln sind ein Fragment der höherstufigen Logik.

In den ersten zwei Kapiteln der vorliegenden Arbeit stellen wir diese zwei Logiken ausführlich vor. Viele moderne Systeme zum Lösen von quantifizierten Booleschen Formeln setzen auf einen Vorverarbeitungsschritt. Ein solcher Schritt produziert ein äquivalentes, aber potentiell einfacheres Problem. Wir untersuchten, ob es möglich ist einige dieser Algorithmen auf die Logik höherer Stufe zu adaptieren. Deshalb präsentieren wir zuerst einige dieser Techniken und stellen dann die folgenden vier Techniken für die Logik höherer Stufe vor:

*Universal reduction* erlaubt das Entfernen von Literalen aus Klauseln, wenn diese universell quantifiziert sind und in keinem anderen Literal der Klausel auftauchen. *Constant extraction* verwendet einen SAT Löser um Literale zu finden, welche notwendigerweise von der Formel impliziert sind, und fügt diese als neue Klausel zum Problem hinzu. *Blocked clause elimination* ermöglicht das Entfernen von Klausel wenn alle Resolventen bezüglich eines Literals tautologisch sind. Unsere Adaption verwendet Pattern-Literale und funktioniert mit Formeln ohne Gleichheit. *First-order re-encoding* verpackt die Literale eines Problems in eine spezielle Relation um die primitive Substitution zu verzögern.

Alle vier Techniken wurden für den Beweiser Leo-III implementiert. Weiters wurden zwei Werkzeuge programmiert, welche quantifizierte Boolesche Formeln in Formeln der höherstufigen Logik übersetzen. Leo-III konnte aus nur wenige dieser Probleme lösen. Die Algorithmen wurden deshalb mit Problemen aus der TPTP Bibliothek untersucht.

# Abstract

In this thesis we present some preprocessing techniques for theorem proving in higher-order logic which are based on preprocessing techniques for quantified Boolean formulas. Higher-Order Logic (HOL) is a language which extends the well-known first-order logic with support for quantification over predicates and functions. In particular, we are interested in the development of automatic theorem provers for this logic. Automatic theorem provers attempt to find proofs for HOL formulas without further user input. One such system is Leo-III.

Quantified Boolean Formulas (QBF), on the other hand, are an extension of propositional logic with explicit quantification over truth values. The validity of QBFs is, in contrast to HOL, decidable. Since HOL also supports quantification over truth values it is possible to translate every QBF into an equivalent HOL formula. We also discuss DQBF, an extension of QBF.

Most modern QBF solving systems apply a preprocessing step before they start solving the input problem. Often this is done by invoking an external preprocessing tool. The output of the preprocessing tool is a potentially easier, but equivalent problem. We investigated whether it is possible to adapt some of the techniques used by QBF preprocessing systems to HOL. Towards this end, we give an overview of common QBF preprocessing techniques and present four algorithms for HOL.

*Universal reduction* allows the removal of literals from clauses if they are universally quantified and appear in no other literal of the clause. *Constant extraction* uses a SAT solver to find literals necessary implied by the problem and adds them as a unit clause. *Blocked clause elimination* allows the removal of clauses if all resolvents on a literal are tautological. Our adaption uses pattern literals and works for problems without equality. Finally, *first-order re-encoding* wraps literals in a fresh proposition to delay primitive substitution.

All four preprocessing techniques were implemented in the Leo-III theorem prover. To evaluate the performance of Leo-III and Leo-III augmented with our techniques on QBF and DQBF problems, we developed two flexible tools which translate those problems into HOL. Unfortunately, Leo-III could only solve a few of these. Hence, we evaluate the impact of our preprocessing techniques on HOL problems instead.

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# 1 Introduction

The field of automatic reasoning is concerned with developing computer programs which perform formal logical reasoning without additional user input. Such reasoning might be the construction of proofs, checking proofs for their correctness, or searching for models of logical formulas.

This thesis connects two areas in automatic reasoning: Higher-order theorem proving and QBF solving. Higher-order logic is an expressive logic which allows quantification over functions and relations in addition to quantification over individuals. Quantified Boolean Formulas (QBF) are an extension of classical propositional formulas with quantification over Boolean values. The set of QBFs forms a fragment of higher-order formulas. Recent tools for solving QBFs rely heavily on preprocessing techniques (see section 3.2) and the question arises: Can some of those preprocessing techniques be lifted to higher-order theorem proving? This question is the starting point for this thesis.

The first two chapters introduces the background used in the subsequent chapters.

Since we aim to improve higher-order theorem proving, we first describe higher-order logic, including its syntax and semantics. Furthermore, we give an overview of software systems related to higher-order theorem proving. Our description will focus on Leo-III, the system which was extended by our preprocessing techniques.

Subsection 2.3.2 describes the architecture of the Leo-III prover and the used proof calculus. Then, subsection 2.3.3 describes the TPTP project. The TPTP infrastructure provides a standard format to represent logic problems and proofs. Furthermore, the TPTP library contains an extensive set of logical problems.

In chapter 3, we describe quantified Boolean formulas. Their syntax and semantics is much simpler than syntax and semantics of full higher-order logic. Afterwards the design of modern QBF solvers is discussed in section 3.2. We focus our discussion on systems which performed well in a recent QBF solving competition. Most of those systems relied on preprocessing.

Before giving an overview of preprocessing techniques used in QBF solving at the end of this chapter, we introduce Dependently Quantified Boolean Formulas (DQBF), a generalization of QBFs.

In the fourth chapter, we propose four preprocessing techniques for higher-order theorem proving. We start with lifting *universal reduction* to HOL. Universal reduction is an integral part of resolution for QBF and has a direct correspondence in higher-order logic.

Then we introduce a technique which uses a SAT solver to find constant literals. Constant literals are added to the input problem as new unit clauses without changing its satisfiability status. Furthermore, transitivity constraints induced by the equalities appearing in the input problem enrich the SAT problem.

The third proposed preprocessing technique is *blocked clause elimination*. This is a successful and important technique in QBF preprocessing. Lifting blocked clause elimination to higher-order logic is possible, but results in additional challenges. We present an adaptation of blocked clause elimination to higher-order logic without equality which utilizes the pattern fragment of higher-order logic.

Finally, we investigate *first-order re-encoding*. This technique is not directly inspired by a QBF preprocessing technique, but imitates the encoding of QBFs into first-order logic. By doing so, application of the primitive substitution rule can be delayed. This rule guesses instantiations and therefore induces non-determinism into the search procedure. We describe primitive substitution in section 2.3.1.

In chapter 5 we describe the implementation of the preprocessing techniques presented above. We start with an overview of the challenges encountered while implementing the preprocessing techniques. Then we discuss a set of tools which was developed to encode QBFs and DQBFs as higher-order problems in the TPTP format. Documentation for the tools can be found in section 5.2. Afterwards, the results of evaluating the implemented techniques are presented.

Finally, chapter 6 puts the work done in this thesis in a wider context by presenting related work and suggesting future work.

## 2 Higher-order Logic

In this section we introduce higher-order logic. We start with an informal discussion and then present its formal syntax and semantics. For the sake of brevity we will use the abbreviation HOL for higher-order logic from now on. In fact there are many higher-order logics and the abbreviation HOL will be used for a specific incarnation of Church’s type theory as discussed below.

Higher-order logic is a generalization of second-order logic, which itself is, not surprisingly, a generalization of first-order logic. The Stanford Encyclopedia of Philosophy introduces second-order logic in the following way:

Second-order logic is an extension of first-order logic where, in addition to quantifiers such as “for every object (in the universe of discourse),” one has quantifiers such as “for every *property* of objects (in the universe of discourse).” (Enderton 2015)

Second-order logic is a genuine extension of first-order logic. Consider, for example, an axiomatization of the natural numbers. One well-known property of the natural numbers is *well-ordering*: Every set of natural numbers has a smallest member. If  $P$  is a predicate symbol with one argument, then the first-order sentence

$$\exists x. Px \rightarrow \exists x. (Px \wedge \forall y. (Py \rightarrow (y = x \vee x < y)))$$

expresses the idea, that if  $P$  is true for at least one element, then there is an element for which  $P$  is true and all other elements, for which  $P$  is true, are bigger. Then, to express the well-ordering property, we need to speak about *all* properties (sets)  $P$ . This results in the second-order sentence

$$\forall P. (\exists x. Px \rightarrow \exists x. (Px \wedge \forall y. (Py \rightarrow (y = x \vee x < y))))).$$

In first-order logic, however it is always possible to find models of Peano arithmetic which are not isomorphic to the natural numbers. Those *non-standard models* are not well-ordered. Thus, it is not possible to express the well-ordering of the natural numbers as a first-order sentence (see Enderton 2015).

After allowing quantification over properties of individuals, it is just natural to allow quantification over properties of properties, over properties of properties of properties, and so on. If any arbitrary, but finite, nesting is allowed, the resulting logic is often called *type theory*. One can also think about continuing this into the transfinite. All those logics are called higher-order logics (see Enderton 2015).

A specific incarnation of type theory is Church's type theory, which is often also called *simple type theory*. Alonzo Church formulated this type theory in Church (1940). As the name suggests, type theory assigns types to the entities (individuals, functions, etc.) the language speaks about. By doing so Russell's paradox can be avoided, since the paradoxical formulas become untypeable. This means, that it is impossible to assign a type to the set of all sets that do not contain themselves. Church's type theory utilizes Church's  $\lambda$ -calculus to denote functions. Furthermore, since properties, sets, and relations can be expressed as functions from entities to Booleans the notion of functions is primitive in Church's type theory (see Andrews 2014).

Today, a wide variety of variants and extensions of Church's type theory exist. The variant of logic used by the Leo-III prover is also called *extensional type theory with choice*. For an overview of the development of HOL in the context of automatization see Benzmüller and Miller (2014).

For the purpose of this thesis we only consider classical higher-order logic and classical quantified Boolean formulas and omit their intuitionistic variants. One central difference between classical and intuitionistic logic is that classical logic admits the law of the excluded middle ( $A \vee \neg A$ ) while intuitionistic logic does not. An overview of intuitionistic logic with a focus on first-order logic, the related philosophical questions and further references can be found in Moschovakis (2015).

### 2.1 The Syntax

We now introduce the version of Church's type theory which is used by the Leo-III prover. When using the abbreviation HOL, we refer to exactly this logic. We follow the syntax definition and the notations given by Steen, Wisniewski, and Benzmüller (2016).

The *types* of HOL are terms freely generated by the binary *type constructor*  $\rightarrow$  and a finite set of base types  $T_0$ . This means that the base types in  $T_0$  are types and if  $\tau$  and  $\nu$  are types, then  $(\tau \rightarrow \nu)$  is a type too. The set  $T_0$  contains at least the two *primitive types*  $o$  and  $\iota$ . Intuitively the type  $o$  denotes Boolean values and  $\iota$  is the domain of individuals. Furthermore, a type  $(\tau \rightarrow \nu)$  is the type of

functions with domain type  $\tau$  and codomain type  $\nu$ .  $\mathcal{T}$  is the set of all types and the letters  $\tau$  and  $\nu$  denote arbitrary types. More formally:

**Definition 2.1.1** (Types and Primitive Types). The set  $\mathcal{T}_0 = \{o, \iota\}$  are the *primitive types*. Let  $\mathcal{T}_i = \{\tau \rightarrow \nu \mid \tau, \nu \in \mathcal{T}_{i-1}\}$ . Then

$$\mathcal{T} = \bigcup_{i \geq 0} \mathcal{T}_i$$

is the set of *types*.

To omit parenthesis, we assume that  $\rightarrow$  associates to the right. Hence,  $\alpha \rightarrow \beta \rightarrow \gamma$  denotes  $(\alpha \rightarrow (\beta \rightarrow \gamma))$ .

Now the *terms* of HOL can be defined. Let  $\Sigma$  be a *signature*: A countable infinite set of symbols, each annotated with a type. Those are the *constants*. If the constant  $c$  is in the signature and annotated with the type  $\tau$ , we say that  $c$  is of *type*  $\tau$  and write  $c_\tau \in \Sigma$ . All constants are *terms*. Furthermore, for each type  $\tau$  we have a countable infinite set of variables  $\mathcal{V}_\tau := \{x_\tau^1, x_\tau^2, \dots\}$ . All variables are terms too. From those primitive terms more complex terms can be constructed by using *abstraction* and *application*. Let  $S_\nu$  be any term, then  $(\lambda x_\tau^i. S_\nu)_{\tau \rightarrow \nu}$  is a term and if  $S_{\tau \rightarrow \nu}$  and  $T_\tau$  are terms, then  $(S_{\tau \rightarrow \nu} T_\tau)_\nu$  is a term too. This construction can be written in a more compact form as a grammar:

**Definition 2.1.2** (HOL Terms). The *terms* of HOL are generated by the following grammar rule:

$$S, T ::= c_\tau \mid x_\tau^i \mid (\lambda x_\tau^i. S_\nu)_{\tau \rightarrow \nu} \mid (S_{\tau \rightarrow \nu} T_\tau)_\nu$$

**Definition 2.1.3** (HOL Formulas). The *formulas* of HOL are the terms of type  $o$ .

Readers familiar with first-order logic might notice a subtlety here: The distinction between atomic formulas (formulas without logical symbols) and regular formulas disappears. This allows us to define the usual logical connective as merely constants.

For HOL we require the set of constants  $\Sigma$  to contain a complete logical signature. The connectives for disjunction, negation, and, for each type, equality and universal quantification already form a complete logical signature. Hence,  $\{\vee_{o \rightarrow o \rightarrow o}, \neg_{o \rightarrow o}\} \subset \Sigma$ . Furthermore,  $\Sigma$  contains an equality constant  $=_{\tau \rightarrow \tau \rightarrow o}^\tau$  and an universal quantification constant  $\Pi_{(\tau \rightarrow o) \rightarrow o}^\tau$  for each type  $\tau$ . Intuitively,  $\Pi_{(\tau \rightarrow o) \rightarrow o}^\tau p_{\tau \rightarrow o}$  is true if the predicate  $p_{\tau \rightarrow o}$  is true for every element in the type  $\tau$ .

## 2 Higher-order Logic

To declutter the syntax, we omit the type superscript. This results in the overloaded constants  $=_{\tau \rightarrow \tau \rightarrow o}$  and  $\Pi_{(\tau \rightarrow o) \rightarrow o}$  where  $\tau$  is arbitrary. We will also omit the types if they are clear from the context. In this text we will use the letters  $S$ ,  $T$ , and  $U$  as syntactic variables denoting arbitrary terms. If they are annotated with a type in the subscript (e.g.  $S_\tau$ ), they denote arbitrary terms of that type. Furthermore, we will use  $x_\tau, y_\tau, z_\tau$  as alternative variable names to avoid the index superscript.

Variables can occur in a term either bound or free. The sets of free and bound variables can be defined by a simple recursion:

**Definition 2.1.4** (Free Variables). Given a HOL term  $S$  the *free variables*  $\mathcal{F}_S$  of  $S$  are:

$$\begin{aligned} & \emptyset \text{ if } S = c_\tau \text{ and } c_\tau \in \Sigma \\ & \{x_\tau^i\} \text{ if } S = x_\tau^i \text{ and } x_\tau^i \in \mathcal{V}_\tau \\ & \mathcal{F}_T - x_\tau^i \text{ if } S = \lambda x_\tau^i. T \\ & \mathcal{F}_T \cup \mathcal{F}_U \text{ if } S = (TU)_\tau \end{aligned}$$

**Definition 2.1.5** (Bound Variables). Given a HOL term  $S$  the *bound variables*  $\mathcal{B}_S$  of  $S$  are:

$$\begin{aligned} & \emptyset \text{ if } S = c_\tau \text{ and } c_\tau \in \Sigma \\ & \emptyset \text{ if } S = x_\tau^i \text{ and } x_\tau^i \in \mathcal{V}_\tau \\ & \mathcal{B}_T \cup \{x_\tau^i\} \text{ if } S = \lambda x_\tau^i. T \\ & \mathcal{B}_T \cup \mathcal{B}_U \text{ if } S = (TU)_\tau \end{aligned}$$

Note that variables can occur both free and bound in a term. For example, the variable  $x_o^1$  occurs both free and bound in the formula  $((\lambda x_o^1. x_o^1 \vee x_o^2)_{o \rightarrow o} x_o^1)_o$ .

To avoid clustering the signature we define the other logical connectives as functions.

**Definition 2.1.6** (Conjunction, Implication, and Disequality). *Conjunction* is defined as  $(\wedge)_{o \rightarrow o \rightarrow o} := \lambda x_o. \lambda y_o. \neg((\neg x_o) \vee (\neg y_o))$  and *implication* is defined as  $(\rightarrow)_{o \rightarrow o \rightarrow o} := \lambda x_o. \lambda y_o. (\neg x_o) \vee y_o$ . *Inequality* is defined for arbitrary types  $\tau$  as  $(\neq)_{\tau \rightarrow \tau \rightarrow o} := \lambda x_\tau. \lambda y_\tau. \neg(x_\tau = y_\tau)$ .

We will write this operators in the usual infix notation. Let  $S_o$  and  $T_o$  be arbitrary formulas then we will write  $S_o \wedge T_o$ ,  $S_o \rightarrow T_o$ , and  $S_\tau \neq T_\tau$  for arbitrary terms  $S_\tau, T_\tau$  of arbitrary type  $\tau$ .

Furthermore, the universal quantification can be used to define the usual quantifiers syntactically.

**Definition 2.1.7** (For-All and Existential Quantifiers).

$$\begin{aligned}\forall x_\tau. S_o &:= \Pi(\lambda x_\tau. S_o) \\ \exists x_\tau. S_o &:= \neg(\Pi(\lambda x_\tau. \neg(S_o)))\end{aligned}$$

The connectives  $\lambda$ ,  $\forall$ , and  $\exists$  are the *binders*. To make the syntax clearer, we will allow lists of variables to appear in the head of the binders. Hence,  $\lambda x_o, y_o. (x_o \wedge y_o)$  is the same as  $\lambda x_o. \lambda y_o. (x_o \wedge y_o)$ . Furthermore, parenthesis can be omitted as usual. Note that the logical operators and application are left associative. This interacts nicely with the right associativity of the type constructor. The term  $(f_{\alpha \rightarrow \beta \rightarrow \gamma} a_\alpha b_\beta)_\gamma$  is equivalent to  $((f_{\alpha \rightarrow (\beta \rightarrow \gamma)} a_\alpha) b_\beta)_\gamma$ .

## 2.2 Two Semantics

Formulas by themselves are often not enough, one is generally also interested in their meaning. To this end we now give a short presentation of the semantic of Church's type theory.

This is not as straightforward as it is in the case of first-order logic. While the completeness of first-order logic was established in a seminal result by Gödel (1930). Incompleteness of second-order logic with standard semantic, however, is a direct consequence of Gödel's famous incompleteness theorem (Gödel 1931). To recover completeness a weaker notion of validity is needed. One such notion is the Henkin semantic (cf. Benzmüller and Miller 2014).

Both, standard and Henkin semantic, use similar concepts. We will introduce standard semantic as a stronger case of Henkin semantic. Again the definitions and notations given here follow Steen, Wisniewski, and Benzmüller (2016).

We start with the definition of a frame.

**Definition 2.2.1** (Domains and Frames). A *frame* is a collection  $\{\mathcal{D}_\tau\}_{\tau \in \mathcal{T}}$  of non-empty sets, such that  $\mathcal{D}_o = \{\mathbb{T}, \mathbb{F}\}$  and  $\mathcal{D}_{\tau \rightarrow \nu} \subseteq \mathcal{D}_\nu^{\mathcal{D}_\tau}$  is a collection of functions from  $\mathcal{D}_\tau$  to  $\mathcal{D}_\nu$ .

$\mathbb{T}$  and  $\mathbb{F}$  are the entities representing truth and falsehood.

This definition does not put any restrictions on the domain of  $\iota$ . If all the function domains contain all functions, then we call this frame a *standard frame*.

**Definition 2.2.2** (Standard Frame). A frame, where  $\mathcal{D}_{\tau \rightarrow \nu} = \mathcal{D}_\nu^{\mathcal{D}_\tau}$  for all types  $\tau$  and  $\nu$  is called a *standard frame*.

## 2 Higher-order Logic

As the name suggests, standard semantic only allows standard frames as possible interpretation, while Henkin semantic allow general frames. Before formalizing this notion we need to define the notion of interpretation and the valuation of a term.

**Definition 2.2.3** (Interpretation). An interpretation is a pair  $\mathcal{M} = (\{\mathcal{D}_\tau\}_{\tau \in \mathcal{T}}, \mathcal{I})$  where  $\{\mathcal{D}_\tau\}_{\tau \in \mathcal{T}}$  is a frame and  $\mathcal{I}$  is a function from the set of constants  $\Sigma$  to  $\bigcup_{\tau \in \mathcal{T}} \mathcal{D}_\tau$ , such that  $\mathcal{I}(c_\tau) \in \mathcal{D}_\tau$  for all  $\tau \in \Sigma$ .

A *variable assignment* is an arbitrary mapping  $\sigma : \bigcup_{\tau \in \mathcal{T}} \mathcal{V}_\tau \rightarrow \bigcup_{\tau \in \mathcal{T}} \mathcal{D}_\tau$ , such that for all  $x \in \mathcal{V}_\tau$ :  $\sigma(x) \in \mathcal{D}_\tau$ . We write  $\sigma[z/x_\tau]$  for the variable assignment which maps  $x_\tau$  to  $z$  and every other variable  $y_\tau$  to  $\sigma(y_\tau)$ .

**Definition 2.2.4** (Valuation). Given an interpretation  $\mathcal{M}$  and a variable assignment  $\sigma$ , the *valuation*  $\|\cdot\|^{\mathcal{M}, \sigma}$  of a term is recursively defined as:

$$\begin{aligned} \|c_\tau\|^{\mathcal{M}, \sigma} &= \mathcal{I}(c_\tau) \text{ if } c_\tau \in \Sigma \\ \|x_\tau\|^{\mathcal{M}, \sigma} &= \sigma(x_\tau) \text{ if } x_\tau \in \mathcal{V}_\tau \\ \|(S_{\tau \rightarrow \nu} T_\tau)\|^{\mathcal{M}, \sigma} &= \|S_{\tau \rightarrow \nu}\|^{\mathcal{M}, \sigma} (\|T_\tau\|^{\mathcal{M}, \sigma}) \\ \|\lambda x_\tau. S_\nu\|^{\mathcal{M}, \sigma} &= (f : z \mapsto \|S_\nu\|^{\mathcal{M}, \sigma[z/x_\tau]}) \in \mathcal{D}_{\tau \rightarrow \nu} \end{aligned}$$

From now on we assume that  $\mathcal{I}$  assigns the usual denotations to the primitive logical connectives.

**Definition 2.2.5** (Denotation of the Primitive Logical Connectives). The interpretation of the primitive constants is:

- $\mathcal{I}(\vee_{o \rightarrow o \rightarrow o})$  is the function  $(x, y) \mapsto \begin{cases} \mathbb{F}, & \text{if } x = \mathbb{F} \text{ and } y = \mathbb{F} \\ \mathbb{T}, & \text{otherwise.} \end{cases}$
- $\mathcal{I}(\neg_{o \rightarrow o})$  is the function which maps  $\mathbb{T}$  to  $\mathbb{F}$  and  $\mathbb{F}$  to  $\mathbb{T}$ .
- $\mathcal{I}(=_{\tau \rightarrow \tau \rightarrow o})$  is the function  $(x, y) \mapsto \begin{cases} \mathbb{T}, & \text{if } x = y \\ \mathbb{F}, & \text{otherwise.} \end{cases}$
- $\mathcal{I}(\Pi_{(\tau \rightarrow o) \rightarrow o})$  is the function  $f \mapsto \begin{cases} \mathbb{T}, & \text{if for all } x \in \mathcal{D}_\tau : f(x) = \mathbb{T} \\ \mathbb{F}, & \text{otherwise.} \end{cases}$

**Definition 2.2.6** (Standard Interpretation). A *standard interpretation* is an interpretation  $\mathcal{M} = (\{\mathcal{D}_\tau\}_{\tau \in \mathcal{T}}, \mathcal{I})$  where  $\{\mathcal{D}_\tau\}_{\tau \in \mathcal{T}}$  is a standard frame.

**Definition 2.2.7** (Henkin Interpretation). A *Henkin interpretation* is an interpretation  $\mathcal{M} = (\{\mathcal{D}_\tau\}_{\tau \in \mathcal{T}}, \mathcal{I})$  where  $\{\mathcal{D}_\tau\}_{\tau \in \mathcal{T}}$  is a frame such that  $\|\cdot\|^{\mathcal{M}, \sigma}$  is well defined (i.e. total) for all terms.

The totality of the valuation function has to be added as an additional constraint for Henkin interpretations, otherwise it is possible to have  $\lambda$ -terms whose corresponding function is not part of the domain.

A formula  $S_o$  is called *standard (Henkin) valid*, if and only if  $\|s_o\|^{\mathcal{M}, \sigma} = \mathbb{T}$  for every variable assignment  $\sigma$  and every standard (Henkin) interpretation  $\mathcal{M}$ . If for any given standard (Henkin) interpretation  $\|s_o\|^{\mathcal{M}, \sigma} = \mathbb{T}$ , we call this interpretation a *standard (Henkin) model*. Otherwise it is called a *standard (Henkin) counter model*. Note that every standard model is also a Henkin model. We say a set of formulas  $\{s_o^1, s_o^2, \dots, s_o^n\}$  is *valid*, if and only if every formula  $s_o^i$  is valid.

As mentioned before, a consequence of Gödel’s incompleteness theorem is the incompleteness of higher-order logic. This means, that there are *valid* formulas which are not provable in any consistent deduction system for higher-order logic. Henkin (1950) showed soundness and completeness for Henkin semantics<sup>1</sup> (cf. Benzmüller and Miller 2014). However, this comes at a cost. Higher-order logic with Henkin semantics is essentially many-sorted first-order logic (see Enderton 2015).

While HOL with Henkin semantics has essentially the same valid sentences as first-order logic, the convenient language is a justification for continued interest in it. Furthermore, Parikh (1973) has shown that there are exist theorems in arithmetic that have an arbitrary smaller proof in second-order logic than in first-order logic. In a similar spirit Boolos (1987) presented a theorem of first-order logic whose shortest proof is so large that it can not be conceived to be written down. The proof in second-order logic, on the other hand, fits comfortably onto a few pages.

While we give an overview on the deduction systems used in automatic theorem provers in the next section, more classical proof calculi might be of interest. A discussion of Frege–Hilbert style calculi can be found in Benzmüller and Miller (2014). Furthermore, it is worth mentioning, that the type expressions can be extended in various ways. One such way is to allow for free variables in type expressions, resulting in a polymorphic type theory. This is implemented in the Leo-III prover (see Steen, Wisniewski, and Benzmüller 2017).

<sup>1</sup> Henkin himself called Henkin models *general models*.

## 2.3 Automating Higher-order Logic

Given some assumptions  $s_o^1, s_o^2, \dots, s_o^n$  and a hypothesis  $s_o$ , can we find a deduction in some proof calculus that shows  $s_o$  from the assumptions? If the question can be answered affirmatively, we say that  $s_o$  is provable from  $s_o^1, s_o^2, \dots, s_o^n$  and write  $s_o^1, s_o^2, \dots, s_o^n \vdash s_o$ . The goal of an automatic theorem prover is to search for such a deduction without user input other than the problem. Unfortunately, HOL with Henkin semantic is only semi-decidable. Therefore, it is only possible to construct computer systems which can theoretically find a proof if one exists, but might not terminate if the goal is unprovable. In practice theorem proving systems may not be able to find proofs, even if they exist.

This section gives a rough overview of state-of-the-art automatic theorem proving. We start with an overview of currently used systems. Then subsection 2.3.1 will describe some aspects of the proof calculi commonly used in HOL provers. Subsection 2.3.2 describes the Leo-III prover in more detail. Finally, subsection 2.3.3 describes the TPTP infrastructure.

Automatic theorem proving is not the only way in which logical reasoning can be automatized. If a theorem is not provable, it might be possible to find a counter model. Nitpick by Blanchette and Nipkow (2010) is a tool which searches for finite counter models. Furthermore, *interactive theorem provers* construct proofs by interacting with the user. Examples of such systems are the Coq system (see Bertot and Castran 2010) and Isabelle/HOL (Nipkow, Paulson, and Wenzel 2002). The last system also incorporates Nitpick and the Sledgehammer (Paulson and Blanchette 2010) tool, which allows the user to use external, fully automated theorem provers for proof search.

The CADE ATP *System Competition* is an annual competition of automatic theorem proving systems. Its latest iteration was CASC-J8, which was part of The 8th International Joint Conference on Automated Reasoning in 2016<sup>2</sup>. In the higher-order theorems track four systems participated: Satallax (in two different configurations), LEO-II, Leo-III (in two different configurations), and Isabelle. While Leo-III is discussed in subsection 2.3.2, we will now give a short description of the other systems.

**Leo-II** is the successor of LEO-I by Benzmüller and Kohlhase (1998). The calculus used by LEO-II is called a RUE (resolution by unification and equality) calculus and equality with extensionality is supported natively. This removes the challenging extensionality axiom from the search space.

<sup>2</sup> see: <http://www.cs.miami.edu/~tptp/CASC/J8/>

Unification is implemented as a variation of Huet’s pre-unification. We discuss this algorithm in subsection 2.3.1. Furthermore, LEO-II supports cooperation with first-order provers (see Benzmüller, Paulson, et al. 2015).

**Satallax** uses a tableau calculus based approach in combination with a SAT solver. The reasoning process creates a series of potentially refutable formulas. Then propositional formulas corresponding to the meaning of those formulas are generated and handed to the SAT solver *minisat*. If the propositional formulas are unsatisfiable the original set of higher-order formulas is unsatisfiable (see Brown 2013).

**Isabelle** is an automatized version of the Isabelle/HOL system. While Isabelle/HOL is, as discussed above, foremostly an interactive system, it provides a wide set of automatic tools. While the individual tools are incomplete and will by themselves often fail to find a proof, a scheduler can be used to automatically try various configurations of the automatic tools<sup>3</sup>.

This list of competitors from the CASC-J8 competition is of course not a exhaustive list of available HOL automation tools. Furthermore, it blatantly disregards the historical development of HOL automation. For more general overview see Benzmüller and Miller (2014).

### 2.3.1 Aspects of Proof Calculi for HOL

In this and the next section, we will describe some aspects of proof calculi used by automatic theorem provers. While multiple complete calculi exist and are used for automatic proof search, many aspects discussed here are shared between systems.

#### LAMBDA TERMS

As outlined in section 2.1, HOL terms contain  $\lambda$ -expressions to syntactically represent functions. The proof calculus must be able to handle those expressions. The calculus of inference on  $\lambda$ -terms is the  $\lambda$ -calculus. For an introduction into the  $\lambda$ -calculus, and further references, see Alama (2016).

The first requirement to correctly handle  $\lambda$ -expressions is renaming of bound variables:

<sup>3</sup> For a description of the automatic mode see: <http://www.cs.miami.edu/~tptp/CASC/J8/SystemDescriptions.html#Isabelle---2015>

## 2 Higher-order Logic

$$\frac{\lambda x_\tau. S_\nu \quad y_\tau \in \mathcal{V}_\tau \text{ and } y_\tau \notin \mathcal{F}_S \cup \mathcal{B}_S}{\lambda y_\tau. S_\nu[y_\tau/x_\tau]} \mathcal{R}(\alpha\text{-conv.})$$

The restrictions on the  $\alpha$ -conversion rule ensure that the new variable  $y$  is fresh. This avoids erroneous renaming such as changing the term  $\lambda x. \lambda y. x$  to  $\lambda y. \lambda y. y$ . Usually terms which only differ in the name of bound variables are identified with each other:

**Definition 2.3.1** ( $\alpha$ -equivalence). A term  $s_\tau$  is  $\alpha$ -equivalent with a term  $t_\tau$ , if  $t_\tau$  can be constructed from  $s_\tau$  by multiple applications of the  $\alpha$ -conversion rule.

Explicit application of  $\alpha$ -conversion can be omitted by using a nameless representation of the variables in  $\lambda$ -terms such as de-Bruijn indices (see Wisniewski, Steen, and Benzmlle 2015).

A substitution  $\sigma$  is a function from variables to terms, which differs from the identity function only for finitely many variables. We write  $t/x$  for the substitution which replaces the variable  $x$  with the term  $t$ . A substitution can be extended to a function from terms to terms by applying them to the free variables occurring in the input terms. When doing so,  $\alpha$ -conversion must be applied to avoid capturing of free variables. E.g. when applying the substitution  $y/x$  to the term  $\lambda y. x$  one must first rename the bound variable  $y$  to get the  $\lambda z. y$ . For a given substitution  $\sigma$ , we written  $t[\sigma]$  for an  $\alpha$ -equivalent term of the term  $t$  after applying the substitution  $\sigma$ .

Substitutions can be composed. Let  $\sigma, \theta$  be two substitutions, then  $t[\theta][\sigma]$  denotes the resulting term after applying first  $\theta$  and then  $\sigma$  to  $t$ .

Secondly, the notion of applying an argument to a function needs to be expressed. This is done by the  $\beta$ -reduction rule.

$$\frac{(\lambda x_\tau. S_\nu)_{\tau \rightarrow \nu} T_\tau \quad \mathcal{F}_T \cap (\mathcal{F}_S \cup \mathcal{B}_S) = \emptyset}{S_\nu[T_\tau/x_\tau]} \mathcal{R}(\beta\text{-red.})$$

As for substitutions the rule is restricted such that accidental binding of free variables is avoided. Therefore, it might be necessary to apply  $\alpha$ -conversion before applying  $\beta$ -reduction.

The final rule is  $\eta$ -conversion, which expresses functional extensionality. Two functions are considered equivalent, if they map the all arguments to the same value. Formally, this is expressed by the following two rules:

$$\frac{\lambda x_\tau. f_{\tau \rightarrow \nu} x_\tau \quad x_\tau \notin \mathcal{F}_f}{f_{\tau \rightarrow \nu}} \mathcal{R}(\eta\text{-conv.})$$

$$\frac{f_{\tau \rightarrow \nu} \quad x_{\tau} \notin \mathcal{F}_f}{\lambda x_{\tau}. f_{\tau \rightarrow \nu} x_{\tau}} \mathcal{R}(\eta\text{-conv.}')$$

Again two terms, such that one term can be transformed into the other by multiple application of the two  $\eta$ -conversion rules are called  $\eta$ -equivalent.

## RESOLUTION

The fundamental idea behind the proof calculi used by LEO-II and Leo-III is *resolution*. While the Leo-III system extends and modifies this approach, as described in subsection 2.3.2, LEO-II is directly based on resolution.

Resolution was first introduced by Robinson (1965) for first-order logic and was subsequently lifted to higher-order logics. Andrews (1971) gave a proof of completeness for elementary type theory. Subsequently, Huet (1972) described a complete proof search method for elementary type theory. The approach taken by LEO-II is based on Resolution by Unification and Equality (RUE-resolution), which again was first developed for first-order logic and subsequently lifted by Benzmüller (1999) to HOL. A discussion of the evolution of higher-order resolution is presented in Benzmüller (2002) and Benzmüller (2015) gives a short description of the development and calculus used in LEO-II. The following presentation of resolution is based on the latter.

As a proof calculus, resolution proofs can only show that a contradiction can be derived from an input problem. To show the validity of conjecture it has to be negated first. The resolution rules operate on clauses and the proof is finished when the empty clause is found.

A clause is a disjunction of formulas. More exactly: A clause is a formula of the form  $s_o^1 \vee s_o^2 \vee \dots \vee s_o^n$ . A set of the form  $\{s_o^1, s_o^2, \dots, s_o^n\}$  is often used instead of the explicit formula. This also removes the necessity to handle removal of repeated formulas and the commutativity of the conjunction explicitly. For a given clause  $C$ , we will write  $C \vee t_o$  for  $C \cup \{t_o\}$  and  $C \vee D$  for  $C \cup D$ . From the definition of disjunction, we know that in any given interpretation and variable assignment a clause is true if at least one of its members is true. Hence, any valuation of the empty clause will always be false. A set of *clauses*, on the other hand, is a conjunction of clauses. Such a set is true, if and only if all of its member clauses are true. Therefore, the empty conjunction is always true.

Furthermore, we will add the *polarity* of a formula as an annotation:  $[s_o]^\alpha$  for  $\alpha \in \{tt, ff\}$ .  $[s = t]^{tt}$  is equivalent to  $s = t$ ,  $[s = t]^{ff}$  is equivalent to  $s \neq t$ ,  $[s]^{tt}$  is equivalent to  $s$ , and  $[s]^{ff}$  is equivalent to  $\neg s$ .

The proof search procedure maintains a set of clauses and iteratively applies the calculus rules which will produce new clauses to add to the clause set. If the

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empty clause is found, a proof for the unsatisfiability of the input problem has been found. Therefore, to prove the validity of  $s_o^1, s_o^2, \dots, s_o^n \vdash s_o$ , the prover starts with the clause set  $\{[s_o^1]^{tt}, [s_o^2]^{tt}, \dots, [s_o^n]^{tt}, [s_o]^{ff}\}$ .

The resolution rule is:

$$\frac{C \vee [s]^{tt} \quad D \vee [t]^{ff}}{C \vee D \vee [s = t]^{ff}} \mathcal{R}(Res)$$

That is: If a clause of the form  $C \vee [s]^{tt}$  and one of the form  $D \vee [t]^{ff}$  is found in the clause set, the clause  $C \vee D \vee [s = t]^{ff}$  can be added. The new term  $[s = t]^{ff}$  is called the *unification constraint*. How the prover operates on unification constraints is described in the next section.

To understand the motivation for this rule it is helpful to sketch an argument for its soundness. To show soundness, it is necessary to argue, that if the preconditions are true (under a given interpretation and variable assignment) the conclusion is true too. If the preconditions are true, then either  $C$  or  $[s]^{tt}$  and either  $D$  or  $[t]^{ff}$  are true. If  $s$  and  $t$  are not equal, then the conclusion holds. On the other hand, if they are equal then either  $[s]^{tt}$  or  $[t]^{ff}$  is false, and therefore either  $C$  or  $D$  is true.

Note that the resolution rule together with unification is not a complete calculus. See Benzmüller (2015) for a presentation of the full calculus used in LEO-II.

### UNIFICATION

As mentioned above, resolution introduces unification constraints which need to be handled by the prover. The inclusion of explicit unification constraints is a result of the pre-unification approach which is required to handle unification in HOL.

Knight (1989) gives the following general definition for the unification problem:

Given two terms of logic built up from function symbols, variables, and constants, is there a substitution of terms for variables that will make the two terms identical?

In the case of HOL this definition means, that we are interested in replacing the free variables of HOL terms with new HOL terms, such that two given terms become equal. Such a substitution is called an unifier:

**Definition 2.3.2 (Unifier).** Given two terms  $s$  and  $t$ , a substitution  $\sigma$  is a unifier of  $s$  and  $t$  if  $s[\sigma] = t[\sigma]$ .

In the case of first-order logic the existence of a unifier is always decidable and there is exactly one *most general unifier* (MGU). A unifier  $\sigma$  is a MGU of  $s$  and  $t$ , if for every other unifier  $\theta$  there is a substitution  $\tau$  such that  $\tau(\sigma(x)) = \theta(x)$  for all variables  $x$ . In fact, there are linear time algorithms to find the most general unifier of two first-order terms, even though they might not be the ones used in practice (see Knight 1989).

The following rule describes a possible application of unification during proof search:

$$\frac{\{u_o^1, u_o^2, \dots, u_o^n\} \vee [s = t]^{\text{ff}} \quad \sigma \text{ is the MGU of } s \text{ and } t}{\{u_o^1[\sigma], u_o^2[\sigma], \dots, u_o^n[\sigma]\}}$$

Intuitively, this rule expresses the fact, that if we can find a substitution  $\sigma$  of the free variables in a clause, such that  $s[\sigma] = t[\sigma]$ , then the unification constraint  $[s[\sigma] = t[\sigma]]^{\text{ff}}$  is certainly false and can be removed from the clause.

Unfortunately, in the case of higher-order logic finding a unifier is not decidable anymore and there might be infinitely many MGUs. Therefore, the rule we just described can not be used. To solve this, Huet (1975) presented the *pre-unification* algorithm to tightly incorporate higher-order unification into the proof search procedure. Unification problems are no longer solved eagerly, but first added as unification constraints to the clauses and then solved by appropriate calculus rules. LEO-II and Leo-III implement a variation of pre-unification extended by extensionality principles. For an overview of the unification rules used in LEO-II see again Benzmüller (2015).

While we know that unification for first-order terms is decidable, we might be interested in further fragments of HOL where unification is decidable. Such a fragment is the pattern fragment. Nipkow (1993) developed a complete unification algorithm for the pattern fragment and presented an implementation in a functional programming language. The definition of patterns used there is:

**Definition 2.3.3** (Pattern). A term  $t$  in  $\beta$ -normal form is a (*higher-order*) *pattern* if every free occurrence of a variable  $F$  is in a subterm  $F u_1 \dots u_n$  of  $t$  such that  $u_1 \dots u_n$  is  $\eta$ -equivalent to a list of distinct bound variables.

For example, the term  $\lambda x. \lambda y. F x y$  is a pattern, while  $\lambda x. F x x$  is a non-pattern (cf. Nipkow 1993).

Since first-order terms do not contain free function or predicate symbols they all are patterns and therefore pattern unification subsumes first-order unification. Pattern unification has been implemented in Leo-III and we use it to implement blocked clause elimination in section 4.4.

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### PRIMITIVE SUBSTITUTION

The primitive substitution rule is used to guess instantiations for predicates in provers such as Leo-III. Similar rules were proposed by Huet (1972) as *splitting* and as primitive substitution by Andrews (1989).

Given a literal of the form  $Q_{\tau \rightarrow o} S$  which appears in some clause of the HOL problem at hand, then primitive substitution will blindly guess the structure of  $Q$  from the operators  $\neg$ ,  $\vee$ ,  $\Pi$ , and  $=$ .

Hence, primitive substitution as realized by Leo-II and Leo-III is restricted to the outermost symbol. Only iterated application of the rule can reach deeper into the term structure.

During prove search, primitive substitution must be applied to ensure completeness and often plays the role of a fallback, when resolution is not applicable anymore. The classical example to illustrate the need for primitive substitution is the term  $\exists x_o. x$  which is first processed to the singular unit clause  $[x_o]^{ff}$ . There is no resolution partner for this clause and the empty clause can not be derived from this clause alone. Primitive substitution, however, might guess the structure of  $x_o$  to be  $\neg y_o$  introducing the clause  $[\neg y_o]^{ff}$  into the problem which normalizes to  $[y_o]^{tt}$ . Resolution easily derives the empty clause from  $[x_o]^{ff}$  and  $[y_o]^{tt}$  (cf. Benzmüller 2015).

### CLAUSIFICATION

As mentioned above, resolution works on a set of clauses. Therefore, automatic theorem provers attempt to first transform the input problem such that it is a conjunction of disjunctions.

Problems in first-order logic and QBFS can be transformed into a conjunctive normal form where the only occurring logical connectives are in the topmost conjunction of disjunctions. For the case of QBFS this is discussed in section 3.1.

Clausification for HOL formulas is complicated by the free nesting of functions and operators. Logical connectives and even quantifiers can occur in arguments to functions and inside  $\lambda$ -terms. Nevertheless, various methods to aid the clausification of HOL terms exist. See Wisniewski, Steen, Kern, et al. (2016) for an overview.

When we say a HOL problem is in *clause form*, we mean that some processing has been done to the problem towards creating a conjunction of clauses.

## SKOLEMIZATION

Skolemization is part of the pre-processing step and is applied directly after clausification. Clausification will create a problem of the form

$$Q_1 x_{\tau_1}^1 \cdot Q_2 x_{\tau_2}^2 \cdot \dots \cdot Q_i x_{\tau_i}^i \cdot C_1 \wedge C_2 \wedge \dots \wedge C_j$$

where the  $Q_n \in \{\forall, \exists\}$  are quantifiers and the  $C_n$  are clauses.

Skolemization will replace the existentially quantified variables with function constants. That is, if the quantifiers contain  $\exists x_i \dots$  each occurrence of  $x_i$  will be replaced by  $(sk_i x_{\pi_1} \dots x_{\pi_j})$  where  $sk_i$  is of appropriate type and the  $x_{\pi_i}$  are the variables where  $Q_{\pi_i} = \forall$  and  $\pi_i \leq i$ . The freshly introduced functions  $sk_i$  are called *Skolem functions*.

Consider for example the term  $\forall x_\tau. \exists y_\delta. P x y$ , then skolemization will result in the term  $\forall x_\tau. P x (sk_{\tau \rightarrow \delta} x)$ .

In the case of HOL, skolemization requires either the inclusion of the axiom of choice, or it must be restricted in its application to be sound (cf. Benzmüller and Miller 2014). The automatic theorem provers LEO-II and Leo-III can use skolemization without problems, since they support choice implicitly.

We will encounter Skolem functions in subsection 3.3 to define the semantics of DQBFS and their translation to HOL.

## 2.3.2 The Leo-III Prover

Leo-III is the successor of the LEO-II theorem prover. Development started in 2014 and is mostly conducted in the Scala programming language. This is a departure from the OCAML language used to develop LEO-II. Besides improving upon the proof search by employing ordered para-modulation/superposition and agent based parallelism, Leo-III also supports some new features. One goal is to extend the type system by including support for type classes and type constructors similar to System  $F^\omega$ . At the time of writing, type polymorphism was already implemented. Furthermore, embedding non-classical logics, such as modal logic, into HOL has been developed as a successful strategy to automate modal logic (see Benzmüller and Woltzenlogel Paleo 2015). Leo-III will feature native support for this (cf. Wisniewski, Steen, and Benzmüller 2014).

We will now describe three aspects of Leo-III, which are relevant for the preprocessing techniques presented in chapter 4. Since the preprocessing techniques are implemented as a part of the overall Leo-III system, we will discuss the software architecture and the agent based approach.

Leo-III is build upon the system platform *LEOPARD* (Wisniewski, Steen, and Benzmüller 2015), which provides data structures for term representation, in-

dexing and search. Furthermore, *LEOPARD* provides the general blackboard architecture used by the agent based parallelization. While *LEOPARD* was published independently of Leo-III, recent iterations of the platform have been developed as part of Leo-III. LEO-II, the predecessor of Leo-III, utilized a main loop which loops over a set of clauses while generating new clauses which do not change the satisfiability status of the problem and subsequently adds the new clauses to the set of clauses. In Leo-III, however, the reasoning tasks are separated into specialized agents. The agents autonomously decide when to execute their work. To find a proof, the agents utilize a blackboard onto which they can write, or delete content (e.g. clauses) from. Leo-III also provides interfaces to external systems, such as LEO-II, or the first order prover  $\mathbb{E}$  (cf. Steen, Wisniewski, and Benzmüller 2016). Furthermore, as part of this thesis project an interface to the SAT solver *PICOSAT* was developed (see subsection 5.1.1).

The calculus of Leo-III is – similar to classical resolution – refutation-based. Instead of trying to prove  $s_o^1, s_o^2, \dots, s_o^n \vdash x_o$  directly,  $x_o$  is negated and the prover tries to show that the set  $\{s_o^1, s_o^2, \dots, s_o^n, \neg x_o\}$  is inconsistent. Provers do this by adding new clauses until the empty clause is found. The para-modulation calculus is a refutation-based calculus, which is successfully applied by first-order provers. This approach extends naïve resolution with an appropriate handling of equality, and uses term orders to avoid redundancy in the generated clauses. Lifting this approach to HOL is complicated by the difficult nature of higher-order unification. For a sketch of para-modulation as it is applied in Leo-III and further references see Steen, Wisniewski, and Benzmüller (2016).

### 2.3.3 The TPTP Infrastructure

To test automatic theorem provers, one needs to collect a diverse library of HOL problem. The TPTP (Thousands of Problems for Theorem Provers) project provides such a library<sup>4</sup>. While the TPTP library was first designed for first order provers, it has been extended to HOL, HOL with polymorphic types, and various other logics (cf. Sutcliffe and Benzmüller 2010).

The TPTP language is used to represent input problems. The grammar of this language is designed such that it can be read directly as a Prolog term. Consider as an example the surjective Cantor’s theorem:

$$\neg(\exists G_{\iota \rightarrow (\iota \rightarrow o)}. \forall F_{\iota \rightarrow o}. \exists x_{\iota}. (G x) = F)$$

<sup>4</sup> The TPTP library is available at: <http://www.cs.miami.edu/~tptp/>

This theorem expresses the fact, that for some domains (i.e. infinite domains) there is no surjective function  $G$  from the set of individuals to set of sets of individuals. Note that in this case sets are expressed by functions of type  $\iota \rightarrow o$  (predicates).

Listing 1 is the TPTP encoding of this problem. It can be found in the TPTP library under the name SET557<sup>1</sup>.p<sup>5</sup>. Some comment lines have been omitted from the problem for the sake of brevity.

```

1 %-----
2 % File      : SET557^1 : TPTP v6.4.0. Released v3.6.0.
3 % Domain    : Set Theory
4 % Problem   : Cantor's theorem
5 % Version   : Especial.
6 % [...]
7 % Status    : Theorem
8 % Rating    : 0.29 v6.4.0, 0.33 v6.3.0, 0.40 v6.2.0, 0.43
   → v5.5.0, 0.50 v5.4.0, 0.20 v5.3.0, 0.40 v5.2.0, 0.20 v4.1.0,
   → 0.00 v4.0.1, 0.67 v4.0.0, 0.33 v3.7.0
9 % [...]
10 %-----
11 thf(surjectiveCantorThm,conjecture,(
12   ~ ( ? [G: $i > $i > $o] :
13     ! [F: $i > $o] :
14     ? [X: $i] :
15       ( ( G @ X )
16         = F ) ) )).
17
18 %-----

```

Listing 1: Cantor's theorem as TPTP problem.

The `thf()` function is used to describe a HOL formula in the TPTP language. It takes as first argument a name followed by a role keyword. Available roles include `axiom`, `definition`, `hypothesis`, and `theorem` among others. Then the actual formula is given. Depending on the chosen logic, the syntax can differ significantly. In the case of HOL the universal quantifier is represented by `!` and a `?` represents the existential quantifier. Furthermore, function application

<sup>5</sup> The problem can be found on the TPTP website too: <http://www.cs.miami.edu/~tptp/cgi-bin/SeeTPTP?Category=Problems&Domain=SET&File=SET557^1.p>

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is made explicit as a result of the Prolog compatibility with the @ operator. For a complete overview of the language, see the annotated BNF grammar available on the TPTP web page<sup>6</sup>.

All problems provided by the TPTP library are categorized into domains. The problem presented above is part of the *set theory* (SET) domain. Beside the categorization into domains, the TPTP project also provides tools to search for problems by various parameters such as the number of symbols, the status, or the rating of the problem. The rating of a problem is a number between 0.0 and 1.0 and represents the ratio of theorem provers that could solve the problem. A problem with rating 0.0 could be solved by all provers, whereas a problem with rating 1.0 is yet unsolved. The TPTP language is also widely used as a unified input language for theorem provers and also supports the representation of proofs. Furthermore, some additional projects are part of the TPTP infrastructure, such as the *systemONTPTP* service. This service provides a web interface for a collection of centrally hosted theorem proving systems (see Sutcliffe 2009).

<sup>6</sup> <http://www.cs.miami.edu/~tptp/TPTP/SyntaxBNF.html>

# 3 Quantified Boolean Formulas

After introducing the quite expressive language of HOL, we will now introduce Quantified Boolean Formulas (QBF). One can define QBFs either as a fragment of HOL, or as a generalization of propositional formulas. We will start by giving a definition of the HOL fragment below and then define the generalization of propositional formulas in section 3.1.

The fragment of QBF formulas are formulas without equality in which only Boolean variables appear.

**Definition 3.0.4** (The QBF Fragment). A HOL formula  $t_o$  is in the QBF fragment if:

1. All variables in  $t_o$  are of type  $o$ ,
2. Equality does not appear in  $t_o$ ,
3. Lambda terms only appear in the form  $\Pi(\lambda x_o^i. S_o)$ , where  $S_o$  is a formula in the QBF fragment.

Because of the third restriction, lambda terms are only allowed to express universal ( $\forall x_o. S$ ) and existential ( $\exists x_o. S$ ) quantification.

The HOL formula  $\forall x_o. \exists y_o. (\neg y \vee x) \wedge (\neg x \vee y)$  is a semantically valid formula in the QBF fragment, whereas  $\exists y_o. \forall x_o. (\neg y \vee x) \wedge (\neg x \vee y)$  is counter satisfiable. Since the domain of Boolean values ( $\mathbb{T}$  and  $\mathbb{F}$ ) is finite and therefore only finitely many assignments to the variables in a formula from the QBF fragment exist, the validity of formulas in the QBF fragment is decidable.

In the next section we will define QBFs independently from HOL and we will introduce the notion of *solving* QBFs. Subsequently, section 3.2 describes some state of the art QBF solving systems. To do so we start with a list of systems which participated in a recent competitive evaluation of QBF solving systems. As we will see, the majority of those systems rely on preprocessing tools.

When generalizing from propositional formulas one does not need to stop at QBFs, but can go further to Dependently Quantified Boolean Formulas (DQBF). We will introduce DQBFs in section 3.3. While dependently quantified Boolean formulas do not correspond to a fragment of HOL as directly as vanilla QBFs do,

we will identify HOL formulas that correspond to DQBF problems. Some QBF preprocessing techniques have been lifted to DQBF solving. Those techniques are good candidates for lifting to HOL.

### 3.1 The Structure of Quantified Boolean Formulas

Quantified Boolean formulas (QBF) are generalizing propositional formulas by adding universal and existential quantifiers over truth values. We will now present QBFs without using the language of HOL. Our discussion is inspired by the discussion in Büning and Bubeck (2009). Let  $V = \{x_1, x_2, \dots\}$  be a countable infinite set of propositional variables, then the QBFs are:

**Definition 3.1.1.** Quantified Boolean Formulas

- Every  $x_i \in V$  is a QBF,
- if  $s$  is a QBF, then so is  $\neg s$ ,
- if  $s$  and  $t$  are QBFs, then so is  $s \vee t$ ,
- if  $s$  and  $t$  are QBFs, then so is  $s \wedge t$ ,
- if  $s$  is a QBF and  $x_i \in V$  is a variable, then  $\exists x_i. s$  is a QBF,
- if  $s$  is a QBF and  $x_i \in V$  is a variable, then  $\forall x_i. s$  is a QBF.

The difference between pure propositional formulas and QBFs are the two last rules. If those two rules are not used, the resulting formulas are propositional formulas. The connectives  $\vee$  and  $\wedge$  express disjunction and conjunction and the quantifiers express quantification over truth values. Free ( $\mathcal{F}_s$ ) and bound ( $\mathcal{B}_s$ ) variables of a QBF  $s$ , are defined in the same way as they are defined for HOL terms. Furthermore, we will use the same shorthand notations as for HOL formulas. The QBF that intuitively correspond the HOL formulas in the QBF fragment mentioned above are  $\forall x. \exists y. (\neg y \vee x) \wedge (\neg x \vee y)$  and  $\exists y. \forall x. (\neg y \wedge x) \vee (\neg x \wedge y)$ .

To define the semantics of QBFs we again use the notion of an interpretation. A QBF-interpretation is a mapping  $I : V \rightarrow \{\mathbb{T}, \mathbb{F}\}$ . Given a QBF-interpretation  $I$ , we can again extend this recursively to a mapping from QBFs to truth values.

**Definition 3.1.2** (Valuation of Quantified Boolean Formulas). Given a QBF-interpretation  $I$  then the valuation  $\|\cdot\|^I$  is recursively defined on QBFs as:

$$\|x\|^I = I(x) \text{ if } x \in V$$

$$\begin{aligned}
 \|\neg s\|^I &= \begin{cases} \mathbb{T}, & \text{if } \|s\|^I = \mathbb{F} \\ \mathbb{F}, & \text{otherwise.} \end{cases} \\
 \|s \vee t\|^I &= \begin{cases} \mathbb{T}, & \text{if } \|s\|^I = \mathbb{T} \text{ or } \|t\|^I = \mathbb{T} \\ \mathbb{F}, & \text{otherwise.} \end{cases} \\
 \|s \wedge t\|^I &= \begin{cases} \mathbb{T}, & \text{if } \|s\|^I = \mathbb{T} \text{ and } \|t\|^I = \mathbb{T} \\ \mathbb{F}, & \text{otherwise.} \end{cases} \\
 \|\exists x. s\|^I &= \begin{cases} \mathbb{T}, & \text{if } \|s\|^{I[\mathbb{T}/x]} = \mathbb{T} \text{ or } \|s\|^{I[\mathbb{F}/x]} = \mathbb{T} \\ \mathbb{F}, & \text{otherwise.} \end{cases} \\
 \|\forall x. s\|^I &= \begin{cases} \mathbb{T}, & \text{if } \|s\|^{I[\mathbb{T}/x]} = \mathbb{T} \text{ and } \|s\|^{I[\mathbb{F}/x]} = \mathbb{T} \\ \mathbb{F}, & \text{otherwise.} \end{cases}
 \end{aligned}$$

Based on QBF-interpretations, validity and satisfiability of QBFs is defined in the same way as the for HOL formulas:

**Definition 3.1.3** (Status of Quantified Boolean Formulas). A QBF  $s$  is *valid*, if  $\|s\|^I = \mathbb{T}$  for all QBF-interpretations  $I$ . A QBF is *satisfiable*, if there is a QBF-interpretation  $I$ , such that  $\|s\|^I = \mathbb{T}$ .

A QBF which is not valid is called *counter satisfiable* and a QBF which is not satisfiable is *unsatisfiable*.

If a QBF  $s$  is valid, then  $\neg s$  is unsatisfiable. Two QBFs  $s, t$  are logically equivalent ( $s \equiv t$ ) if  $\|s\|^I = \|t\|^I$  for all QBF-interpretations  $I$ .

Since the set of variables is countable infinite, there are infinitely many interpretations. However only those that differ on the free variables appearing in a formula really matter:

**Lemma 3.1.1.** Given a QBF  $s$  and two QBF-interpretations  $I_1$  and  $I_2$ , such that  $I_1(x) = I_2(x)$  for all  $x \in \mathcal{F}_s$ , then  $\|s\|^{I_1} = \|s\|^{I_2}$ .

This lemma can easily be shown by a structural induction over the structure of QBFs. As consequence of this lemma, we know that the interpretation does not matter for closed QBFs (formulas without free variables) and that validity and satisfiability coincides in this case. Furthermore, a QBF  $s$  with free variables  $x_1, x_2, \dots, x_n$  is satisfiable if and only if the QBF  $\exists x_1. \exists x_2. \dots \exists x_n. s$  is valid.

Every QBF can be translated into a logically equivalent formula in *conjunctive normal form*. The description given here is loosely based on the description presented by Büning and Bubeck (2009). It is customary to first assume the

### 3 Quantified Boolean Formulas

formula is in Negative Normal Form (NNF). A formula is in negative normal form if negation only occurs immediately in front of variables. The NNF of any QBF can be constructed by using the following rules:

$$\begin{aligned} \neg\neg s &\equiv s \\ \neg(s \vee t) &\equiv \neg s \wedge \neg t & \neg(\forall x. s) &\equiv \exists x. \neg s \\ \neg(s \wedge t) &\equiv \neg s \vee \neg t & \neg(\exists x. s) &\equiv \forall x. \neg s \end{aligned}$$

After constructing the NNF, we construct the *prenex* form. A QBF  $s$  is in prenex form, if it has the form  $Q_1x_1. Q_2x_2. \dots Q_nx_n. t$  with  $Q_i \in \{\forall, \exists\}$  where  $t$  is quantifier free and in NNF. We call  $Q_1x_1. Q_2x_2. \dots Q_nx_n.$  the *prefix* and  $t$  the *matrix* of the formula in CNF. While the prenex form can easily be constructed by lifting the quantifiers from inside of conjunctions and disjunctions to the outside, one has to pay attention to variables being bound multiple times in different subterms. For example, the term  $(\forall x. s) \vee (\forall x. t)$  is not equivalent to  $\forall x. (s \vee t)$  if  $x$  appears free both in  $s$  and  $t$ . To solve this, one can rename bound variables first, such that they are never bound by more than one quantifier. This can easily be done and we will from now on assume that this has been done. Formulas that have this property are called *cleansed*. In practice, there are different strategies to construct the prenex form which have measurable influence on the reasoning performance of QBF solvers. For an overview see Egly et al. (2004).

After the prenex form has been constructed, the formula can be transformed into a formula in conjunctive normal form (CNF). A formula is in CNF, if it is in prenex normal form and the matrix is a conjunction of clauses.

To naively construct the CNF from the NNF the distributive laws  $s \vee (t \wedge u) \equiv (s \vee t) \wedge (s \vee u)$  and  $s \wedge (t \vee u) \equiv (s \wedge t) \vee (s \wedge u)$  can be used. This method, however, can lead to a QBF in CNF which is exponentially longer than the original formula. To avoid this problem one can introduce new variables recursively as *names* for subterms, replacing subterms with their names and adding terms equating the now simplified terms with their new names. As a last step, the conjunction of the equivalency terms is formed. This method was first described by Tseitin 1983. Since new variables are introduced, the resulting formula is not logically equivalent to the original one, but is satisfiable if and only if the original formula is. Overall we get:

**Lemma 3.1.2.** Any quantified Boolean formula  $s$  can be transformed into a cleansed formula  $t$  in CNF which is satisfiable if and only if  $s$  is satisfiable. The time to construct  $t$  and the length of  $t$  is linear in the length of  $s$ .

Since in formulas in CNF negation only appears directly in front of variables, we will call variables  $(x_i)$  and negated variables  $(\neg x_i)$  *literals*. If  $l$  is a literal,

then  $\bar{l}$  is the negated literal i.e. if  $l = x_i$  then  $\bar{l} = \neg x_i$  and if  $l = \neg x_i$  then  $\bar{l} = x_i$ . Obviously  $\bar{\bar{l}} = l$ . We will use the variables  $x_1, x_2, \dots$  for variables bound by the universal quantifiers and  $y_1, y_2, \dots$  for existentially quantified variables. We write  $\text{var}(l)$  for the variable of a literal. I.e.  $\text{var}(x) = x$  and  $\text{var}(\neg x) = x$ . Furthermore, a literal  $l$  is a  $\forall$ -literal, if  $\text{var}(l)$  is bound by a universal quantifier. On the other hand, a  $\exists$ -literal  $l$  is a literal with  $\text{var}(l)$  bound by an existential quantifier. We express the *polarity* of a literal as  $\text{pol}(l)$ . That means, that  $\text{pol}(x) = \mathbb{T}$  and  $\text{pol}(\neg x) = \mathbb{F}$ .

Deciding satisfiability of QBFs is solvable in polynomial space and PSPACE-complete. There is a close correspondence between QBFs and the polynomial-time hierarchy. The complexity depends on the number of quantifier alternations in the prefix.

**Definition 3.1.4** (Prefix Type of Quantified Boolean Formulas). A QBF which is propositional has prefix type  $\Sigma_0 = \Pi_0$  (empty prefix). If a formula  $s$  has prefix type  $\Sigma_n$ , then the formula  $\forall x_1. \dots \forall x_m. s$  is of type  $\Pi_{n+1}$ . If a formula  $s$  has prefix type  $\Pi_n$ , then the formula  $\exists y_1. \dots \exists y_m. s$  is of type  $\Sigma_{n+1}$ .

The correspondence between QBFs and the polynomial-time hierarchy is:

**Theorem 3.1.3.** For  $k \geq 1$ , the satisfiability problem for QBFs with prefix type  $\Sigma_k$  is  $\Sigma_k^P$ -complete, and for formulas with prefix type  $\Pi_k$ , it is  $\Pi_k^P$ -complete (cf. Büning and Bubeck 2009).

The class  $\Sigma_k^P$  is the class of all problems which can be decided in polynomial time by a non-deterministic Turing machine with the help of a  $\Sigma_{k-1}^P$ -oracle. A  $\mathcal{C}$ -oracle is a subroutine which allows the program to solve a problem in the complexity class  $\mathcal{C}$  in constant time. The class  $\Pi_k^P$  contains every problem whose complement is in  $\Sigma_k^P$ . Furthermore,  $\Sigma_1^P = NP$  and  $\Pi_1^P = coNP$ .

While we only consider classical QBF in this work, intuitionistic QBF has some applications too. In fact, it corresponds to the type system *System F*<sup>1</sup>. Since the type inhabitation problem for System F is undecidable, this also means that the satisfiability of intuitionistic QBFs is undecidable.

## RESOLUTION FOR QUANTIFIED BOOLEAN FORMULAS

Resolution for QBFs was first introduced by Büning, Karpinski, and Flögel (1995). In contemporary literature resolution for QBFs is often called Q-resolution. This emphasizes the explicit handling of universally quantified variables.

<sup>1</sup> See this blog post by Aaron Stump: <https://queuea9.wordpress.com/2011/03/24/quantified-boolean-conundrum/>.

### 3 Quantified Boolean Formulas

Alternatively, it is possible to use propositional resolution together with instantiation of the universally quantified variables. See Janota and Marques-Silva (2015) for a proof theoretic comparison.

As in the case of RUE-resolution, Q-resolution attempts to show the unsatisfiability of a problem by deriving the empty clause. To show the validity of an arbitrary QBF the formula is first negated and then transformed into CNF.

The Q-resolution rule is:

$$\frac{C \vee l \quad D \vee \bar{l} \quad l \text{ is an } \exists\text{-literal}}{\underbrace{(\mathcal{R}_\forall(C) \vee \mathcal{R}_\forall(D)) \setminus \{l, \bar{l}\}}_{\text{not tautological}}} \mathcal{R}(\text{Q-Res})$$

As indicated by the first rule, Q-resolution works by removing complementary  $\exists$ -literals as long as the resulting clause is not tautological. A clause is a tautological if it contains two complementary literals. Furthermore, Q-resolution performs *universal reduction* as indicated by  $\mathcal{R}_\forall(\cdot)$ . Given a clause  $C$ ,  $\mathcal{R}_\forall(C)$  is the clause after removing all  $\forall$ -literals which are not before any  $\exists$ -literal in the clause. A literal  $l_1$  is said to be before a literal  $l_2$ , if the variable of  $l_1$  occurs in the prefix left of the variable of  $l_2$ . If  $l_1$  is before  $l_2$ , we will write  $l_1 < l_2$ .

In other words, universal reduction removes all universally quantified variables which no existentially quantified variables depend on. Since a clause consisting only of non-complementary  $\forall$ -literals is always false, deriving such a clause is enough to finish the proof. If the resulting clause, however, would be tautological, the Q-resolution rule is not applicable. Not only are tautological clauses useless to finish the proof, they are harmful in combination with universal reduction<sup>2</sup>.

Consider the formula

$$\exists y_1. \forall x. \exists y_2. y_1 \wedge (\neg y_1 \vee x \vee y_2) \wedge (\neg x \vee \neg y_2).$$

This formula is valid and therefore Q-resolution should not be able to derive the empty clause. Consider, however, the following derivation where a disallowed tautology is generated:

$$\frac{\frac{\frac{y_1}{(\mathcal{R}_\forall(y_1) \vee \mathcal{R}_\forall(\neg y_1 \vee x \vee \neg x)) \setminus \{y_1, \neg y_1\}} \mathcal{R}(\text{Q-Res}) \quad \frac{\frac{\neg y_1 \vee x \vee y_2 \quad \neg x \vee \neg y_2}{(\mathcal{R}_\forall(\neg y_1 \vee x \vee y_2) \vee \mathcal{R}_\forall(\neg x \vee \neg y_2)) \setminus \{y_2, \neg y_2\}} \mathcal{R}(\text{Q-Res}')}{\frac{y_1}{(\mathcal{R}_\forall(y_1) \vee \mathcal{R}_\forall(\neg y_1 \vee x \vee \neg x)) \setminus \{y_1, \neg y_1\}} \mathcal{R}(\text{Q-Res}) \quad \frac{\neg y_1 \vee x \vee \neg x}{(\mathcal{R}_\forall(\neg y_1 \vee x \vee \neg x)) \setminus \{y_2, \neg y_2\}} \mathcal{R}(\text{Q-Res}')}{\{}} \mathcal{R}(\text{Q-Res})$$

- <sup>2</sup> Martin Suda pointed out this subtlety in a personal conversation. As he noticed, first-order (and higher-order) resolution does allow tautologies. However in those cases unification does block the removal of tautologies.

The first resolution step generates the tautology  $\neg y_1 \vee x \vee \neg x$ . Universal reduction is unable to delete the variable  $x$  from this clause, because  $y_2$  is still dependent on it. In the second resolution step, with  $y_1$ , the tautology is removed by universal reduction which is obviously unsound.

While universal reduction can be applied as a standard simplification rule, it is also necessary for the completeness of Q-resolution (cf. Büning and Bubeck 2009).

#### FROM QUANTIFIED BOOLEAN FORMULAS TO HIGHER ORDER LOGIC

We will now show that QBFS correspond to the QBF fragment of HOL. The translation between the two formalisms is straightforward.

**Definition 3.1.5** (Translation from QBF to HOL). The function  $\rho$  from QBF formulas to HOL formulas is defined recursively:

$$\begin{aligned} \rho(x_i) &= x_o^i & \rho(\neg s) &= \neg_{o \rightarrow o} \rho(s) \\ \rho(s \vee t) &= \rho(s) \vee_{o \rightarrow o \rightarrow o} \rho(t) & \rho(s \wedge t) &= \rho(s) \wedge_{o \rightarrow o \rightarrow o} \rho(t) \\ \rho(\exists x_i. s) &= \exists x_o^i. \rho(s) & \rho(\forall x_i. s) &= \forall x_o^i. \rho(s) \end{aligned}$$

By definition 3.0.4, all formulas generated by  $\rho$  are in the QBF fragment of HOL. Furthermore, the function is invertible and the inverse is defined on all formulas of the QBF fragment. Therefore, this function is a bijection between QBFS and the QBF fragment of HOL.

The satisfiability of the two formalisms corresponds:

**Theorem 3.1.4.** Let  $s$  be an arbitrary QBF, then there is an interpretation  $\mathcal{M}$  and variable assignment  $\sigma$  such that  $\|\rho(s)\|^{\mathcal{M}, \sigma} = S$  if and only if there is a QBF-interpretation  $\mathbb{I}$ , such that  $\|s\|^{\mathbb{I}} = S$  where  $S \in \{\mathbb{T}, \mathbb{F}\}$ .

*Proof.* Given any QBF-interpretation  $\mathbb{I}$ , let  $\sigma_{\mathbb{I}} : x_o^i \mapsto \|x_i\|^{\mathbb{I}}$  be the corresponding variable assignment. Since no formula in the QBF fragment contains variables which are not of type  $o$ , this assignment is well defined for all formulas in the QBF fragment. Furthermore, we can choose an arbitrary HOL interpretation, since the domain will always contain both truth values and the functions for the logical operators.

Given an interpretation  $\mathcal{M}$  and a variable assignment  $\sigma$ , then the corresponding QBF-interpretation  $\mathbb{I}_{\sigma}$  is defined as:  $\mathbb{I}_{\sigma} : x_i \mapsto \sigma(x_o^i)$ .

Now first assume a QBF  $s$  and a QBF-interpretation  $\mathbb{I}$ , such that  $\|s\|^{\mathbb{I}} = S$ . We have to show that  $\|\rho(s)\|^{\mathcal{M}, \sigma_{\mathbb{I}}} = S$ . This can be shown by induction over the structure of QBFS.

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As the base case for the induction we have to consider terms which are just variables. By definition the corresponding interpretation will assign the same value to the mapped HOL variable in this case.

Now we do a case distinction on the topmost operator:

- If  $s = \neg t$  and
  1.  $\|\neg t\|^I = \mathbb{T}$ , then  $\|t\|^I = \mathbb{F}$  and by applying the induction hypothesis  $\|\rho(\neg t)\|^{\mathcal{M}, \sigma_I} = \|\neg \rho(t)\|^{\mathcal{M}, \sigma_I} = \mathcal{I}(\neg)(\|\rho(t)\|^{\mathcal{M}, \sigma_I}) = \mathcal{I}(\neg)(\mathbb{F}) = \mathbb{T}$ .
  2.  $\|\neg t\|^I = \mathbb{F}$ , then  $\|t\|^I = \mathbb{T}$  and  $\|\rho(\neg t)\|^{\mathcal{M}, \sigma_I} = \mathcal{I}(\neg)(\mathbb{T}) = \mathbb{F}$
- If  $s = t \vee u$  and
  1.  $\|t \vee u\|^I = \mathbb{T}$ , then without loss of generality  $\|t\|^I = \mathbb{T}$  and by induction hypothesis  $\|\rho(t \vee u)\|^{\mathcal{M}, \sigma_I} = \mathcal{I}(\vee)(\|\rho(t)\|^{\mathcal{M}, \sigma_I}, \|\rho(u)\|^{\mathcal{M}, \sigma_I}) = \mathcal{I}(\vee)(\mathbb{T}, \_) = \mathbb{T}$ .
  2.  $\|t \vee u\|^I = \mathbb{F}$ , then  $\|t\|^I = \mathbb{F}$  and  $\|u\|^I = \mathbb{F}$ . We get by induction hypothesis  $\|\rho(t \vee u)\|^{\mathcal{M}, \sigma_I} = \mathcal{I}(\vee)(\|\rho(t)\|^{\mathcal{M}, \sigma_I}, \|\rho(u)\|^{\mathcal{M}, \sigma_I}) = \mathcal{I}(\vee)(\mathbb{F}, \mathbb{F}) = \mathbb{F}$ .
- If  $s = t \wedge u$  and
  1.  $\|t \wedge u\|^I = \mathbb{T}$ , then  $\|t\|^I = \mathbb{T}$  and  $\|u\|^I = \mathbb{T}$ . We get by induction hypothesis  $\|\rho(t \wedge u)\|^{\mathcal{M}, \sigma_I} = \mathcal{I}(\wedge)(\|\rho(t)\|^{\mathcal{M}, \sigma_I}, \|\rho(u)\|^{\mathcal{M}, \sigma_I}) = \mathcal{I}(\wedge)(\mathbb{T}, \mathbb{T}) = \mathbb{T}$ .
  2.  $\|t \wedge u\|^I = \mathbb{F}$ , then without loss of generality  $\|t\|^I = \mathbb{F}$  and by induction hypothesis  $\|\rho(t \wedge u)\|^{\mathcal{M}, \sigma_I} = \mathcal{I}(\wedge)(\|\rho(t)\|^{\mathcal{M}, \sigma_I}, \|\rho(u)\|^{\mathcal{M}, \sigma_I}) = \mathcal{I}(\wedge)(\mathbb{F}, \_) = \mathbb{F}$ .
- If  $s = \exists x_i. s$  and
  1.  $\exists x_i. s = \mathbb{T}$ : Then there is some  $S \in \{\mathbb{T}, \mathbb{F}\}$ , such that  $\|s\|^{I[S/x]} = \mathbb{T}$  and we get  $\|\rho(s)\|^{\mathcal{M}, \sigma_{I[S/x]}} = \mathbb{T} = \|\rho(\exists x_i. s)\|^{\mathcal{M}, \sigma_I}$ .
  2.  $\exists x_i. s = \mathbb{F}$ : Then  $\|\rho(s)\|^{\mathcal{M}, \sigma_{I[S/x]}} = \|s\|^{I[S/x]} = \mathbb{F}$  for both  $S \in \{\mathbb{T}, \mathbb{F}\}$  and therefore  $\|\rho(\exists x_i. s)\|^{\mathcal{M}, \sigma_I} = \mathbb{F}$ .
- If  $s = \forall x_i. s$  and
  1.  $\forall x_i. s = \mathbb{T}$ : Then  $\|\rho(s)\|^{\mathcal{M}, \sigma_{I[S/x]}} = \|s\|^{I[S/x]} = \mathbb{T}$  for both  $S \in \{\mathbb{T}, \mathbb{F}\}$  and therefore  $\|\rho(\forall x_i. s)\|^{\mathcal{M}, \sigma_I} = \mathbb{T}$ .
  2.  $\forall x_i. s = \mathbb{F}$ : Then there is some  $S \in \{\mathbb{T}, \mathbb{F}\}$ , such that  $\|s\|^{I[S/x]} = \mathbb{F}$  and we get  $\|\rho(s)\|^{\mathcal{M}, \sigma_{I[S/x]}} = \mathbb{F} = \|\rho(\forall x_i. s)\|^{\mathcal{M}, \sigma_I}$ .

Constructing the QBF-interpretation from a given HOL interpretation and variable assignment is similar.  $\square$

Theorem 3.1.4 covers satisfiability and counter satisfiability. Therefore, we get as an immediate result:

**Corollary 3.1.5.** Let  $s$  be an arbitrary QBF, then  $\rho(s)$  is *satisfiable* if and only if  $s$  is.

Since validity of a formula is equivalent to the unsatisfiability of its negation and for all quantified Boolean formulas  $\rho(\neg s) = \neg_{o \rightarrow o} \rho(s)$ , theorem 3.1.4 also shows:

**Corollary 3.1.6.** Let  $s$  be an arbitrary QBF, then  $\rho(s)$  is *valid* if and only if  $s$  is.

## 3.2 The Solving Pipeline

After introducing QBFS in the last section, we will have a look at state of the art QBF solving. What the CASc is for HOL provers, is QBFEVAL for QBF solvers. The latest iteration of this competitive evaluation, QBFEVAL'16, was part of the SAT 2016 conference in Bordeaux, France. See the report by Pulina (2016) and the web page of QBFEVAL<sup>3</sup> for an extensive description of the competition. The competition featured multiple tracks. Amongst them are tracks for QBFS in prenex CNF, for QBFS in prenex non-CNF form, and for proof producing solvers.

Overall 22 systems participated in the evaluation, some of them in multiple configurations for a total of 44 systems. The prenex CNF track featured 12 systems in overall 19 configurations<sup>4</sup>. Of those 12 systems all, but the two systems which do not reason on prenex CNF formulas (*AIGsolve* and *ghosttextscq*), integrate a preprocessing tool in at least one configuration. Therefore, we will first describe the three systems which solved the most problems in the prenex CNF track and then discuss the used preprocessing tools.

**RAReQS** by Janota, Klieber, et al. (2012) is based on Counterexample Abstraction Refinement (CEGAR). The solver step by step expands the formula into a propositional formula by replacing variables with Boolean

<sup>3</sup> [http://www.qbflib.org/index\\_eval.php](http://www.qbflib.org/index_eval.php)

<sup>4</sup> The prenex CNF track originally had 24 entrants. Some systems, however, have been discarded from the results. See Pulina (2016) for details.

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constants. For existentially quantified variables a disjunction, for universally quantified variables a conjunction is introduced. The resulting propositional formula can then be solved by a SAT solver. To mediate the exponential growth of the formulas generated by this procedure, the system expands only a selected subset of the variables. This subset is called an *abstraction*. The solver then uses solutions for the propositional formulas induced by a given abstraction as candidate solutions. Candidate solutions are not necessary solutions for the original problem, but the solver uses counterexamples for candidate solutions to refine the abstraction. The version of RAREQS submitted to QBFEVAL'16 uses the preprocessing tool BLOQQER. RAREQS solved 640 of 825 problems.

**QSTS** is based on nested SAT solving. The solver first applies a tool to transform the input problem into an internal SAT-TO-SAT format while applying various transformations and a structure extraction algorithm. Then the SAT-TO-SAT solver is called. This tool is a general purpose SAT solver that extends classical propositional satisfiability with nesting of SAT solving (see Bogaerts, Janhunen, and Tasharrofi 2016). The most successful configuration of QSTS used BLOQQER as a preprocessor and solved 613 of 825 problems.

**DepQBF** by Lonsing and Biere (2010) is a search based QBF solver. The search procedure guesses variable assignments and then propagates assignments necessary implied by the guessed assignment. If a contradiction is found, the procedure backtracks the implication process and tries another assignment. A number of techniques are used to speed this process up, most notably the addition of clauses found contradictory to exclude new, but ultimately unsatisfying assignments early on. Overall the algorithm implemented by DepQBF is QDPLL with conflict-driven clause and solution-driven cube learning. As the name suggests, it is an extension of the successful DPLL approach.

While some configurations of DepQBF, which participated in QBFEVAL'16 use the BLOQQER preprocessing tool, version 5.0 of DepQBF also supports the use of the preprocessing technique blocked clause elimination during reasoning (see Lonsing, Bacchus, et al. 2015). The most successful configuration of DepQBF solved 603 of 825 problems.

While the top three QBF solvers all use quite different solving techniques, they all used the preprocessing tool BLOQQER. This indicates that competitive QBF solving relies on a pipeline approach.

First, the problem is processed by one, or multiple preprocessing tools. The preprocessing tools usually accept the problem in CNF prenex form using a standardized input format. The format used for the competition is the QDIMACS format. A description of the QDIMACS format can be found on page 73. The output of the preprocessing tool is a satisfiability-equivalent problem which again is in the QDIMACS format. Secondly, the preprocessed file is consumed by the solver proper which outputs the status of the problem.

While the preprocessing tool BLOQQER is widely used, it is not the only preprocessing tool used by systems which participated in QBFEVAL'16. The HIQQER system used the two preprocessors PLODDER and EQXBF (cf. Janota, Jordan, et al. 2016). In one of its configurations the QSTS tool uses BREAKID (Devriendt, Bogaerts, and Bruynooghe 2014), a symmetry breaking tool for propositional problems which has been extended to QBF. This particular configuration, however, has been omitted from the result because it reported discrepancies. Finally, the preprocessing tool *squeezeBF* (Giunchiglia, Marin, and Narizzano 2010) has been coupled with the *Aqua*, *AQME*, and *struQS* system.

The BREAKID tool is developed publicly<sup>5</sup> and BLOQQER is available on its web page<sup>6</sup>. We could not find publicly available versions of the other mentioned preprocessing tools.

As the name BLOQQER indicates, the main preprocessing technique it uses is Quantified Blocked Clause Elimination (QBCE) (Biere, Lonsing, and Seidl 2011). It also implements a wide variety of other preprocessing techniques and recent versions can produce a proof output in the QRAT format (Heule, Seidl, and Biere 2014). We will give a general overview on some preprocessing techniques for QBFs in subsection 4.1.

## 3.3 Dependently Quantified Boolean Formulas

Dependently Quantified Boolean Formulas (DQBF) are a generalization of QBF which does not form a fragment of HOL. DQBFs, however, can be easily translated to satisfiability equivalent HOL formulas. Preprocessing techniques which have been generalized from QBF solving to DQBF solving are good candidates for lifting to HOL.

As we discussed in the previous sections, the variables in the quantifier prefix of a QBF in prefix normal form are linearly ordered. One can drop this restriction and extend the syntax with a way to explicitly declare the dependencies of the existentially quantified variables from the universally quantified variables.

<sup>5</sup> at: <https://bitbucket.org/krr/breakid>

<sup>6</sup> at: <http://fmv.jku.at/bloqqer/>

### 3 Quantified Boolean Formulas

This extension of QBF was first defined by Peterson and Reif (1979) to model games with partially hidden information. Their definition of DQBFs is restricted to formulas in prenex normal form and while it is straightforward to define the syntax of arbitrary DQBFs, we follow their definition:

**Definition 3.3.1** (Dependently Quantified Boolean Formulas). The DQBFs are formulas of the form

$$\forall \mathbf{x}_1. \forall \mathbf{x}_2. \dots \forall \mathbf{x}_n. \exists \mathbf{y}_1(\mathbf{x}_{l_1^1}, \mathbf{x}_{l_1^2}, \dots, \mathbf{x}_{l_1^{n_1}}). \dots \exists \mathbf{y}_m(\mathbf{x}_{l_m^1}, \dots, \mathbf{x}_{l_m^{m_m}}). \Phi$$

where  $1 \leq l_i^j \leq n$  and  $\Phi$  is a propositional formula with the free variables  $\mathbf{x}_1, \mathbf{x}_2, \dots, \mathbf{x}_n, \mathbf{y}_1, \mathbf{y}_2, \dots, \mathbf{y}_m$ .

In this new formulation, the existentially quantified variables carry the list of universally quantified variables with them. This obviously makes multiple quantifier alternations superfluous. Furthermore, the order and multiplicity of the variables denoting the dependencies does not matter. Therefore, for quantifiers of the form  $\exists \mathbf{y}_i(\mathbf{x}_{l_i^1}, \mathbf{x}_{l_i^2}, \dots, \mathbf{x}_{l_i^{n_i}})$ . . . we can collect universally quantified variables in a set  $D_{y_i} = \{\mathbf{x}_{l_i^1}, \mathbf{x}_{l_i^2}, \dots, \mathbf{x}_{l_i^{n_i}}\}$ . The set  $D_{y_i}$  is the *dependency set* of the existentially quantified variable  $y_i$ .

Every QBF can be expressed as a DQBF by collecting all universally quantified variables that appear before an existentially quantified variable  $y_i$  in the prefix into the dependency set  $D_{y_i}$ . So  $\forall \mathbf{x}. \exists \mathbf{y}. (\neg \mathbf{y} \vee \mathbf{x}) \wedge (\neg \mathbf{x} \vee \mathbf{y})$  corresponds to  $\forall \mathbf{x}. \exists \mathbf{y}(\mathbf{x}). (\neg \mathbf{y} \vee \mathbf{x}) \wedge (\neg \mathbf{x} \vee \mathbf{y})$  and if we switch the quantifiers to  $\exists \mathbf{y}. \forall \mathbf{x}. (\neg \mathbf{y} \vee \mathbf{x}) \wedge (\neg \mathbf{x} \vee \mathbf{y})$  the corresponding DQBF changes to  $\forall \mathbf{x}. \exists \mathbf{y}(). (\neg \mathbf{y} \vee \mathbf{x}) \wedge (\neg \mathbf{x} \vee \mathbf{y})$ . Not all DQBFs correspond to QBFs. Consider for example the formula

$$\forall \mathbf{x}_1, \mathbf{x}_2. \exists \mathbf{y}_1(\mathbf{x}_1). \exists \mathbf{y}_2(\mathbf{x}_2). (\mathbf{y}_1 \vee \mathbf{x}_1) \wedge (\neg \mathbf{y}_1 \vee \neg \mathbf{x}_1) \wedge (\mathbf{y}_2 \vee \mathbf{x}_2) \wedge (\neg \mathbf{y}_2 \vee \neg \mathbf{x}_2).$$

In general, evaluating DQBFs is harder than evaluating QBFs. Peterson and Reif (1979) have shown, that evaluating DQBFs is NEXPTIME-complete. Furthermore, DQBFs can be used to solve the *partial equivalence checking* problem. The task of this problem is to extend a only partially defined logical circuit to match the functionality of a fully specified circuit (see Gitina, Reimer, et al. 2013).

To define the semantic of DQBF, one can use Skolem functions. Since this directly leads to the embedding of DQBF into HOL, we will forgo the definition of the semantic of DQBF and instead directly give the embedding into HOL. First we need the concept of a Skolem function.

**Definition 3.3.2** (Skolem Function for DQBFs). Let  $\tau$  be an arbitrary DQBF and  $y_i$  a existentially quantified variable of  $\tau$ . Furthermore, let  $D_{y_i} = \{\mathbf{x}_{l_1}, \mathbf{x}_{l_2}, \dots,$

$x_{l_n}$  be the dependency set of  $y_i$  in  $\tau$  where  $l_1 < l_2 < \dots < l_n$ . I.e. we fix an order of the universally quantified variables appearing in the dependency set. Then  $\sigma_\tau$  is the following mapping from existentially quantified variables to HOL terms:

$$\sigma_\tau : y_i \mapsto \underbrace{\tau sk_{o \rightarrow \dots \rightarrow o \rightarrow o}^i}_{n \text{ times}} x_o^{l_1} x_o^{l_2} \dots x_o^{l_n}.$$

The fresh function symbol  $\tau sk^i$  is the *Skolem function* of  $y_i$  in  $\tau$ .

**Definition 3.3.3** (Translation from DQBF to HOL). Let  $\tau$  be a DQBF in prenex form, then

$$\begin{aligned} \rho(\forall x_1, x_2, \dots, x_n. \exists y_1(x_{l_1}^1, \dots, x_{l_1}^{n_1}), \dots, y_m(x_{l_m}^1, \dots, x_{l_m}^{n_m}). \Phi) = \\ \exists \tau sk^1, \dots, \tau sk^m. \forall x_o^1, x_o^2, \dots, x_o^n. \rho'(\Phi) \end{aligned}$$

is the translation function into a HOL formula. The function  $\rho'$  translates the matrix and is defined recursively as:

$$\begin{aligned} \rho'(x_i) &= x_o^i & \rho'(\mathbf{s} \vee \tau) &= \rho'(\mathbf{s}) \vee_{o \rightarrow o \rightarrow o} \rho'(\tau) \\ \rho'(y_i) &= \sigma_\tau(y_i) & \rho'(\mathbf{s} \wedge \tau) &= \rho'(\mathbf{s}) \wedge_{o \rightarrow o \rightarrow o} \rho'(\tau) \\ \rho'(\neg \mathbf{s}) &= \neg_{o \rightarrow o} \rho'(\mathbf{s}) \end{aligned}$$

In other words, we translate a DQBF into HOL by replacing existentially quantified variables with functions that calculate satisfying assignment to the existentially quantified variables from the universally quantified variables. A DQBF is satisfiable if such functions exist. Therefore, a DQBF  $\tau$  is true if and only if  $\rho(\tau)$  is valid.

### SYSTEMS FOR DQBF SOLVING

Recently some systems which solve DQBF formulas have emerged. For a relatively recent overview see Kovásznai (2015).

A first attempt at developing an efficient general purpose DQBF solver has been made by Fröhlich, Kovásznai, and Biere (2012). Their approach was to develop DPLL-style search based algorithm and to lift various successful techniques from SAT solving to DQBF solving. The authors, however, concluded that «it does not perform very well».

Another approach emerges from the observation that Effectively Propositional Logic (EPR), a fragment of first-order logic, is also NEXPTIME-complete. A solving approach for EPR formulas is the *Inst-Gen* calculus. This calculus combines unification to generate clause instantiations with an abstraction refinement loop. The *idQ* system<sup>7</sup> (Fröhlich et al. 2014) implements this calculus

<sup>7</sup> available at: <http://fmv.jku.at/idq/>

### 3 *Quantified Boolean Formulas*

with specific low level optimizations for the Boolean domain, such as bit mask operations for unification. This system was the first publicly available solver for DQBF.

The HQS system (Gitina, Wimmer, et al. 2015) is a DQBF solver based on quantifier elimination. A universally quantified variable  $x$  can be removed from the dependency set of an existentially quantified variable by forming the conjunction of the formula where  $x$  is instantiated once with a constant which is always true and once with a constant which is false. The HQS system attempts to reduce the DQBF input problem to a QBF problem by systematically eliminating variables. To minimize the number of necessary expansions the HQS system utilizes a partial MAXSAT solver.

Furthermore, some preprocessing techniques have been lifted from QBF solving to DQBF by Wimmer, Gitina, et al. (2015) and subsequently been implemented in DQBF preprocessing tool for HQS (Wimmer, Reimer, et al. 2017).

# 4 Preprocessing Techniques

In this chapter we will introduce multiple preprocessing techniques for HOL which are inspired by QBF and DQBF preprocessing techniques.

As outlined in section 3.2, modern QBF solving systems employ various preprocessing tools. Thus, we give an overview of preprocessing techniques used in QBF and DQBF solving in section 4.1, before moving to HOL in the subsequent section.

Section 4.2 discusses universal reduction in the context of HOL. We already introduced this method as part of Q-resolution in the introduction chapter and will describe its extension to HOL.

Subsequently, section 4.3 will discuss ways to detect constant literals. In the context of QBF solving a literal is constant, if it can be replaced with a constant in all clauses. Pure literals, i.e. literals which only appear with one polarity are constant. In the context of HOL we will add constants as unit clauses.

One of the central preprocessing techniques for QBF is Quantified Blocked Clause Elimination (QBCE). The preprocessing system BLOQQER is named after this preprocessing method (cf. Biere, Lonsing, and Seidl 2011). While we have a look at QBCE in the context of QBF solving as part of section 4.1, we will also discuss an adaption to HOL in section 4.4.

Finally, in section 4.5 we will present a preprocessing technique which is not an adaption of a QBF, or DQBF preprocessing technique. Instead, we take inspiration from the encoding of QBF formulas into first-order logic and hypothesize that we can adapt this encoding to accelerate reasoning with Boolean literals by delaying primitive substitution.

The discussion in the chapter is a theoretical one, but we also implemented all the techniques presented here as part of the Leo-III theorem prover. This implementation alongside an empirical evaluation is discussed in the next chapter.

## 4.1 Preprocessing for QBF and DQBF

In this section we give an overview of current preprocessing techniques used for QBF and DQBF problems. Instead of attempting to give a full survey over all

## 4 Preprocessing Techniques

available preprocessing techniques, we rely on two useful sources.

The first source is the description of QRAT by Heule, Seidl, and Biere (2014). The QRAT system is used to represent proofs of deductions conducted during preprocessing of QBF problems. After concluding the preprocessing process, the generated QRAT proofs can be verified by a special proof checking tool. At the heart of QRAT are three Quantified Resolution Asymmetric Tautology rules. According to the authors, those three rules can be used to «efficiently express *all* preprocessing techniques used in state-of-the-art preprocessors». Therefore, this publication necessarily gives an overview of most currently used preprocessing techniques for QBF solving.

Secondly, we rely on the lifting of multiple QBF preprocessing techniques to DQBF preprocessing by Wimmer, Gitina, et al. (2015). Not only are these techniques good candidates for further lifting to HOL, their presentation and categorization is also very insightful.

### PREPROCESSING TECHNIQUES IN THE CONTEXT OF QRAT

In the presentation of QRAT Heule, Seidl, and Biere (2014) distinguished between three types of preprocessing rules. *Clause Elimination Rules* are used to remove clauses, while preserving unsatisfiability. *Clause Modification Rules*, on the other hand, changes clauses by adding or removing literals. Finally, *Clause Addition Rules* are used to add new clauses to the problem.

A rule in the first category is the elimination of clauses which are tautological. Clauses which contain a literal and its negation at the same time are tautological and can be removed. Furthermore, a clause which is a subset of another clause is subsumed by that clause and can be removed. Clauses consisting of only an  $\exists$ -literal are also superfluous and can be removed safely. Finally, clauses which fulfill certain properties are called *blocked*. Removal of these clauses is the (*Quantified*) *Blocked Clause Elimination* (QBCE) technique. Since this technique is very successful, we will discuss it separately later.

As discussed above, universal reduction allows the removal of certain  $\forall$ -literals from clauses. Hence, this is a clause modification rule. Moreover, if the problem contains two clauses  $C \vee l$  and  $D \vee \bar{l}$  where  $C \subseteq D$ , then after applying Q-resolution the resolvent subsumes  $C$ . Using this insight, the *strengthening* rule modifies  $C$  by removing  $l$ . The *Unit Literal Elimination* rule can be used to remove clauses containing  $l$  and occurrences of  $\bar{l}$  if  $l$  is an  $\exists$ -literal and occurs as a clause by itself and *Universal Pure Literal Elimination* removes  $\forall$ -literals if it occurs in only one polarity. *Covered Literal Addition* adds literals that occur in all non-tautological resolvents of a clause to that clause. *Equivalence Replacement* replaces literals with literals which are equivalent with them.

Two literals  $l$  and  $k$  are equivalent, if the two clauses  $l \vee \bar{k}$  and  $\bar{l} \vee k$  are part of the problem.

The two clause addition rules both remove variables. *Variable Elimination* removes an existentially quantified variable, by replacing clauses containing this variables by their non-tautological resolvents. Secondly, the *Universal Expansion* rule removes innermost  $\forall$ -literals by duplicating and modifying clauses.

#### PREPROCESSING TECHNIQUES LIFTED TO DQBF

The difference between QBFs and DQBFs is that DQBFs admits quantifier relations which do not form a total order. This translates to preprocessing techniques. Those QBF preprocessing techniques, where the implicit dependency («left of») can be substituted by an explicit dependency are good candidates for a translation to DQBF.

When describing preprocessing for DQBF, Wimmer, Gitina, et al. (2015) start with *backbones*, *monotonic*, and *equivalent* variables. Backbone variables are variables that will result in an unsatisfiable DQBF when replaced with a constant. And for a DQBF  $\phi$  a variable  $v$  is monotonic, if either  $\phi[\mathbb{F}/v] \wedge \neg\phi[\mathbb{T}/v]$  or  $\phi[\mathbb{T}/v] \wedge \neg\phi[\mathbb{F}/v]$  is unsatisfiable. Both definitions are generalizations of unit and pure literals as discussed before and either indicate an unsatisfiable problem, or can be replaced by a constant. In fact, unit literals or pure literals are two purely syntactic criteria for detecting backbone or monotonic variables, respectively. A further criterion utilizes the binary implication graph. This is the graph formed by taking the literal as nodes and adding edges for all implications  $l \rightarrow m$  in the problem<sup>1</sup>. Not only can this graph be used to give some syntactic criterion for backbone and monotonic literals, it can also be used to find equivalent literals. Furthermore, the authors mention, that SAT solvers can be utilized to find backbone and monotonic literals. While this might be untypical for QBF and DQBF solving, we investigated this approach since HOL problems typically have much fewer and smaller clauses.

The second type of DQBF preprocessing techniques reduces the dependency sets of variables. Again a sufficient criteria is given. This criterion utilizes the *variable-clause incidence graph*, which is a bipartite graph with edges from variables to the clauses they appear in. An existential variable  $x$  then is independent of a universal variable  $y$  if there is no path from  $x$  to  $y$  visiting only existentially quantified variables which are dependent on the universal variable  $y$ .

Techniques resulting from proof search procedures for QBF form the third set of DQBF preprocessing techniques. This includes universal reduction, res-

<sup>1</sup> This especially means that a clause of the form  $x \vee y$  induces four edges:  $(x, \neg y)$ ,  $(\neg x, y)$ , and the reverse edges.

olution and universal expansion. While universal reduction and resolution together form a complete calculus for QBF, this is no longer true for DQBF. Nevertheless, resolution can be used under some condition to eliminate existential variables. Universal expansion is used to eliminate a universal variable by duplicating the clauses and replacing the variable with the constants  $\mathbb{T}$  and  $\mathbb{F}$ .

Then the authors present blocked clause elimination for DQBF. They also generalize then notion of hidden and covered literals, which are literals that can be added to clauses without changing the status of the problem. Adding them will increase the chance that a clause is blocked and can removed.

Finally, the structure extraction is discussed. In practice, DQBF problems are often generated from Boolean circuits or more complex expressions by clausification. Some solvers do not rely on a problem in CNF. In this case, structure extraction can be used to recover the original structure of the problem.

#### BLOCKED CLAUSE ELIMINATION

Blocked Clause Elimination (BCE) is one of several preprocessing techniques which utilize *blocked clauses*. The concept of blocked clauses has its origins in SAT solving (cf. Jarvisalo, Biere, and Heule 2010). It was then lifted to QBF solving by Biere, Lonsing, and Seidl (2011) and to DQBF by Wimmer, Gitina, et al. (2015).

Beside eliminating blocked clauses, it is also possible to carefully add small blocked clauses. This technique is called *blocked clause addition* and has been shown to be useful in certain situations (Jarvisalo, Heule, and Biere 2012).

We now give definitions of blocked clause as used by Wimmer, Gitina, et al. (2015) for DQBF. A clause is blocked if it contains an  $\exists$ -literal such that all resolvents on that literal are tautological.

**Definition 4.1.1** (Blocked Clause). Let  $\Phi$  be a DQBF,  $C$  a clause in  $\Phi$ , and  $l \in C$  a  $\exists$ -literal. The literal  $l$  is a *blocking literal* for  $C$  if for all  $C' \in \Phi$  with  $\bar{l} \in C'$  there is a literal  $m$  such that  $\{m, \bar{m}\} \in (C \cup C') \setminus \{l, \bar{l}\}$  and  $D_m \subseteq D_l$ . A clause is *blocked* if it contains a blocking literal.

Blocked clauses can be removed from problems without changing the satisfiability status of the problem. As this is a purely syntactic criterion, it is straightforward to search for blocked clauses. To increase the chance of a clause being blocked, hidden literals can be added. A literal  $l \notin C$  is a *hidden literal* for  $C$  if there is a clause  $\{l_1, \dots, l_n, \bar{l}\} \in \Phi$  such that  $\{l_1, \dots, l_n\} \subseteq C$ . Then replacing  $C$  by  $C \cup \{l\}$  results in a satisfiability equivalent problem. Another type of literal which can be added to clauses without changing the status of the problem

are *covered literals*. The definition of covered literals in the case DQBF is a quite technical and we instead quote a rough intuition given by Wimmer, Gitina, et al. (2015):

If a literal  $k$  is already contained in all non-tautological resolvents of a clause  $C$  with pivot literal  $l$ , then  $k$  may be added to  $C$  resulting in an equivalent formula.

Recently Kiesl et al. (2017) adapted the concept of blocked clause to first-order logic. The authors faced the challenge of handling unification and equality. When analyzing blocked clause elimination in the context of higher-order logic, the same difficulties arise. We discuss this generalization together with additional challenges arising in HOL in section 4.4.

## 4.2 Universal Reduction

In section 3.1 we already introduced universal reduction as part of the Q-resolution rule. Universal reduction is necessary for the completeness of Q-resolution.

Assuming a problem in CNF, we can describe universal reduction as defined by Wimmer, Gitina, et al. (2015) for DQBF problems as an explicit rule:

$$\frac{l \vee j_1 \vee \dots \vee j_n \quad l \text{ is a } \forall\text{-literal, } \text{var}(l) \notin D_{\text{var}(j_i)}}{j_1 \vee \dots \vee j_n}$$

The meaning of the rule is: Given a clause and a  $\forall$ -literal, if none of the existentially quantified variables in that clause depend on the variable of that literal, then the literal can be removed from the clause. This is quite intuitive. If all the other variables appearing in the clause are independent of the  $\forall$ -literal, then this literal can just be set to false and therefore become irrelevant for the truth value of the clause.

In skolemized HOL formulas the dependencies are explicitly present in the terms. All the universally quantified variable an existentially quantified variable depends on appear as arguments to the Skolem functions. Furthermore, a more complex term depends on a universally quantified variable if the variable appears free in that term.

Hence, the universal reduction rule for HOL is<sup>2</sup>:

- 2 This rule is slightly more restrictive than it needs to be. If the literal in question appears multiple times, the rule can not be applied anymore. We can, however, assume that duplicated literals have been removed during clausification.

#### 4 Preprocessing Techniques

$$\frac{[x_o]^\alpha \vee [s_1]^{\beta_1} \vee \dots \vee [s_n]^{\beta_n} \quad x_o \notin \mathcal{F}_{s_i}}{[s_1]^{\beta_1} \vee \dots \vee [s_n]^{\beta_n}} \mathcal{R}(UR)$$

Since  $n \geq 1$ , this rule can not derive the empty clause, even though the clause containing only a singular Boolean variable is certainly counter-satisfiable. Applying this rule does not change the validity status of the problem:

**Theorem 4.2.1** (Soundness of Universal Reduction). Let  $S$  be a skolemized HOL formula. Then  $S$  is valid if and only if  $S'$  is. Where  $S'$  is  $S$  after applying  $\mathcal{R}(UR)$  to a clause  $C \in S$ .

*Proof.* Let  $C'$  be the result of applying the rule to  $C$  and  $l = [x_o]^\alpha \in C$  the removed literal. We have that  $C' \subset C$ .

Assume  $S$  is counter-satisfiable. Then there is an interpretation  $\mathcal{M}$  and a variable assignment  $\sigma$  such that  $\|S\|^{\mathcal{M},\sigma} = \mathbb{F}$ . Since  $S$  is of the form  $D_1 \wedge \dots \wedge D_n \wedge C \wedge D_{n+1} \wedge \dots \wedge D_m$ , we either have that  $\|C\|^{\mathcal{M},\sigma} = \mathbb{F}$ , or that  $\|C\|^{\mathcal{M},\sigma} = \mathbb{T}$ . In the first case,  $\|C'\|^{\mathcal{M},\sigma} = \mathbb{F}$  since all terms in  $C$  are false and therefore so are the terms in  $C'$ . In the second case, some other clause  $D$  in  $S$  and  $S'$  is false and the status of  $C'$  does not matter. We can conclude, that interpretation and variable assignment also falsifies  $S'$ .

Assume  $S'$  is counter-satisfiable and again let  $\mathcal{M}$  and  $\sigma$  be the falsifying interpretation and variable assignment. The only non-trivial case is if  $C'$  is the only clause falsified by this assignment and  $\|l\|^{\mathcal{M},\sigma} = \mathbb{T}$ . We can define:

$$\sigma' : v \mapsto \begin{cases} \mathbb{F}, & \text{if } v = x_o \text{ and } \sigma(x_o) = \mathbb{T} \\ \mathbb{T}, & \text{if } v = x_o \text{ and } \sigma(x_o) = \mathbb{F} \\ \sigma(v), & \text{otherwise.} \end{cases}$$

Then, by definition  $\|l\|^{\mathcal{M},\sigma'} = \mathbb{F}$  and, since  $x_o$  does not appear free in any other term of  $C$ , we also get  $\|C\|^{\mathcal{M},\sigma'} = \mathbb{F}$ . This flip might also change to truth value of other clauses in  $C$  too, but this does not change the conclusion that  $s$  is also counter-satisfiable.  $\square$

Listing 2 is a pseudo code implementation of the universal reduction rule. The polarity ( $\text{pol}(l)$ ) and variable ( $\text{var}(l)$ ) of HOL literals is defined analogously to the QBF case.

### 4.3 Constant Extraction

Two simple syntactic rules which are applied in QBF and DQBF preprocessing are the detection and elimination of pure and unit literals.

```

1 function UniversalReduction( $M$ )
   Input: A set  $M$  of skolemized clauses.
   Output: A set  $R$  of clauses, where some contain fewer literals.
2    $R \leftarrow \emptyset$ 
3   foreach  $C \in M$  do
4      $C' \leftarrow C$ 
5      $B \leftarrow \{l \mid l \in C, l \text{ is either } x_o^i \text{ or } \neg x_o^i.\}$ 
6      $P \leftarrow \{(\text{var}(l), \text{pol}(l)) \mid l \in B.\}$ 
7     foreach  $l \in B$  do
8       if  $(\text{var}(l), \neg \text{pol}(l)) \in P$  then
9         // Clause is tautological and can be removed.
          Continue in line 3.
10      else
11         $F \leftarrow \cup_{l' \in C, l' \neq l} \mathcal{F}_{l'}$ 
12        if  $\text{var}(l) \notin F$  then
13           $C' \leftarrow C' - l$ 
14       $R \leftarrow R \cup \{C'\}$ 
15 return  $R$ 

```

Listing 2: Universal Reduction for HOL.

A pure literal is a literal which only occurs in one polarity in the whole problem. Since existential pure literals can be replaced by the true constant, all clauses containing pure  $\exists$ -literals can be removed. Pure  $\forall$ -literals, on the other hand, can be removed from the clauses they appear in.

A literal  $l$  is a unit literal, if there is a clause which contains  $l$  and no other literals. Hence, unit literals are literals which occur alone in a clause. Accordingly, clauses which contain only one literal are called *unit clauses*. The variable in a unit  $\exists$ -clause can be replaced by a constant and problems containing unit  $\forall$ -literals are unsatisfiable.

As described in section 4.1 backbone and monotonic variables are a more general and semantic variant of unit and pure literals.

#### PURE LITERAL DETECTION VIA UNIFICATION TESTS IS UNSOUND

A first attempt of implementing pure literal detection for HOL might be along the following lines:

Given an input formula  $s_o$  in clause form and a literal  $l$  appearing in this

problem. Iterate over all clauses  $C$  and literals  $l$  in  $C$ . Continue if  $l = l'$ . Otherwise, check if  $\text{pol}(l) = \neg \text{pol}(l')$ . If this is the case  $l$  is not pure, nor is  $l'$ . Finally, check if  $l$  and  $l'$  are unifiable. Since unification for HOL is undecidable, this step must be an over approximation. If the two literals are possibly unifiable, neither  $l$  nor  $l'$  are pure. If no counterexample for pureness is found,  $l$  is pure and can be added as a unit clause.

This procedure, however, is unsound in the presence of equality. Consider for example the problem  $(f_{o \rightarrow o} = \lambda x. \neg x) \wedge (f_{o \rightarrow o} (g_{l \rightarrow o} a)) \wedge (g_{l \rightarrow o} a \vee x_o)$ . In this case,  $g_{l \rightarrow o} a$  is not unifiable with any literal in the first two clauses and therefore could be added as a unit clause. This results in  $(f_{o \rightarrow o} = \lambda x. \neg x) \wedge (f_{o \rightarrow o} (g_{l \rightarrow o} a)) \wedge (g_{l \rightarrow o} a \vee x_o) \wedge (g_{l \rightarrow o} a)$ . Note that the original problem is satisfiable by setting  $x_o = \mathbb{T}$  and  $g_{l \rightarrow o} a = \mathbb{F}$ . The new one, however, is no longer satisfiable, since  $(\lambda x. \neg x) (g_{l \rightarrow o} a)$   $\beta$ -reduces to  $\neg(g_{l \rightarrow o} a)$ .

Overall, this approach fails because HOL formulas contain equality symbols which carry semantical meaning without being explicitly axiomatized in the input problem. Furthermore, the arbitrary nesting of function symbols and operators is an additional challenge which must be handled by a constant extraction algorithm.

### 4.3.1 SAT Based Constant Extraction

As discussed in section 4.1 Wimmer, Gitina, et al. (2015) mention SAT solving as a procedure for detecting backbone and monotonic variables. Such a procedure was described by Pigorsch and Scholl (2010). We will first describe their algorithm, before describing our adaption to HOL.

Given a QBF formula  $t$  in CNF. That is,  $t$  has the form  $Q_1 x_1 \dots Q_n x_n. s$  where  $s$  is a conjunction of clauses. If for any literal  $l$  it holds that  $s \rightarrow l$ , then  $Q_1 x_1 \dots Q_n x_n. s \wedge l$  is equivalent to  $t$ . In this case  $l$  is a *constant literal*. Since variables which are existentially quantified in the QBF problem  $t$  are implicitly universally quantified in  $s \rightarrow l$  this procedure is only approximative. The validity of  $s \rightarrow l$  is equivalent to the unsatisfiability of  $s \wedge \neg l$ . Hence, a simple procedure to search for constant literals in a problem with  $n$  variables could make  $2 \times n$  calls to a SAT solver to test for unsatisfiability. One call for each propositional variable and one call for the negated variable.

This can be optimized. Should a problem handed to the SAT solver be satisfiable – which will be the case for all literals which are not constant – the solver returns a model for the formula. This model can be used to avoid future checks. Given a model for  $s \wedge l_i$  which assigns  $\mathbb{T}$  to the literal  $l_j$  ( $i \neq j$ ), then this model will also be a model for  $s \wedge l_j$ . Hence, the algorithm can be improved by maintaining a queue of literals yet to be checked. If a generated

problem is satisfiable with a model which makes a literal  $l$  true the literal  $\bar{l}$  can be removed from the queue.

Furthermore, some capabilities of modern SAT solvers can be exploited. Before doing the unsatisfiability checks, a model for  $s$  by itself can be computed. After removing literals from the queue based on this model as described before, the SAT solver can be introduced to try the yet unused value first when guessing assignments for variables. This increases the chance to remove literals from the queue after the first check. Lastly, many recent SAT solvers support incremental solving, which allows for the addition and removal of temporary assumptions without the necessity to restart the whole reasoning process. All those optimizations have been implemented in our adaption of the algorithm.

#### ADAPTING SAT BASED CONSTANT EXTRACTION TO HOL

Our adaption to HOL is quite straightforward. In a first step we map all HOL literals to SAT literals, ensuring that syntactically equal HOL terms get the same literal assigned. Then we invoke the SAT solver as described above.

This will result in a set of constant SAT literals, which then can be mapped back to HOL literals and added to the original input formula. To capture some relations between equality terms we add some transitivity constraints to the generated SAT problem. Before we describe this procedure, we give a more formal definition of the translation to SAT:

**Definition 4.3.1** (SAT Literal Mapping). A SAT literal mapping is a function  $[\cdot]$  from a set of HOL terms to propositional literals, such that:

1. If  $[S]$  is defined for a HOL term  $S$ , then so is  $[\neg S]$  and  $[\overline{S}] = [\neg S]$ .
2. If  $[S = T]$  is defined for HOL terms  $S$  and  $T$ , then so is  $[S \neq T]$  and  $[S = T] = [\overline{S \neq T}]$ .
3. If  $[S = T]$  is defined, then so is  $[T = S]$  and  $[T = S] = [S = T]$ .
4. If  $[S \neq T]$  is defined, then so is  $[T \neq S]$  and  $[T \neq S] = [S \neq T]$ .

Overall, we require that negation behaves as expected, and that the order of terms in equations does not matter. The first requirement can be implemented by maintaining a directory which maps HOL terms to SAT variables and by appropriately removing and adding negations. The second requirement can be fulfilled by ordering the terms consistently before translation. This can be done by any total order. We choose a simple lexicographic order on terms.

We can now describe the translation from HOL into SAT:

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**Definition 4.3.2** (SAT Mapping of Formulas). Given a SAT literal mapping  $\llbracket \cdot \rrbracket$  and a HOL formula  $S$  in clause form. I.e.  $S$  is of the form  $C_1 \wedge C_2 \wedge \dots \wedge C_n$ , where  $C_i$  are clauses and the explicit quantifiers have been removed by Skolemization. Then the SAT mapping  $\langle S \rangle$  is  $\{\llbracket l \rrbracket \mid l \in C_1\} \wedge \dots \wedge \{\llbracket l \rrbracket \mid l \in C_n\}$ .

Note that the mapping removes all the quantifiers from the input formula. This SAT mapping has the desired property:

**Theorem 4.3.1** (Soundness of SAT Mapping). Given a HOL formula  $S$  of the form as described above and a SAT literal mapping  $\llbracket \cdot \rrbracket$ . If  $\langle S \rangle \rightarrow \llbracket l \rrbracket$  is valid for an arbitrary literal  $l$ , then  $S$  and  $S \wedge l$  are satisfiability equivalent.

To prove this, we first prove this lemma:

**Lemma 4.3.2.** Given a SAT literal mapping  $\llbracket \cdot \rrbracket$  and a HOL formula  $S$  in clause form. Then if  $S$  is satisfiable if and only if  $\langle S \rangle$  is satisfiable.

*Proof.* Assume that  $S$  is satisfiable and let  $\mathcal{M}$  be an interpretation and  $\sigma$  a variable assignment such that  $\|S\|^{\mathcal{M},\sigma} = \mathbb{T}$ .

We use this interpretation and assignment to construct a model  $\mathbb{I}$  for  $\langle S \rangle$ . Let  $\mathbb{I}$  be the mapping from propositional variables to truth values, such that  $\mathbb{I}(x_i) = \|l\|^{\mathcal{M},\sigma}$  if  $\llbracket l \rrbracket$  is a variable  $x_i$  and  $\|x_i\|^{\mathbb{I}} = \neg\|l\|^{\mathcal{M},\sigma}$  if  $\llbracket l \rrbracket$  is of the form  $\neg x_i$ . Since  $\llbracket \neg l \rrbracket = \neg\llbracket l \rrbracket$  this is well-defined.

For each clause  $C \in S$ , there is a literal  $l$  such that  $\|l\|^{\mathcal{M},\sigma} = \mathbb{T}$ . By definition  $\|\llbracket l \rrbracket\|^{\mathbb{I}} = \mathbb{T}$  and hence for each propositional clause there is a mapped literal which the valuation  $\|\cdot\|^{\mathbb{I}}$  is true. Therefore,  $\|\langle S \rangle\|^{\mathbb{I}} = \mathbb{T}$ .

The second statement directly follows from the first one by negation.  $\square$

We can now proof theorem 4.3.1:

*Proof.* First note that  $\langle S \rangle \rightarrow \llbracket l \rrbracket$  is equivalent to  $\neg(\langle S \rangle \wedge \neg\llbracket l \rrbracket)$  and, by the definition of the mappings, to  $\neg\langle S \wedge \neg l \rangle$ . Therefore,  $\langle S \rangle \rightarrow \llbracket l \rrbracket$  is valid if and only if  $\langle S \wedge \neg l \rangle$  is unsatisfiable and by lemma 4.3.2 the validity of  $\langle S \rangle \rightarrow \llbracket l \rrbracket$  implies unsatisfiability of  $S \wedge \neg l$ .

Assume  $S$  is satisfiable, but  $S \wedge l$  is not. Hence, we know that for all interpretation  $\mathcal{M}$  and a variable assignment  $\sigma$  such that  $\|S\|^{\mathcal{M},\sigma} = \mathbb{T}$  we have that  $\|l\|^{\mathcal{M},\sigma} = \mathbb{F}$ . This implies that  $\|\neg l\|^{\mathcal{M},\sigma} = \mathbb{T}$  and therefore  $\|S \wedge \neg l\|^{\mathcal{M},\sigma} = \mathbb{T}$ . Since at least one such interpretation exists this is a contradiction to the remark above.

The other direction is trivial, since all subsets of a satisfiable set of clauses are satisfiable.  $\square$

### ADDING TRANSITIVITY CONSTRAINTS

The translation procedure just described disregards most of the structure of the formula. Only the basic propositional structure is retained. We now describe a simple procedure to add some additional structural information to the generated SAT problem.

Some literals in the generated SAT problem encode equalities between terms. The equality relation is transitive. Consider for example the formulas  $s = t$  and  $t = u$ , then the transitivity of equality implies that  $s = u$  holds. That is  $s = t \wedge t = u \rightarrow s = u$  is a valid HOL formula. Since equality is implicitly handled by the Leo-III calculus, those clauses do not appear in the clause set during reasoning. They, however, can be added to the generated SAT problem as additional clauses.

A naïve procedure to do so would be to first collect all SAT literals which encode equalities an equality set  $\mathcal{E}$  and then add all possible transitivity constraints to the SAT problems. The generated transitivity constraints would be formulas of form

$$\lfloor S_1 = S_2 \rfloor \wedge \lfloor S_2 = S_3 \rfloor \wedge \cdots \wedge \lfloor S_{k-1} = S_k \rfloor \rightarrow \lfloor S_1 = S_k \rfloor$$

where  $\lfloor S_i = S_j \rfloor \in \mathcal{E}$ . Those formulas can be translated to equivalent clauses which can be added to the problem:

$$\neg \lfloor S_1 = S_2 \rfloor \vee \neg \lfloor S_2 = S_3 \rfloor \vee \cdots \vee \neg \lfloor S_{k-1} = S_k \rfloor \vee \lfloor S_1 = S_k \rfloor.$$

This naïve approach, however, leaves much to be desired. Given the unrestricted length of the constraints they might have an arbitrary, even infinite, length. A more goal-oriented approach is needed for this problem. Efficient ways to create transitivity constraints have been proposed by Bryant and Velev (2000) which we adapted to the problem at hand and implemented.

In a first step, the set of equalities is represented as an undirected graph  $G = (V, E)$ . The set of nodes  $V$  is the set of all HOL terms<sup>3</sup>, and  $\{S, T\} \in E$  if  $\lfloor S = T \rfloor \in \mathcal{E}$ .

The authors then show that the set of all transitivity constraints is satisfied by those interpretations which satisfy the transitivity constraints induced by the chord-free cycles in the graph. A chord free cycle is a cycle  $[v_1, v_2, \dots, v_k, v_1]$  where  $\{v_i, v_j\} \notin E$  for any  $j \neq (i+1)$ , except  $i = k$  and  $j = 1$ . The transitivity constraint induced by such a cycle is  $\lfloor v_1 = v_2 \rfloor \wedge \cdots \wedge \lfloor v_{k-1} = v_k \rfloor \rightarrow \lfloor v_1 =$

<sup>3</sup> Since the procedures by Bryant and Velev (2000) consider literals which encode equalities abstractly, their set of nodes is a bounded set of natural numbers.

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$v_k]$ . Unfortunately, the number of chord-free cycles can be exponential in the number of edges in the graph.

By adding additional derived equality literals this can be avoided. To do so, the graph must be made *chordal*. A graph is chordal, if no cycle with more than three nodes is chord-free. Making a graph chordal by adding a minimal amount of additional edges is NP-complete, but good heuristic solutions exist. We implemented the heuristic presented by Bryant and Velev (2000).

The heuristic starts with  $G_0 = G$  and iteratively constructs  $G_i = (V_i, E_i)$  from  $G_{i-1}$  by removing a node from  $G_{i-1}$  and adding edges. If in step  $i$  the node  $v_i$  is removed, edges are added to  $G_i$  for each two nodes  $j, k$  where  $\{j, v_i\} \in E_{i-1}$ ,  $\{k, v_i\} \in E_{i-1}$ , but  $\{j, k\} \notin E_{i-1}$ . This procedure is iterated until all nodes have been deleted. The final graph is  $G$  plus all edges created during the procedure.

### THE ENTIRE ALGORITHM

We now give a pseudo code description of the complete algorithm. The presentation is split into two parts. Listing 3 shows the overall skeleton of the algorithm, while listing 4 contains the generation of the transitivity constraints from an equality graph.

The algorithm uses a mapping from tuples of terms to propositional variables. This mapping allows the representation of both equational and ordinary literals. A tuple  $(l, j)$  represents the equation  $l = j$ . To represent ordinary literals their partner is set to  $\mathbb{T}$ . Hence, the tuple  $(l, \mathbb{T})$  is used to represent  $l = \mathbb{T}$ , which is equivalent to just  $l$ . In fact, the algorithm will generate literals of the form  $[l_o = \mathbb{T}]^\alpha$  instead of  $[l_o]^\alpha$ . These two forms are equivalent and the first form is very similar to the internal representation used in Leo-III, while also simplifying the pseudo code.

We assume that the mapping from term tuples to propositional variables reorders the terms before requesting the variable. Hence,  $M(a, b) = M(b, a)$ . This removes the necessity to handle terms of the form  $S = T$  and  $T = S$  differently. Any total order can be used.

Assuming a literal in the SAT solver means adding this literal to the SAT problem as a until clause for the next call to solve. The SAT solver then automatically removes the assumed literal after finishing solving the problem<sup>4</sup>. Furthermore, asking the SAT solver for a model returns the set of SAT *literals* set to  $\mathbb{T}$ .

Line 29 in listing 3 uses a shorthand notation for expressing the polarity of a HOL literal. Here  $[l]^\mathbb{T}$  means  $[l]^{tt}$  and  $[l]^\mathbb{F}$  means  $[l]^{ff}$ .

<sup>4</sup> This is exactly the behavior of *picosat*, which was used in our implementation.

```

1 function ConstantExtraction( $M$ )
   Input: A set  $M$  of skolemized clauses.
   Output: A set  $U$  of new unit clauses.
2    $M \leftarrow$  empty map from term tuples to propositional variables.
3    $S \leftarrow$  new incremental SAT solver instance.
4    $E \leftarrow$  new equality graph.
5   foreach  $C \in M$  do
6      $C' \leftarrow \emptyset$ 
7     foreach  $[l]^\alpha \in C$  do
8       if  $l$  is of the form  $s = t$  then
9          $T \leftarrow (s, t)$ 
10        Add nodes  $s, t$  and the edge  $\{s, t\}$  to  $E$ .
11       else
12          $T \leftarrow (l, \mathbb{T})$ 
13          $V \leftarrow M(T)$ 
14         if  $V$  not defined then
15            $V \leftarrow$  fresh SAT variable.
16            $M(T) \leftarrow V$ 
17         Add  $V$  to  $C'$  if  $\alpha = tt$  and  $\neg V$  otherwise.
18       Add  $C'$  to SAT solver  $S$ .
19   Add TransitivityConstraints( $E, M$ ) to  $S$ .
20   if Solve( $S$ )=UNSAT then
21     return Input is contradictory.
22    $L \leftarrow \{\bar{l} \mid l \in \text{Model}(S)\}$ 
23   Introduce  $S$  to try  $\bar{l}$  first for  $l \in \text{Model}(S)$ .
24   while  $L \neq \emptyset$  do
25      $l \leftarrow$  an element from  $L$ .
26     Assume  $l$  in  $S$ .
27     if Solve( $S$ )=UNSAT then
28        $(s, t) \leftarrow M^{-1}(\text{var}(l))$ 
29        $N \leftarrow [s = t]^{-\text{pol}(l)}$ 
30       if  $\{N\} \notin M$  then
31          $U \leftarrow U \cup \{N\}$ 
32     else
33        $L \leftarrow L \setminus \text{Model}(S)$ 
34   return  $U$ 

```

Listing 3: SAT Based Constant Extraction.

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The last missing piece is the generation of the transitivity constraints given in listing 4. As described above the graph is first made chordal. Our presentation is simplified and will add edges already in the graph. Since a chordal graph has no chord-free cycles of length greater than three, enumerating the chord free cycles is trivial.

```
1 function TransitivityConstraints( $E, M$ )
   Input: An equality graph  $E = (v, e)$ , a mapping  $M$  from term tuples to
           propositional variables.
   Output: A set  $T$  of new propositional clauses.
2    $v' \leftarrow v$ 
3   while  $v' \neq \emptyset$  do
4      $n \leftarrow$  node from  $v'$ 
5      $v' \leftarrow v' - n$ 
6      $e \leftarrow e \cup \{\{s, t\} \mid \{n, s\} \in e, \{n, t\} \in e\}$ 
7   return  $\{M(a, b), M(a, c), \neg M(c, d) \mid \{a, b\} \in e, \{a, c\} \in e, \{c, d\} \in e\}$ 
```

Listing 4: Generate Transitivity Constraints.

### 4.4 Blocked Clause Elimination

We now discuss Blocked Clause Elimination (BCE) for HOL. Blocked Clause Elimination is an efficient SAT preprocessing technique which was also successfully applied to QBF (as QBCE) and DQBF solving. A description of BCE and QBCE can be found in section 4.1.

Recently BCE was lifted to first-order logic by Kiesl et al. (2017). When doing so, equality requires specialized handling. Therefore, FOL clauses can either be *blocked* or *equality-blocked*. Every equality-blocked clause is also a blocked clause, but some blocked clauses are not equality blocked. For input problems which do not contain equalities general first-order BCE is applied, while for problems containing equalities, only equality-blocked clauses can be eliminated.

We lifted ordinary blocked clause elimination to HOL and implemented the resulting algorithm. The theoretical adaption from FOL is straightforward. The main additional challenge is the undecidability of unification in the case of HOL. Adapting the implementation is a bit more tricky, since indexes which track unification partners must be adapted.

In the next subsection we will describe ordinary and equality blocked clause

elimination for FOL based on Kiesel et al. 2017. Subsection 4.4.2 then describe the specialties emerging in the HOL case.

#### 4.4.1 First-order Blocked Clause Elimination

Remember that a DQBF clause is blocked if there is a blocking literal in the clause. A literal  $l$  is a blocking literal, if all resolvents on  $l$  contain complementary literals  $k$  and  $\bar{k}$  with  $D_k \subseteq D_l$ .

##### BLOCKED CLAUSES WITHOUT EQUALITY

As mentioned above, BCE for FOL differs if equalities are present. We first assume that the FOL formula under consideration does not contain any equalities.

In the case of FOL, resolution involves unification. This translates to the definition of blocked clauses.

**Definition 4.4.1** (*l-resolvent*). Given two clauses  $C = l \vee C'$  and  $D = n_1 \vee \dots \vee n_i \vee D'$  with  $i > 0$ , such that  $l, \bar{n}_1, \dots, \bar{n}_i$  are unifiable by an MGU  $\sigma$ . Then  $C'[\sigma] \vee D'[\sigma]$  is the *l-resolvent* of  $C$  and  $D$ .

**Definition 4.4.2** (*Blocked Clause*). A clause  $C$  is *blocked* by a literal  $l \in C$  in a FOL formula  $F$  if all *l-resolvents* of  $C$  with clauses in  $F \setminus \{C\}$  are valid.

A *l-resolvent*  $R$  without equality is valid if and only if there is a literal  $k$  such that  $\{k, \bar{k}\} \subseteq R$ . Again blocked clauses can be removed from the problem without changing its satisfiability status.

Checking the validity of *l-resolvents* without further optimizations would require checking an exponential number of literal pairs per clause. This, however, can be reduced to a polynomial number of checks by using the algorithm described in listing 5.

Two further optimizations exist. First a index, such as a hash map, can be used to map literal to clause-literal pairs. A literal  $l$  is mapped to a pair  $(l', C)$  where  $l'$  has opposite polarity of  $l$ ,  $l' \in C$ , and the predicate<sup>5</sup> of  $l$  and  $l'$  are the same. This index can be used to quickly retrieve all unification candidates. Furthermore, a queue of clause literal pairs is maintained. These represent clauses with potential blocking literals. The procedure first dequeues a clause-literal pair  $(C, l)$  from the queue and retrieves all resolution candidates  $D$ . Then the testing function is called. If the *l-resolvent* is found to be countersatisfiable,

<sup>5</sup> In FOL predicates are the symbols which take terms to propositions. For example, in the proposition  $P(a, f(b)) \wedge R(c)$  the symbols  $P$  and  $R$  are predicates. They correspond to functions of type  $\tau \rightarrow o$  in the HOL world.

```

1 function TestLResolvValid( $C, l, D, N$ )
   Input: Clause  $C = l \vee C'$  and  $D = N \vee D'$ , where  $\bar{l}$  and  $n$  are unifiable for
           all  $n \in N$ .
   Output:  $\mathbb{T}$  if all  $l$ -resolvents of  $C$  and  $D$  are valid,  $\mathbb{F}$  otherwise.
2   foreach  $n \in N$  do
3      $T \leftarrow \{n\}$ 
4     while  $l$  is unifiable with all literals in  $\bar{T} := \{\bar{t} \mid t \in T\}$  do
5        $\sigma \leftarrow$  MGU of  $l$  and all literals in  $\bar{T}$ 
6        $K \leftarrow$  all pairs of complementary literals in  $C'[\sigma] \vee (D \setminus T)[\sigma]$ .
7       if  $K = \emptyset$  then
8         return  $\mathbb{F}$ 
9       if All pairs in  $K$  contain a literal  $n[\sigma]$  with  $n \in N$  then
10         $T \leftarrow T \cup \{n \mid n[\sigma] \text{ is part of a pair in } K\}$ 
11      else
12        break the while loop

```

Listing 5: Testing the Validity of  $l$ -resolvents.

the clause-literal pair  $(C, l)$  is deactivated and saved as being deactivated by  $D$ . Should, on a later point,  $D$  be found to be blocked and therefore be removed, then  $(C, l)$  is re-enqueued.

We adapted this algorithm to HOL. First, however, we discuss BCE with equalities for FOL.

#### EQUALITY BLOCKED CLAUSES

When equality is added as a native symbol, blocked clause elimination becomes unsound. Consider the following example taken from Kiesl et al. (2017). Let  $C := P(a)$  and  $F := (a = b \wedge \neg P(b))$ . In this example,  $P(a)$  and  $P(b)$  are not unifiable. Hence,  $C$  is blocked. However,  $F$  alone is satisfiable while  $(P(a) \wedge a = b \wedge P(b))$  is not.

To solve this problem more clauses must be taken into account during the test. The algorithm must not only test clauses which are unifiable with the clause under consideration, but it must consider all those clauses which have a literal with the same head symbol and opposite polarity.

This is implemented by *flattening* a literal:

**Definition 4.4.3** (Flattening). Given a clause  $C = L(t_1, \dots, t_n) \vee C'$ . Flattening the literal  $L(t_1, \dots, t_n)$  in  $C$  yields the clause:  $\tilde{C} = \bigvee_{1 \leq i \leq n} x_i \neq t_i \vee$

$L(x_1, \dots, x_n) \vee C'$

The literal  $L(x_1, \dots, x_n)$  in  $\tilde{C}$  is the *flattened literal*.

Such a flattened clause is equivalent to  $\bigvee_{1 \leq i \leq n} x_i = t_i \rightarrow L(x_1, \dots, x_n) \vee C'$ . Hence, flattening clauses in a problem does not change the satisfiability status of the problem. We can define the concept of flat resolvents based on flattening of clauses. Flat resolvents are resolvents generated by resolving between flattened clauses.

**Definition 4.4.4** (Flat  $l$ -resolvent). Given two clauses  $C = l \vee C'$  and  $D = n_1 \vee \dots \vee n_i \vee D'$  with  $i \geq 1$ , and such that  $l, \bar{n}_1, \dots, \bar{n}_i$  have the same predicate symbol and polarity. Let  $\tilde{C}$  and  $\tilde{D}$  be the result of flattening the literals  $l, n_1, \dots, n_i$  in  $C$  and  $D$  and  $\tilde{l}, \tilde{n}_1, \dots, \tilde{n}_i$  are the flattened literals.

The resolvent

$$(\tilde{C} \setminus \{\tilde{l}\})[\sigma] \vee (\tilde{D} \setminus \{\tilde{n}_1, \dots, \tilde{n}_i\})[\sigma]$$

of  $\tilde{C}$  and  $\tilde{D}$ , with  $\sigma$  being an mgu of  $\tilde{l}, \tilde{n}_1, \dots, \tilde{n}_i$ , is a *flat  $l$ -resolvent* of  $C$  and  $D$ .

Finding a mgu for flattened clauses becomes trivial. For the flat literals  $L(x_1, \dots, x_n), \bar{N}_2(y_{11}, \dots, y_{1n}), \dots, \bar{N}_i(y_{i1}, \dots, y_{in})$  the substitution  $\sigma : y_{ij} \mapsto x_j$  is already a mgu.

As in the case of FOL without equality, a clause  $C$  is equality blocked if all flat  $l$ -resolvents for a literal  $l \in C$  are valid:

**Definition 4.4.5** (Equality Blocked). A clause  $C$  is *equality blocked* by a literal  $l \in C$  in a FOL formula  $F$  if the predicate of  $l$  is not  $=$  and all flat  $l$ -resolvents of  $C$  with clauses in  $F \setminus \{C\}$  are valid.

Equality blocked clauses are redundant and can be removed from the formula without changing its satisfiability status. Checking validity in the presence of equality is more complex. E.g. clauses containing literals such as  $x = x$  are already valid.

To detect the validity of flat  $l$ -resolvents a congruence closure algorithm can be used. A flat  $l$ -resolvent  $R$  is valid if and only if the negation of its universal closure  $\neg \forall R$  is unsatisfiable. Applying distributivity to the negation will result in an existentially quantified conjunction of negated literals. Skolemization will then replace the free variables of  $R$  with fresh constants. Hence,  $\neg \forall R$  is equivalent to conjunction of ground equational literals.

The satisfiability of a conjunction of equalities between ground terms can be efficiently decided by congruence closure algorithms (see Shostak 1978). Kiesel

et al. (2017) note, that a complete congruence closure algorithm was too inefficient and they instead used a simplified version. First they ignored all equalities which were present in the clauses before flattening, secondly they only applied the equalities once, instead of recursively applying the congruence rule.

#### 4.4.2 Higher-order Blocked Clause Elimination

We now discuss the adaption of ordinary blocked clause elimination to HOL. Therefore, we restrict ourselves to HOL problems without equality.

When lifting BCE to HOL, the main challenge is the undecidability of unification. This can be solved by restricting the used (blocking) literals to pattern literals. As described on page 20, unification between pattern literals is always decidable and if the literals are unifiable there is exactly one MGU. The definition of blocked clause, however, speaks about all resolvents. Hence, some additional care has to be taken to ensure the correct handling of literals which are not patterns. To do so we first introduce a few helpful terms.

**Definition 4.4.6** (Head Symbol). Given a HOL formula  $S_\tau$  which has been  $\beta$ -normalized.  $S_\tau$  is of the form  $\lambda x_{\tau_1}^1. \dots \lambda x_{\tau_i}^i. t \dots$  where  $i \geq 0$  (there might be no  $\lambda$ -binders) and  $t$  is either a constant, a locally bound variable, or a free variable. The symbol  $t$  is the *head symbol* of  $S_\tau$ .

Since we are interested in handling literals, we work with formulas (terms of type  $o$ ) which have being  $\beta$ -reduced as far as possible.

The head symbol can either be a *flex* or *rigid* head. Those terms are originally used in the context of higher-order unification. See subsection 2.3.1 for a description of higher-order unification.

**Definition 4.4.7** (Flex Head). Assume  $S_\tau$  is a HOL term, then  $S_\tau$  has a *flex head* if the head symbol of  $S_\tau$  is a free variable.

**Definition 4.4.8** (Rigid Head). Assume  $S_\tau$  is a HOL term, then  $S_\tau$  has a *rigid head* if it does not have a flex head.

Two terms with different rigid heads will never be unifiable. Hence, this can be used as a simple, but incomplete, check for non-unifiability.

We now need to adapt the concept of an  $l$ -resolvent and an blocked clauses based on this definition. We restrict blocking literals to pattern literals. A unification test between a pattern and a non-pattern literal might still be non-terminating. After restricting the (potential) blocking literals  $l$  to pattern literals the following cases can occur when testing unification with another literal  $m$ :

1. The literal  $m$  is a pattern literal. Then unification is decidable and we proceed as in the FOL case.
2. The head of  $l$  is rigid and  $m$  is not a pattern literal:
  - a) The head symbol of  $m$  is rigid and different from the head symbol of  $l$ . Then  $l$  and  $m$  are not unifiable.
  - b) In all other cases  $l$  and  $m$  might be unifiable.
3. The head of  $l$  is flex and  $m$  is not a pattern literal. Then  $l$  and  $m$  might be unifiable.

The cases 2b and 3 are the critical ones. In those cases we must assume the worst case. Furthermore, it is not enough to consider literals of opposite polarity to describe valid  $l$ -resolvents, since switching the polarity by adding an additional negation will still result in unifiable literals. Consider for example the two clauses  $[f a]^t$  and  $[x a]^t$  where  $f$  and  $a$  are constant symbols and  $x$  is a free function variable. Then this problem is equivalent to  $[\neg(f a)]^f$  and  $[x a]^t$ . Now the unification problem  $(\neg(f a)) = (x a)$  can be solved by the substitution which maps  $x$  to  $\lambda y. \neg(f y)$  and the empty clause can be derived by resolution.

Such a transformation is only possible when one of the literals has a flex head. In this situation the negation symbol can be added to the rigid headed literal. When both literals have a rigid head, they are only unifiable if the head is the same and adding the negation literal would change this.

To capture this discussion we first define  $l$ -unification constraints.

**Definition 4.4.9** ( $l$ -unification constraints). Given literals  $[l]^\alpha, [n_1]^{\beta_1}, \dots, [n_i]^{\beta_i}$  and a set of equalities  $U$  with  $i$  elements. Then  $U$  is a  $l$ -unification constraint if for all  $i$  it holds that  $l = n_i \in U$  if  $\beta_i \neq \alpha$  and either  $(\neg l) = n_i \in U$  or  $l = (\neg n_i) \in U$  if  $\beta_i = \alpha$ .

Now we can define  $l$ -resolvents based on this definition.

**Definition 4.4.10** ( $l$ -resolvent). Let  $C = [l]^\alpha \vee C'$  and  $D = [n_1]^{\beta_1} \vee \dots \vee [n_i]^{\beta_i} \vee D'$  be two clauses and  $U$  be a  $l$ -unification constraint on  $l, n_1, \dots, n_i$ . Furthermore, assume that  $U$  is solvable by a MGU  $\sigma$ . Then  $C'[\sigma] \vee D'[\sigma]$  is a  $l$ -resolvent of  $C$  and  $D$ .

The definitions so far did not capture the decidability of the unification problem. We do this in our definition of blocked clauses.

**Definition 4.4.11** (Pattern Blocked Clause). A clause  $C$  is *pattern blocked* by a literal  $l \in C$  in a HOL formula  $F$  if:

- $l$  is a pattern literal.
- None of the clauses in  $F - C$  contains a literal which is not a pattern and has a flex head.
- All non-pattern literals in the clauses of  $F - C$  have a different head symbol than  $l$ .
- All  $l$ -resolvents with clauses of  $F - C$  are valid.

The first three conditions in definition 4.4.11 ensure that deciding if a clause is pattern blocked by a literal is always decidable. Those three conditions can easily be checked and if they are fulfilled all non-pattern literals can be excluded from unification checks, since they will never be unifiable with  $l$ .

We conjecture that pattern blocked clauses can be removed from HOL problems without changing its satisfiability status.

#### AN ALGORITHM FOR PATTERN BLOCKED CLAUSE ELIMINATION

We now describe an algorithm to find pattern blocked clauses in HOL problems. This algorithm corresponds to our implementation in Leo-III and is an adaption of the algorithm described by Kiesl et al. (2017)

The algorithm maintains four data structures. A pattern index  $P$  is used to retrieve potential pattern unification candidates. Secondly, a dictionary  $N$  maps head symbols together with their polarity to rigid non-patterns with the same polarity. While testing clauses and potential blocking literals, two further data structures are used. A priority queue  $Q$  collects clause literal pairs  $(C, l)$ , where  $l \in C$  and  $l$  is a pattern literal. Finally, a deactivation index  $D$  is used to track clauses which prohibit clause literal pairs from being blocked.

Since the pattern index must handle flex headed literals differently from rigid headed pattern literals it internally uses one map and two sets. The dictionary  $P.rigidIndex$  is similar to the non-pattern dictionary  $N$  and maps head symbols and polarity to rigid patterns with the same polarity. The two sets  $P.rigid$  and  $P.flex$  collect all clause pattern literal pairs where the head of the literal is rigid and flex, respectively.

Listing 6 describes how fresh clause literal pairs are inserted into the pattern index. This is done by a simple case distinction on the type of head literal in the added clause literal pair has.

```

1 function AddToIndex( $P, C, l$ )
  Input: A pattern index  $P$ , a clause  $C$ , and a pattern literal  $l \in C$ 
2    $h \leftarrow$  head of  $l$ .
3    $p \leftarrow$  polarity of  $l$ .
4   if  $l$  is a flex literal then
5      $Q.flex \leftarrow Q.flex \cup \{(C, l)\}$ 
6   else
7      $Q.rigid \leftarrow Q.rigid \cup \{(C, l)\}$ 
8      $Q.rigidIndex((p, h)) \leftarrow Q.rigidIndex((p, h)) \cup \{(C, l)\}$ 

```

Listing 6: Adding a clause literal pair to the pattern index.

The method described in listing 7 is used to query the pattern index for the possible resolution candidates. The candidates are grouped by clause to enable a fast retrieval of all potential candidates in any clause. If the query literal has a flex head, all saved literals are returned.

```

1 function QueryPatternIndex( $P, l$ )
  Input: A pattern index  $P$  and a pattern literal  $l \in C$ 
  Output: A set of tuple  $(C, L)$  where  $C$  is a clause and  $L \subseteq C$ , such that
           the elements of  $L$  are potentially unifiable with  $l$ .
2    $h \leftarrow$  head of  $l$ .
3    $p \leftarrow$  polarity of  $l$ .
4   if  $l$  is a flex literal then
5      $S \leftarrow P.flex \cup P.rigid$ 
6   else
7      $S \leftarrow P.flex \cup P.rigidIndex((\neg p, h))$ 
8    $D \leftarrow \{c \mid (c, \_) \in S\}$ 
9   return  $\{(C, L) \mid C \in D, L = \{l \mid (C, l) \in S\}\}$ 

```

Listing 7: Retrieving potential resolution candidates from the pattern index.

The deactivation index  $D$  must maintain two dictionaries. One to save which clauses are certain clause deactivates and another one to remember by which clauses a clause is deactivated.

The priority queue  $Q$  orders clause literal pairs by the number of possible resolution candidates returned by querying the pattern index.

We now present the outer loop of the pattern blocked clause elimination algorithm. The pseudo code is split into two parts. Listing 8 describes the first

#### 4 Preprocessing Techniques

iteration over the input problem to initialize the data structures, while listing 9 is the test loop.

```

1 function PBCE( $M$ )
   Input: A equality free set  $M$  of skolemized clauses.
   Output: A potentially smaller set of clauses.
2    $P \leftarrow$  empty pattern index.  $N \leftarrow$  empty dictionary of non-patterns.
3    $\phi \leftarrow \emptyset$ 
4   foreach  $C \in M$  do
5     foreach  $l \in C$  do
6       if  $l$  is a pattern then
7         AddToIndex( $P, C, l$ )
8          $\phi \leftarrow \phi \cup \{(C, l)\}$ 
9       else
10         $p \leftarrow$  polarity of  $l$ .  $h \leftarrow$  head of  $l$ .
11        if  $l$  is rigid. then
12           $N((p, h)) \leftarrow N((p, h)) \cup \{(C, l)\}$ 
13        else
14          if  $f$  is defined then
15             $\lfloor$  return Input contains two flex non-patterns. Aborting BCE.
16           $f \leftarrow (C, l)$ 
17    $D \leftarrow$  empty deactivation index.  $Q \leftarrow$  empty priority queue.
18   if  $f$  is defined and is  $(C, \_)$ . then
19     Add all pairs  $(C, l)$  with  $l \in C$  and  $l$  is a pattern to  $Q$ .
20     Mark all pairs  $(C', l')$  with  $C' \neq C$  in  $\phi$  as deactivated by  $C$ .
21   else
22     foreach  $(C, l) \in \phi$  do
23        $p \leftarrow$  polarity of  $l$ .  $h \leftarrow$  head of  $l$ .
24        $r \leftarrow N((p, l))$  if  $l$  is rigid,  $N((p, l)) \cup N((-p, l))$  otherwise.
25       if  $r = \emptyset$  then
26         Enqueue  $(C, l)$ .
27       else
28          $\lfloor$  Mark  $(C, l)$  as deactivated by the clauses in  $r$ .

```

Listing 8: Initializing the data structures for pattern blocked clause elimination.

```

28
29  $B \leftarrow \emptyset$ 
30 while  $Q$  not empty do
31    $(C, l) \leftarrow$  dequeue from  $Q$ .
32    $S \leftarrow$  QueryPatternIndex( $Q, l$ ).
33    $b \leftarrow \top$ 
34   foreach  $(C', L) \in S$  do
35     if  $\neg$ ValidOrNotRes( $C, l, C', L$ ) then
36        $b \leftarrow \mathbb{F}$ 
37       break
38   if  $b = \top$  then
39     // Clause is blocked!
40      $B \leftarrow B \cup \{C\}$ 
41     Add clauses literal pairs deactivated by  $C$  to  $Q$ .
42   else
43     Mark  $(C, l)$  as deactivated by  $C'$ .
44 return  $M \setminus B$ 

```

Listing 9: Main loop of pattern blocked clause elimination.

The last missing piece is the check for validity of the  $l$ -resolvents. Since our main loop only collects resolution candidates, the algorithm is slightly more complex. Nevertheless, it again is an adaption of the algorithm for  $\text{FOI}$  which we reproduced in listing 5. To capture the notion of  $l$ -unification constraints, we introduce an additional set  $P$  which first creates the appropriate unification problems and removes those constraints which are by themselves not solvable. Hence, after the first loop, we got rid of all unification candidates which are not actually unifiable.

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```

1 function ValidOrNotRes( $C, l, D, L$ )
   Input: A clause  $C$ , a pattern literal  $l \in C$ , a second clause  $D$ , and a set of
           literals  $L \subseteq D$ 
   Output:  $\mathbb{T}$  if all  $l$ -resolvents on  $C$  with  $D$  are valid.
2    $P \leftarrow \emptyset$  // For unification constraints  $a = b$  where  $a$  is  $l$  or  $\neg l$ .
3   foreach  $l' \in L$  do
4     if The polarity of  $l$  is different from the polarity of  $l'$  then
5       |  $\rho \leftarrow (l = l')$ 
6     else
7       | // Given the overall algorithm, either  $l$  or  $l'$  must have
8         | a flex head at this point!
9       | if  $l$  has a flex head then
10      | |  $\rho \leftarrow (l = (\neg l'))$ 
11      | else
12      | |  $\rho \leftarrow ((\neg l) = l')$ 
13      | if  $\rho$  is solvable (the pair is unifiable) then
14      | |  $P \leftarrow P \cup \{\rho\}$ 
15    foreach  $\rho \in P$  do
16      |  $T \leftarrow \{\rho\}$ 
17      | while  $T$  is solvable with MGU  $\sigma$  do
18      | |  $N' \leftarrow$  the literals from  $L$  which are present in the constraints in  $T$ .
19      | |  $K \leftarrow (C - l)[\sigma] \vee (D \setminus N')[\sigma]$ 
20      | |  $C \leftarrow \{\{t, t'\} \mid t = \bar{t}, t \in K, t' \in K\}$ 
21      | | if  $C \neq \emptyset$  then //  $C$  is valid.
22      | | | if for all  $p \in C$  there is a  $n \in L$  such that  $n[\sigma] \in p$  or  $(\neg n)[\sigma] \in p$ 
23      | | | then
24      | | | |  $T \leftarrow T \cup \{n \mid n \in L, \exists p \in C \text{ s.t. } n[\sigma] \in p \text{ or } (\neg n)[\sigma] \in p\}$ 
25      | | | else
26      | | | | break The while loop.
27      | | else
28      | | | return  $\mathbb{F}$ 
29    return  $\mathbb{T}$ 

```

Listing 10: Check if all  $l$ -resolvents for a clause pair are valid.

## 4.5 First-order Re-encoding

The finale preprocessing technique we discuss is not an adaption of any QBF or DQBF preprocessing technique. Instead, it adapts the encoding of QBFs into FOL to delay costly primitive substitution operations. Such an encoding was presented by Seidl, Lonsing, and Biere (2012) together with a tool which performs this encoding.

In fact, the tool translates input QBF problems into Effectively Propositional Logic (EPR). The target logic is a decidable fragment of classical FOL and while QBF is PSPACE-complete, deciding EPR formulas is NEXPTIME-complete. Hence, EPR is more expressive than QBF and as expressive as DQBF.

A FOL formula is in EPR if it has the following form:

$$\exists x_1. \dots \exists x_k. \forall y_1. \dots \forall y_l. \bigwedge_{i=0}^n \bigvee_{j=0}^{m_i} t_{ij}$$

where the literals  $t_{ij}$  contain no function symbols.

The translation into EPR is done in two steps. We are interested in the first step which translates QBF formulas into first-order predicate logic. The second step then would modify the result of the first step to ensure that the resulting problem has the structure as described above.

A translation function  $\llbracket \cdot \rrbracket_p$  is used to convert QBF formulas to FOL formulas:

$$\begin{aligned} \llbracket \exists \mathbf{x}_i. \phi \rrbracket_p &= \exists x_i. \llbracket \phi \rrbracket_p & \llbracket \forall \mathbf{x}_i. \phi \rrbracket_p &= \forall x_i. \llbracket \phi \rrbracket_p \\ \llbracket \phi \vee \psi \rrbracket_p &= \llbracket \phi \rrbracket_p \vee \llbracket \psi \rrbracket_p & \llbracket \phi \wedge \psi \rrbracket_p &= \llbracket \phi \rrbracket_p \wedge \llbracket \psi \rrbracket_p \\ \llbracket \mathbf{x}_i \rrbracket_p &= p(x_i) & \llbracket \neg \mathbf{x}_i \rrbracket_p &= \neg p(x_i) \end{aligned}$$

With  $p$  being a new predicate symbol. Furthermore, two dedicated unary function symbols (constants)  $\top$  and  $\perp$  are added. This leads to the lemma:

**Lemma 4.5.1.** Let  $\phi$  be a QBF and  $p$  a unary predicate symbol. Then  $\phi$  is satisfiable if and only if the FOL formula  $\llbracket \phi \rrbracket_p \wedge p(\top) \wedge \neg p(\perp)$  is (see Seidl, Lonsing, and Biere 2012).

Intuitively, the predicate  $p$  is used to capture the behavior of QBF variables. This is expressed by the two additional unit clauses. In some sense, it encodes the properties of the Boolean type. Since HOL natively supports Boolean variables, this additional wrapping is not needed when translating from QBF to HOL.

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Recall the description of primitive substitution on page 2.3.1. In the proof calculi used by LEO-II and Leo-III primitive substitution is only applied to the top-level. Hence, when directly translating QBF to HOL primitive substitution will guess Boolean assignments to variables and will guess functions for the generated Skolem functions.

Our procedure wraps literals into a fresh predicate  $p$ , just as the translation function  $\llbracket \cdot \rrbracket_p$  does. The procedure is parameterized by a Boolean value  $e$  which indicates whether equalities should be wrapped into the new predicate too. Listing II contains the pseudo code for the procedure. It first iterates over the matrix and wraps the literals and then adds two unit clauses  $\{[p(\top)]^{\#}\}$  and  $\{[p(\perp)]^{\#}\}$ <sup>6</sup>.

```

1 function FirstOrderReEncoding( $M, e$ )
   Input: A clasified HOL problem  $M$  and a Boolean value  $e$ .
   Output: A modified matrix.
2    $M' \leftarrow \emptyset$ 
3    $p \leftarrow$  a fresh function symbol of type  $o \rightarrow o$ .
4   foreach  $C \in M$  do
5      $C' \leftarrow \emptyset$ 
6     foreach  $l \in C$  do
7       if  $l$  is of the form  $[t_o]^\alpha$  then //  $l$  is not an equalization
         literal.
8         |  $C' \leftarrow C' \cup \{[(p\ t_o)]^\alpha\}$ 
9       else
10        //  $l$  is of the form  $[s_\tau = t_\tau]^\alpha$ .
11        if  $e = \mathbb{T}$  then
12          |  $C' \leftarrow C' \cup \{[p(s_\tau = t_\tau)]^\alpha\}$ 
13        else
14          |  $C' \leftarrow C' \cup \{l\}$ 
15     $M' \leftarrow M' \cup \{C'\}$ 
16   $M' \leftarrow M' \cup \{[p(\top)]^{\#}, [p(\perp)]^{\#}\}$ 
  return  $M'$ 

```

Listing II: First-order Re-Encoding.

Given that the fresh symbol  $p_{o \rightarrow o}$  is forced by the two additional unit clauses

<sup>6</sup> The two symbols  $\top$  and  $\perp$  are commonly used in HOL to represent truth constants. They are defined as  $\llbracket \top \rrbracket^{\mathcal{M}, \sigma} = \mathbb{T}$  and  $\llbracket \perp \rrbracket^{\mathcal{M}, \sigma} = \mathbb{F}$  for an interpretations  $\mathcal{M}$  and variable assignments  $\sigma$ .

to behave exactly like the identity function  $\lambda x_o. x_o$ , it is trivial to see, that this procedure does not change the satisfiability status of the input problem.

Resolving with the two added clauses also removes the wrapping after one step. Consider the following example:

$$\frac{\frac{\frac{[p(x_o)]^{tt} \quad [p(\perp)]^{ff}}{[p(x_o) = p(\perp)]^{ff}} \mathcal{R}(Res)}{[x_o = \perp]^{ff}} \mathcal{R}(DeComp)}{\underline{\underline{[x_o]^{tt}}}}$$

Resolving with the unit clause creates a unification constraint between two application of the  $p_{o \rightarrow o}$  function. Applying one of the pre-unification rules results in an equality between the wrapped term and a constant, which after some simplifications steps leads to the unwrapped term.



# 5 Implementation

We implemented the preprocessing techniques proposed in chapter 4 in the Leo-III system. In this chapter, we discuss various practical aspects related to our implementation. In section 5.1 we start the discussion with an outline of the questions which arose while implementing the techniques and our answers to these questions. To generate suitable benchmarking problems we developed tools to convert QBF and DQBF problems to HOL. A description and manual of those tools can be found in section 5.2. Finally, section 5.3 describes the benchmarking results.

## 5.1 Aspects of the Implementation

Overall our implementation of the preprocessing algorithms is very similar to the pseudo code given in chapter 4. Therefore, we will not reproduce the final source code here, but instead discuss various aspects of the implementation.

As mentioned in subsection 2.3.2, Leo-III is implemented in the Scala programming language. The Leo-III homepage<sup>1</sup> provides information and downloads<sup>2</sup> related to the project. The documentation included with Leo-III provides an introduction how the prover can be compiled on a Linux system.

Leo-III collects the various available rules in a *Control* object. We opted to implement our preprocessing technique as such rules. Each rule is implemented as a Scala object which implements the `CalculusRule` trait to ensure that the rule can be present in the generated proof output. The trait is simple:

```
1 trait CalculusRule {
2   def name: String
3   def inferenceStatus: SuccessSZS
4 }
```

The `name` attribute is a human readable name of the rule and the `inferenceStatus` attribute represents the inference status of the rule. Its values are taken

<sup>1</sup> <http://page.mi.fu-berlin.de/lex/leo3/>

<sup>2</sup> At the time of writing, version 1.1 of Leo-III was publicly available. This version did not include our additions.

## 5 Implementation

from the *szs* ontology<sup>3</sup>. The *szs* ontology by Sutcliffe, Zimmer, and Schulz (2003) provides a systematic collection of status values for logical data and relationships between the values. For our rules the `SZS_EquiSatisfiable` status is used.

Finally, each rule object must have a method which is used by the control object to apply the rule. This is not abstracted into an interface and differs from rule to rule. In our case we implemented one method per technique which takes a set of causes as input and returns a set of clauses. In the case of universal reduction, this set contains all the original clauses after removing the removable literals. Constant extraction, on the other hand, returns a new set of unit clauses which must be added to the clause set. Blocked clause elimination returns a subset of the input clauses and first-order re-encoding also returns the input set, where some clauses are modified and two additional clauses have been added.

The preprocessing rules can be activated and parameterized by command line arguments handed to Leo-III. The arguments are:

- `ure_activate` to activate universal reduction.
- `sce_activate` to activate SAT based constant extraction.
- `bce_activate` to activate blocked clause elimination.
- `fre_activate` to activate first-order re-encoding and `fre_dontWrapEqs` to deactivate wrapping of equality literals.

Since SAT based constant extraction might produce unit clauses consisting of a singular universally quantified variable which universal reduction can then eliminate to generate the empty clause, we run SAT based constant extraction before universal reduction. Furthermore, blocked clause elimination might remove the clauses introduced by SAT based constant extraction and hence must be run first. Overall, the techniques are executed during the preprocessing phase of Leo-III in the following order:

1. Blocked clause elimination,
2. SAT based constant extraction,
3. Universal reduction,
4. First-order re-encoding.

<sup>3</sup> A list of potential *szs* states is available online at <http://www.cs.miami.edu/~tptp/cgi-bin/SeeTPTP?Category=Documents&File=SZSontology>

Executing those «complex» preprocessing methods is done after all the other preprocessing steps, especially clausification and skolemization are completed and right before the main reasoning loop starts.

Implementing universal reduction and first-order re-encoding was straightforward and our implementation corresponds to the pseudo code. Blocked clause elimination and SAT based constant extraction is a bit more complex.

To implement blocked clause elimination, we developed a `PatternIndex` class which implements the pattern index data structure as described in subsection 4.4.2. Furthermore, a class `Deactivations` is used to keep track of deactivated clauses. Internally it used two hash maps. One map is used to map clause literal pairs to the clauses that deactivate them and one is used to map clauses to the clause literal pairs that are deactivated by them.

Our implementation of SAT based constant extraction uses an ad-hoc graph implementation to represent the equality graph. Edges are represented in an adjacency lists. Since edges are only used when making the graph chordal and during enumeration of the transitivity constraints, this representation works well. Furthermore, SAT based constant extraction utilizes the external SAT solver `PicoSAT`.

### 5.1.1 Bindings for PicoSAT

The `PicoSAT` system by Biere (2008) is a small SAT solver, which can be used as a standalone tool, or as a library. The latest release of `PicoSAT` is available on the `PicoSAT` homepage<sup>4</sup>.

While `PicoSAT` is not competitive with recent SAT solvers<sup>5</sup>, its well documented interface and decent performance made it a good choice for the implementation of SAT based constant extraction. Furthermore, the system supports two important features. On one hand, iterative solving is supported. This means, that additional clauses can be added as assumptions after running the solver without invalidating the old intermediate data structures. In the case of SAT based constant extraction, the individual literals can be temporarily assumed. On the other hand, `PicoSAT` also supports the generation of *unsatisfiability cores*. In the case of an unsatisfiability problem, this feature allows the generation of subsection of clauses which are unsatisfiable by themselves. For SAT based constant extraction, this allows the detection which original clauses imply the constant literal. Note that the generated cores are not minimal. Hence,

<sup>4</sup> <http://fmv.jku.at/picosat/>

<sup>5</sup> For example, `PicoSAT` did not participate in the SAT competition 2016. Results are available here: <http://baldur.iti.kit.edu/sat-competition-2016/index.php?cat=results>

there might be a real subset of clauses which is still unsatisfiable. The *picosat* system comes with a standalone tool to compute minimal unsatisfiability cores, this feature, however, is not present in the API, but is also not necessary for our implementation.

The API of *picosat* is completely defined in a single header file `picosat.h`. Every function in this file is commented and this also serves as the main documentation of the *picosat* API. To facilitate the usage of *picosat* in Leo-III, we implemented Java Native Interface (JNI) bindings. This allows calls to *picosat* directly from Leo-III without invoking *picosat* as an external command. A documentation for JNI is available on the Java web page<sup>6</sup>.

Our bindings mirror the functions of the `picosat.h` file and only provide minimal abstraction around them. The *picosat* API allows the creation of multiple solver instances which can be used independently. We represent this in our API too. It is unclear if *picosat* is thread save and we suggest limiting the usage of each *picosat* solver instance to one thread. Furthermore, unsatisfiability core generation has a bigger memory footprint, since prove traces must be stored. Therefore, our API allows the deactivation of unsatisfiability core generation for each solver instance.

The following code gives a short example of the API:

```
1 val solver = PicoSAT(true)
2 solver.addClause(List(-1, -3, -2))
3 solver.addClause(List(-2, -3, -1))
4 solver.addClause(List(3))
5 assert(solver.solve() == PicoSAT.SAT)
6 assert(context.getAssignment(1) == Some(false))
```

In the first line a new solver instance with activated core generation is requested. Then three clauses are added. Literals are expressed as integers. A positive integer  $n$  stands for the propositional literal  $x_n$  and a negative integer  $-n$  for  $\neg x_n$ . Finally, the solver is called and returns that satisfiability state of the problem. Now the generated model of the problem can be queried. In this case  $\mathbb{F}$  must be assigned to  $x_1$ .

## 5.2 From QBF to HOL: QBFTOys

To appropriately benchmark the preprocessing techniques we can use the problems present in the TPTP problem library. We, however, based our preprocessing techniques on QBF preprocessing and hence hoped evaluating their

<sup>6</sup> <http://docs.oracle.com/javase/8/docs/technotes/guides/jni/index.html>

performance on QBF and DQBF problems would give valuable insights. Unfortunately the problems we generated this way were too hard for Leo-III and ultimately unusable. See subsection 5.3.1 for a discussion of this problem.

We implemented tools which perform the translations from QBF and DQBF to HOL as described in section 3.1 and 3.3. The tools are available on GitHub<sup>7</sup>.

## BUILDING THE TOOLS

Both tools are written in Haskell. While there are multiple ways to build the tools, using the *Stack* build tool<sup>8</sup> is strongly recommended by us and documented here. To install Stack follow the instructions on the Stack homepage.

The `qbfTool` folder contains the source code for the QBF to HOL converter, and the DQBF tool is in the `dqbfTool` folder. After the installation of Stack is complete, open a terminal and navigate to either folder. Then run the following commands, where `{tool}` is either `qbfTool`, or `dqbfTool`:

```
1 > stack setup
2 > stack build
3 > stack exec {tool}
```

The `stack build` command outputs the path the resulting binary is placed in. On Linux calling `stack install` will copy the binary to `~/.local/bin`. Alternatively, the programs can be run with additional arguments (`{args}`) by calling

```
1 > stack exec {tool} -- {args}
```

## CONVERTING QBF PROBLEMS

The QDIMACS format is widely used to represent QBF problems in CNF. It is an extension of the DIMACS format used for propositional problems and QDIMACS is compatible with DIMACS. Every valid DIMACS file is also a valid QDIMACS file. Variables in the QDIMACS format are expressed by non-zero natural numbers and literals are expressed by non-zero integers. The variable index of a literal is the absolute value of the integer, the polarity is represented by the sign of the integer. The following is a small QDIMACS file:

```
1 p cnf 3 3
2 c an example problem
```

<sup>7</sup> <https://github.com/hansjoergschurr/QBFTOys>

<sup>8</sup> Available at: <https://haskellstack.org>

## 5 Implementation

```
3 a 1 2
4 e 3
5 -1 -3 -2 0
6 -2 -3 -1 0
7 3 0
```

The first line starting with `p` gives meta information about the problem. The first number is the highest variable appearing in the problem and the second number denotes the number of clauses. Then lists of literals are given line by line and terminated by a 0. The first few lines represent the quantifier prefix and start with a letter. The letter `e` is used for existential quantification and `a` for universal quantification. Then the clauses are given one per line. Comment lines start with `c`.

A formal grammar of the QDIMACS format can be found online<sup>9</sup>. This grammar forbids empty clauses and files without any clauses. We found that BLOQQER sometimes does generate problems with empty clauses and completely empty problems. Hence, our tool tries to be very liberal when parsing QDIMACS problems. It especially ignores the metadata given in the first line.

The QBF2EPR tool by Seidl, Lonsing, and Biere (2012), which translates QBFS to EPR also allows the translation into HOL and supports TPTP output. This feature of the tool, however, is implemented in Python and is too slow on bigger input problems. Furthermore, input problems which did not follow the QDIMACS standard result in crashes.

The default behavior of `qbfTool` is to accept a QDIMACS problem on the standard input and to output a TPTP problem to standard output. Assuming a file `test.qdimacs` which contains the following QDIMACS problem:

```
1 p cnf 6 3
2 e 1 0
3 a 0
4 e 2 0
5 a 3 4 0
6 e 5 6 0
7 1 3 5 0
8 1 2 0
9 2 4 6 0
10 99 0
```

This problem is not a valid QDIMACS file. The meta values are wrong and a quantifier line contains an empty list of variables. Executing `qbfTool` on this

<sup>9</sup> <http://www.qbflib.org/qdimacs.html>

problem results in the following output:

```
1 > cat test.qdimacs | stack exec qbfTool
2 thf(c,conjecture,(? [X1: $o,X2: $o,X3: $o]:(! [X4: $o,X5:
  → $o]:(? [X6: $o,X7:
  → $o]:((X1|X4|X6)&(X1|X3)&(X3|X5|X7)&(X2)))))) .
```

This output is not a direct translation of the input problem. The `qbfTool` also applies some normalization steps. Those are:

- Consecutive lines containing the same quantifier are merged.
- Empty quantifiers are removed and the surrounding quantifiers are merged.
- Variables are renamed by the order they appear in the quantifier. This also assures that all variables are used without gap.
- Variables which are used in the matrix, but which do not appear in the quantifier prefix, are added to the outermost quantifier if the outermost quantifier is an existential quantifier. Otherwise, an existential quantifier containing those variables is added.
- If the input problem does not contain any clauses, the clause  $x \vee \neg x$  is added.
- If the input problem contains the empty clause, a clause  $x$  and a clause  $\neg x$  is added.

If the tool is called with the command line parameter `-n`, the output is a valid QDIMACS problem resulting from these transformations instead of a TPTP problem. For the QDIMACS problem given above this output is:

```
1 p cnf 7 4
2 e 1 2 3 0
3 a 4 5 0
4 e 6 7 0
5 1 4 6 0
6 1 3 0
7 3 5 7 0
8 2 0
```

## 5 Implementation

As described above, free variables are *implicitly* existentially quantified in the input problem and *explicitly* existentially quantified in the output problem. Hence, the input QBF problem is satisfiable if the output problem is valid. A resolution prover will negate the input problem and show its unsatisfiability. Negating a problem in clause normal form and clausifying the negated problem requires repeated application of the distributive laws and is therefore an expensive operation. Furthermore, a prover is geared towards showing validity and will seldom deduce that an input problem is not valid. Hence, the `-i` command line parameter will instruct `qbfTool` to add a negation in front of the output problem. The resulting problem will be a theorem if and only if the input problem is unsatisfiable.

Finally, the `-o filename` parameter can be used to instruct `qbfTool` to write the output into a file. If this parameter is used, `qbfTool` will output exactly one line to the standard output. This line will contain the number of variables and number of clause in the problem separated by a space.

### CONVERTING DQBF PROBLEMS

The `dqbfTool` generates HOL problems in the TPTP format from DQBF problems in the *bunsat* format. This format has been defined for the *bunsat* tool by Finkbeiner and Tentrup (2014) which is an incomplete unsatisfiability checker for DQBF. This format also supports input problems which are not in CNF.

As an alternative to the *bunsat* format, the DQDIMACS format exists. This format is an extension of the QDIMACS format and was developed by Fröhlich et al. (2014) for the *idQ* system. Like QDIMACS every valid (Q)DIMACS file is also a valid DQDIMACS file. Unlike the *bunsat* format DQDIMACS only supports problems in CNF.

The following example of the *bunsat* format was taken from the *bunsat* web page<sup>10</sup>. This page also contains an incomplete grammar of the format. We used the source code of *bunsat* tools published on the web page to «reverse engineer» the missing pieces of the grammar.

```
1 A x1 x2: E{x1} y1 : E{x2} y2: ((~y1) | y2) <--> (x1 ^ x2)
```

Problems in *bunsat* formats start with the universal quantifier denoted by an **A**, follow with the list of universal variables. Then, after a colon, follows a list of existentially quantified variables each of the form `E{x1 ... xn} yi` where the variables in the curly brackets are the dependency set of the variable `yi`. Then, after another colon, follows a Boolean formula. The format supports

<sup>10</sup> <https://www.react.uni-saarland.de/tools/bunsat/>

multiple synonymous names for the operators. To express conjunction the  $\&$  operator can be used and  $\wedge$  denotes the exclusive-or.

The `dqbfTool` behaves the same way as the `qbfTool` behaves, but does not apply any normalizations to the input problem. Hence, the `-n` command line parameter is not supported. The `-i` parameter adds a negation symbol to the output and the `-o` parameter writes the output into a file. If the `-o` parameter is given, the tool does not write any metadata to the standard output.

## 5.3 Benchmarking

We now discuss the results of benchmarking our preprocessing techniques on various problems. This section starts with a description of the benchmarking problems we intended to use. In accordance to the previously discussed logics, this set of problems is threefold. We prepared QBF, DQBF, and HOL problems for benchmarking.

The QBF dataset is based on the *eval2010* set of problems. This set of QBF problems was used for the QBFEVAL'10 competition<sup>11</sup>. It consists of problems selected from the *QBFlib* database<sup>12</sup> of QBF problems and additional contributed problems. The dataset<sup>13</sup> contains a total of 568 problems in the QDIMACS format. Some of these problems are satisfiable, some of them are not. Before translating the problems to HOL problems, we classified them using the *DepQBF* QBF solver. Problems which could not be solved by *DepQBF* were discarded, there is not much hope that problems too hard for a specialized QBF solver can be solved by a HOL theorem prover. Overall this process<sup>14</sup> resulted in 116 satisfiable problems and 201 unsatisfiable problems. Hence, 317 problems were selected. In a second step we converted all problems to HOL problems in the TPTP format. Since the `qbfTool` adds explicit existential quantification, the unsatisfiable problems were negated. Hence, all resulting problems are theorems<sup>15</sup>.

Furthermore, we downloaded the DQBF benchmarks published by the *bunsat* authors<sup>16</sup>. According to Finkbeiner and Tentrup (2014), these problems have

<sup>11</sup> see: [http://www.qbflib.org/event\\_page.php?year=2010](http://www.qbflib.org/event_page.php?year=2010)

<sup>12</sup> The library is available online at <http://www.qbflib.org/>.

<sup>13</sup> The version used for our benchmarks were downloaded from the web page of the QBF GALLERY 2013. This was a non-competitive evaluation of QBF systems held in 2013 and 2014. The results and benchmark downloads, including the used QBFEVAL'10 problem set, are available at <http://www.kr.tuwien.ac.at/events/qbfgallery2013/results.html>.

<sup>14</sup> We used *DepQBF* version 6.02 in its default configuration with a timeout of 30 seconds on a shared computer with two Intel Xeon E5430 quad core CPUs and 32GB RAM.

<sup>15</sup> Assuming that *DepQBF* and our conversation tool is sound.

<sup>16</sup> available at: <https://www.react.uni-saarland.de/tools/bunsat/>

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been generated by taking a Boolean circuit, replacing parts of the circuit with «black boxes» and then taking the original circuit as specifications for the resulting partial circuit. The so generated DQBF problem is satisfiable if the «black boxes» can be filled with Boolean circuits to construct a circuit with the same behavior as the specification circuit. Since *bunsat* is an *unsatisfiability* checker, exactly one random gate was replaced with another gate to make the problem unsatisfiable. Hence, when converting the problems with the `dqbfTool`, we negated the problem to generate a theorem. The input dataset contains problems generated from four circuits: A 32-bit adder, a 32-bit lookahead arbiter implementation, a 32-bit multiplexer, and a 4-bit multiplier. Into this circuit one, three, five, seven, or nine black boxes were inserted. For each of this twenty classes, 100 problems are part of the problem set. We sampled 25 problems from each class to get 500 DQBF problems in total.

Finally, we selected the set of TPTP problems which were eligible for the CASC-J8 HOL track<sup>17</sup>. This is a set of 743 HOL problems from the TPTP library. Unfortunately, as we describe in the next section, we encountered difficulties with the QBF and DQBF problem sets during the empirical evaluation and were ultimately unable to use it.

### 5.3.1 Empirical Results

We ran various empirical experiments on the above described problem sets. The experiments were run on a shared Debian Linux server with two Intel Xeon E5430 quad core CPUs and 32GB RAM running version 3.6. of the Linux kernel.

We used a Python script to automatize the tests. The script consumes a list of input problems and runs a copy of Leo-III on each problem one after another. It also matches the log output of Leo-III against multiple regular expressions which are used to extract values from the log. For example the expression «`^\% URE Time (\d+(\.d*)?)`» extracts a decimal value from the log. In this case an expression was inserted into Leo-III source code which printed the runtime of universal reduction. Furthermore, if performance parameters were measured, Leo-III was forced to abort as soon as the parameter in question was calculated. This means, that if a value could not be measured because of a timeout, preprocessing was not even finished.

For these experiments our preprocessing techniques were added to Leo-III version 1.1. All experiments were run with a timeout of 60 seconds. Our

<sup>17</sup> The list of eligible problems is available at <http://www.cs.miami.edu/~tptp/CASC/J8/EligibleProblems.html>.

	Problem Name	Variables	Clauses
sat	toilet_g_04_01.2-shuffled	22	52
	toilet_g_02_01.2-shuffled	12	22
unsat	lut4_XOR_f0R-shuffled	43	168
	toilet_c_04_01.4-shuffled	58	185
	z4ml.blif_0.10_1.00_0_0_out_exact-shuffled	63	194
	toilet_c_02_01.2-shuffled	17	37
	toilet_a_02_01.2-shuffled	18	39

Table 5.1: Feasible QBF problems.

script waits 30 seconds longer before forcibly killing Leo-III. Since the internal timeout procedure of Leo-III did not work reliably before the prover finished its preprocessing part, this often resulted in an effective timeout of 90 seconds. Leo-III was run in sequential mode.

#### THE INFEASIBILITY OF SOME PROBLEMS

During our first experiments with the problem sets generated by translating QBF and DQBF problems we encountered a challenge: In many instances the prover would not even reach the point where our preprocessing procedures are executed before the timeout is reached. This means that parsing, classification, and build-in preprocessing techniques already took longer than the set timeout.

We decided to not use the DQBF problem set for further experiments and to select those problems from the QBF problem set, where the internal preprocessing steps were finished before the timeout.

The result of this test showed how hard this problem domain is for HOL theorem provers. Of the 317 problems, only seven problems passed minimal processing before the timeout was reached. Two problems were satisfiable, and five problems were counter satisfiable. Table 5.1 shows the remaining problems and the number of distinct variables and clauses appearing in those problems.

We included these seven problems into every one of the following experiments. This resulted in 750 problems in total.

The experiments were conducted in two rounds. In the first round, we applied the algorithms to all input problems and recorded the number of problems Leo-III was able to solve. In the second round, we measured several key

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metrics, such as the runtime, for each algorithm. Leo-III was aborted after recording these measurements.

### IMPACT ON THE NUMBER OF SOLVED PROBLEMS

To gauge the impact of the preprocessing techniques on the number of problems Leo-III is able to solve, we ran the prover on the dataset described above with multiple configurations.

Of the 750 theorems, Leo-III was able to prove

- 475 without preprocessing (None),
- 475 with universal reduction (URE),
- 477 with SAT based constant extraction (SCE),
- 454 with blocked clause elimination (BCE),
- 418 with first-order re-encoding (Fo-Re), and
- 458 with all techniques except first-order re-encoding (Mult.).

The shorthand noted in brackets is used in the following tables to reference the configurations. The last configuration was selected after preliminary experiments showed that first-order re-encoding has a negative impact on prover performance.

Overall, only SAT based constant extraction enabled the prover to solve more problems. Each algorithm, however, made some problems unsolvable, and some other problems solvable. Hence, we calculated the number of problems which were solved by one configuration compared to another one.

In table 5.2 each number denotes the number of problems which were solved by the configuration in the column, but not by the configuration in the row. For example, Leo-III with universal reduction was able to solve seven problems which were not solved by Leo-III with SAT based constant extraction.

This shows, that every technique was able to solve some problems which were not solved by Leo-III without any additional preprocessing. Furthermore, each technique was able to solve appropriately 60 more problems than Leo-III with first-order re-encoding. Hence, this technique is not very promising.

Only one problem (SEU717<sup>18</sup>) was solved by the configuration *Mult.* and none of the three specific preprocessing techniques alone, nor by Leo-III with-

<sup>18</sup> This problem is from the set theory domain and contains the laws of typed sets. It is available at <http://www.cs.miami.edu/~tptp/cgi-bin/SeeTPTP?Category=Problems&Domain=SEU&File=SEU717^1.p>

		Solved					
		None	Mult.	URE	Fo-Re	SCE	BCE
Not Solved	None	0	11	7	7	10	7
	Mult.	28	0	24	25	27	9
	URE	7	7	0	7	9	8
	Fo-Re	64	65	64	0	65	58
	SCE	8	8	7	6	0	7
	BCE	28	13	29	22	30	0

Table 5.2: Problems solved by various preprocessing techniques.

out preprocessing. Since the success on this singular problem might be the result of fluctuations in available computing resources, this result indicates that the techniques do not profit from one another.

#### UNIVERSAL REDUCTION

In the case of universal reduction, we measured the runtime of the algorithm and the number of removed literals.

The runtime was collected for 739 problems. Hence, for 11 problems preprocessing was not finished before the timeout was reached. The mean of the runtime was 32.71 ms, the minimal runtime was 7.9 ms and the maximal runtime was 713.88 ms. Figure 5.1 shows the runtime distribution of universal reduction relative to the number of clauses arriving on the algorithm. Since Leo-III performs some processing during clausification, this number might not correspond to the number of clauses in the input problem. Overall, the performance of this algorithm is acceptable.

Universal reduction was able to remove literals from 184 problems. This includes the two satisfiable QBF problems from which universal reduction could remove 4201<sup>19</sup> and 50<sup>20</sup> literals. On the HOL problems, universal reduction could remove between one and seven literals. However, those problems were all in the domain of set theory (SEU) and software verification (SWV).

Overall, this together with table 5.2 shows that universal reduction is only useful for certain instances.

<sup>19</sup> from the toilet\_g\_04\_01.2-shuffled problem.

<sup>20</sup> from the toilet\_g\_02\_01.2-shuffled problem.

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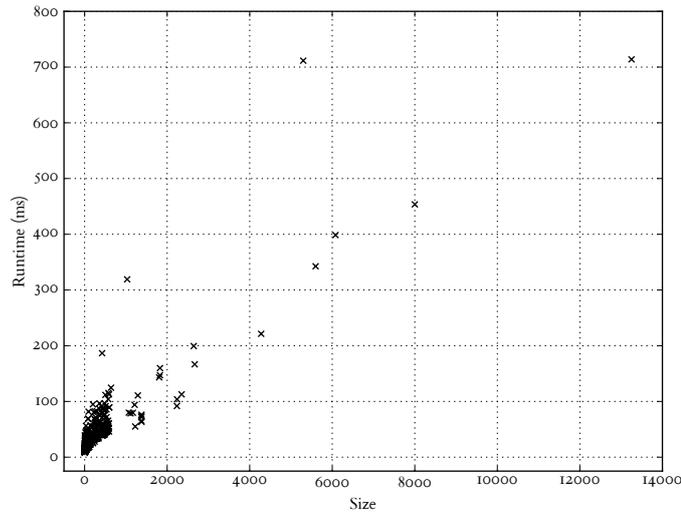


Figure 5.1: Runtime of universal reduction.

### SAT BASED CONSTANT EXTRACTION

As metrics for SAT based constant extraction we used the number of propositional clauses and literals, as reported by *picosat*, both before adding transitivity constraints and after. We also saved the number of SAT calls and, naturally, the number of added unit clauses.

To compare this algorithm with the other preprocessing algorithms, we calculated the runtime relative to the number of clauses. Figure 5.2 is a scatter plot of the runtime. Overall, the algorithm ran between 102.3 ms and 2231.02 ms with a mean runtime of 232.78 ms and a median runtime of 149.33 ms. The SAT solver was called 40.79 times on average, but because of a single problem with 1250 calls, the median number is only eleven calls.

Unit clauses were added to 76 problems distributed over 8 of the 25 HOL domains and to the QBF problems. For 38 problems one unit clause was added, two unit clauses were added to 7 problems, and three unit clauses were added to 18 problems. 13 problems were augmented with more than three unit clauses.

Additional transitivity constraints were added to 248 problems. In the case of 58 of these problems the algorithm was able to add also add unit clauses to the prolem.

As the comparison with the other algorithms indicates, SAT based constant extraction is an effective algorithm. Nevertheless, it still works only for certain instance domains, and is slower than the other algorithms. Furthermore, the

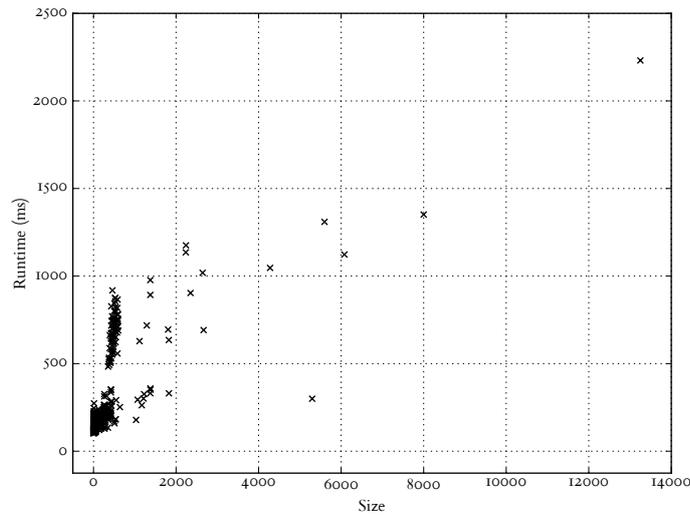


Figure 5.2: Runtime of SAT based constant extraction.

addition of transitivity constraints seems to be a valuable addition.

#### PATTERN BLOCKED CLAUSE ELIMINATION

In the case of Pattern Blocked Clause Elimination, we marked problems which were not equational, problems where two non-pattern literals with flex head appeared, and problems with one non-pattern literal with flex head. In the former two cases, pattern blocked clause elimination can not be applied. Furthermore, we also saved the number of clauses removed by this procedure.

Overall, 261 problems were not equational. Of those 261 problems, 124 contained two clauses with flex headed non-patterns. The remaining 137 problems were candidates for blocked clause elimination.

One problem resulted in an extremely long runtime of 47.5 seconds. This problem was one of the unsatisfiable QBF problems<sup>21</sup>. The negation of this problem resulted in long clauses, which in turn inflated the runtime of the blocked clause elimination algorithm. Because of this outlier the average runtime was 201.7 ms and the median runtime was 25 ms with a minimal runtime of 18.84 ms. This includes equational problems. After removing the equational problems, the median runtime is 31.1 ms. Figure 5.3 shows the runtime of blocked clause elimination and omits the problem with over 40 seconds

<sup>21</sup> the lut4\_XOR\_fOR-shuffled problem.

## 5 Implementation

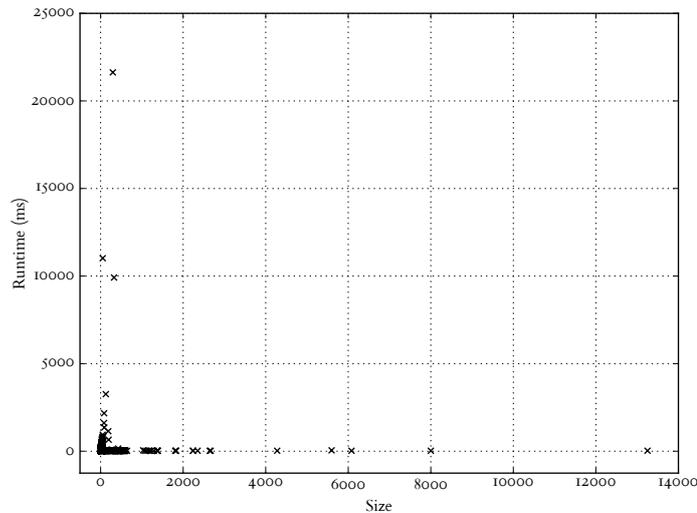


Figure 5.3: Runtime of pattern blocked clause elimination.

runtime.

The algorithm was able to remove clauses from 37 input problems. For ten of those problems, only one clause could be removed. For the problem SY0377<sup>522</sup> the algorithm could remove 42 clauses.

Blocked clause elimination is seriously constrained by the tight requirements on the input problems. In those cases where the algorithm is applicable, however, it was able to remove at least one clause in 27% of the cases.

### FIRST-ORDER RE-ENCODING

Since first-order re-encoding is done during one iteration over the problem, the only meaningful metric is the algorithm runtime. The runtime is expected to be linear in the total number of literals appearing in the input problem.

Figure 5.4 shows a scatter plot of the runtime. The linear behavior is visible. Overall, the runtime varied between 4.88 ms and 389.68 ms, with a median runtime of 9.78 ms. The mean runtime was 16.23 ms.

As we have already seen, applying first-order re-encoding was detrimental for the solvability of many problems. Nevertheless, table 5.2 indicates that seven problems were solved when first-order re-encoding was applied and not

<sup>22</sup> This problem from the syntactic domain is available at <http://www.cs.miami.edu/~tptp/cgi-bin/SeeTPTP?Category=Problems&Domain=SY0&File=SY0377^5.p>

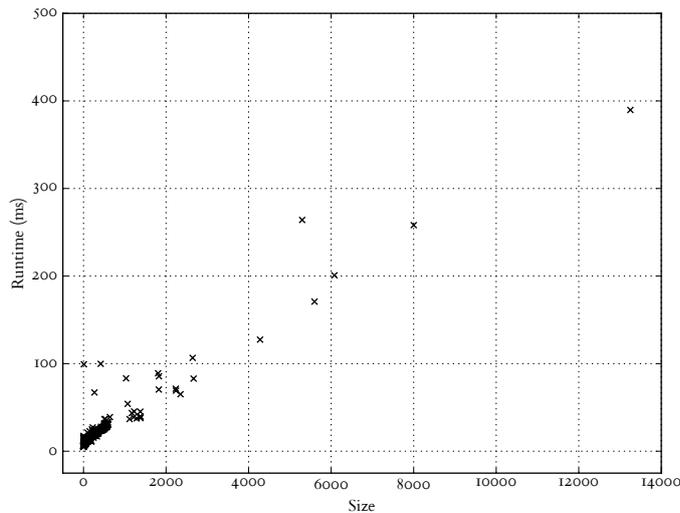


Figure 5.4: Runtime of first-order re-encoding.

by the pure Leo-III system. Of those seven problems, three were not solved by any other configuration. Those three problems were LCL727<sup>5</sup>, AGT031<sup>1</sup>, and SY0213<sup>5</sup>. Since these problems are from different domains and do not have any obvious similarities, we suspect that this coincidentally and not the result of applying first-order re-encoding.

## CONCLUSION

As we have seen, only SAT based constant extraction is clearly beneficial to the proof search. Pattern blocked clause elimination, in its current iteration, and universal reduction are only useful in some cases. There is no empirical evidence that first-order re-encoding is useful at all. Fortunately, all four algorithms have a useful runtime behavior.

Leo-III is a resolution based prover. This means, that it tries to find a proof by creating new clauses. Blocked clause elimination and universal reduction are not necessary helpful during this process, since they might remove elements which are useful during this search process. Constant extraction, on the other hand, aids this process by adding new, short clauses.



## 6 Insights

This chapter concludes this thesis. In the previous chapters we introduced HOL and QBF, discussed preprocessing techniques for QBF, described the adaption to HOL of some of those preprocessing techniques, and the practical implementation of them. We now set this work into a wider context. To do so, we present some related work in section 6.1 and suggest some further work in section 6.2. Section 6.3 concludes the thesis with some final remarks.

### 6.1 Related Work

Many of the preprocessing techniques for QBF solving were originally developed for SAT solving. A tool for SAT preprocessing has been SATELITE (Eén and Biere 2005). Among other techniques, it implemented variable elimination by self-subsuming resolution. Since its publication, it has been subsumed by recent releases of the SAT solver MINISAT2<sup>1</sup>. Another technique used for preprocessing is hyper binary resolution (Bacchus and Winter 2004). Blocked clause elimination was first introduced for SAT by Kullmann (1999) (cf. Jarvisalo, Biere, and Heule 2010).

Preprocessing has received some attention by state of the art theorem provers for first-order logic. We already discussed the recent application of blocked clause elimination to FOL. Earlier work in this area was done by Hoder et al. (2012). This work was motivated by industrial applications of first-order reasoning. The authors investigate two kinds of techniques. On one hand, they present methods for eliminating and simplifying definitions that also result in a clausification process which preserves the EPR fragment. On the other hand, they present a generalization of the And-Inverter Graph (AIG) datastructure, which they call Quantified And-Invert Graph (QAIG). The authors then use this datastructure to adapt various preprocessing techniques for SAT to FOL.

As we discussed, QBF and DQBF reasoners are *satisfiability checkers*. Another class of problems in this domain is Satisfiability Modulo Theories (SMT). As the name suggest, SMT languages enrich propositional satisfiability checking

<sup>1</sup> See the SATELITE homepage: <http://minisat.se/SatELite.html>

with theories such as uninterpreted functions and equality. Systems for solving SMT problems are commonly built from two parts. A SAT solver reasons on a propositional abstraction of the formulas generated by the theory reasoner. If the SAT solver finds a model for a problem, this model is analyzed by the theory reasoner. If the theory reasoner can create a model for the original problem, then the process terminates, otherwise the SAT problem is refined (cf. Barrett, Sebastiani, et al. 2009).

Examples for SMT solvers are Boolector (Niemetz, Preiner, and Biere 2014), CVC4 (Barrett, Conway, et al. 2011), VeriT (Bouton et al. 2009), and Z3 (Moura and Bjørner 2008). Preprocessing is a part of SMT solving and while the solving systems incorporate preprocessing routines, the SMTpp (Bonichon et al. 2015) tool is a standalone preprocessor. Recently, Barbosa, Blanchette, and Fontaine (2017) presented a method to generate proofs for the results of SMT formula processing.

While our focus was on theorem proving for HOL, other tasks which can be automatized exist. One such task is *model finding*. Model finders try to solve the problem: Given a HOL formula, is this formula satisfiable? These tools are especially useful if used as *count model finder*. If we have a formula of unknown status, we can apply a theorem prover and also negate the formula and apply a model finder. If the theorem prover succeeds, we know that the formula is a theorem. If the model finder succeeds, we know that the problem is counter-satisfiable. An example of a model finder is Nitpick (Blanchette and Nipkow 2010).

A form of preprocessing is also part of the *Sledgehammer* tool by (Paulson and Blanchette 2010). As discussed in subsection 2.3, Sledgehammer calls automated provers to assist the proof construction in an interactive prover. To make this process feasible, Sledgehammer must select a small subset of theorems from the big library of theorems available in interactive provers. The selected theorems are then added to the problem for the automated prover as axioms.

## 6.2 Further Work

In this thesis we only investigated a limited number of QBF and DQBF preprocessing algorithms. The remaining algorithms are an obvious topic to continue this work. It might be especially fruitful to investigate dependency schemata. Wimmer, Gitina, et al. (2015) describe a simple dependency schema for DQBF. The goal of dependency schemata is to determine that some universally quantified variable  $x$  in the dependency set of an existentially quantified variable  $y$

can be omitted because it will not be used by the Skolem function of  $y$ . In such a situation the variable  $x$  can be removed from the dependency set of  $y$ . The authors give a simple sufficient criterion for the independence of two variables and note that more complex schemata have been developed for QBF.

On the more practical side of things, implementing our preprocessing techniques as a stand-alone tool would enable us to evaluate the effect of the techniques on other theorem provers and automated tools such as model finders. This is especially interesting for BCE, since Kiesl et al. (2017) report a positive impact of BCE on solving first-order non-theorem problems. To show that a first-order formula is not a theorem, a search for a counter-model is required. Such a tool would take a HOL formula in TPTP format as input and output a new, satisfiability equivalent, formula in the same format. It should also be able to create prove traces of transformations it applies.

Another further topic is to complete the work on BCE for HOL:

#### BLOCKED CLAUSE ELIMINATION

Unfortunately, our discussion of BCE for HOL was very restricted. While we presented a first implementation of BCE for the automated theorem prover Leo-III and evaluated its practical feasibility.

The most apparent missing piece is certainly the lack of a proof of soundness of the BCE procedure. The proof for the redundancy of FOL blocked clauses given by Kiesl et al. (2017) relies on the Herbrands theorem. This theorem states, that an equality free FOL formula is satisfiable if and only if every finite set of ground instances is satisfiable (cf. Fitting 1996). Then in a set of ground instances of a formula  $F$  with a clause  $C$  blocked by the literal  $l$ , the truth value of  $l$  in an assignment which falsifies  $C$  can simply be flipped to satisfy  $C$ . This new assignment will still satisfy all the clauses in  $F \setminus \{C\}$  which are satisfied by the old assignment. A slightly more intricate, but similar argument is used for the case of equality blocked clauses.

A version of the Herbrand theorem for HOL exists. It was given by Miller (1987) and relies on the notion of expansion-tree proofs. We believe that this can be used to prove the soundness of our variant of blocked clause elimination.

Secondly, the lack of support for equality in our variant of BCE is a very undesirable property. Again we hypothesize, that the approach by Kiesl et al. (2017) can be extended to HOL. Handling flex and rigid literals would become more complex, and so would deciding validity of flat  $l$ -resolvents. However, the practical implementation of BCE for FOL with equality already abandoned full congruence closure in favor of a greatly simplified algorithm. Hence, an incomplete algorithm for deciding the validity might be enough. Further-

more, the notion of flattening tightly corresponds to unification constraints generated during pre-unification (cf. Huet 1975).

Overall, a fine analysis of the concept of blocking literals and clauses in the context of HOL and especially resolution based provers for HOL would be a fruitful undertaking.

#### BIT PRECISE REASONING WITH HOL

In hard- and software verification, reasoning on the level of bits is often relevant for the verification process. In practice QBF and DQBF formulas can be used to do this. Another formalism used in this domain is *bit vector logic*.

Those logics provide operators on vectors of bits, such as element wise disjunction and shift operators. Variables range over bit vectors of fixed width. The solving complexity depends on the chosen language features (cf. Biere 2014).

As we have seen, HOL can also be used to describe Boolean problems. Common bit vector logics can probably be expressed in HOL by choosing functions on Booleans with the appropriate arity. This would enable the users to formulate some properties in the general language of HOL. Unfortunately, our investigation into solving QBF and DQBF problems with HOL theorem provers has shown, that they do not perform very well on such problems.

A fruitful way of approaching the problem of bit precise reasoning in HOL could be to combine bit vector and QBF reasoners with HOL theorem provers. One major challenge would be the extraction of Boolean problems from the overall clause set.

### 6.3 Conclusion

This thesis started with an introduction into two distinct logical languages. First we described Higher-order logic, a logic which allows quantification over functions and properties. Then we presented quantified Boolean formulas, a generalization of propositional logic, which allows quantification over Boolean values. Those two logics are quite distinct. Higher-order logic is undecidable, while quantified Boolean formulas are not. On the other hand, we showed that QBFs are a fragment of HOL. We then focused on preprocessing techniques for QBF and presented adaptations of those techniques to the task of proving HOL formulas.

Overall we introduced four techniques: Universal reduction, first-order re-encoding, SAT based constant extraction, and pattern blocked clause elimina-

tion without equality. Not only were they discussed theoretically, but they were also implemented for the Leo-III system. We also evaluated the implementations empirically. Finally, we discussed related work and suggested various ways to continue working on this topic.

Our work connected two worlds: The world of HOL and the world of QBF. While QBF is fragment of HOL on the theoretical level, the world of QBF turns out to be much different. Through the use of translation tools, we were able to show, that QBF and DQBF problems are very hard, even intractable, for state of the art HOL provers. Preprocessing methods for QBF, on the other hand, struggle with the expressiveness of HOL.

While our benchmarks show a promising impact of our preprocessing techniques, we believe, that this comparison is the main contribution of the diploma thesis at hand.



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